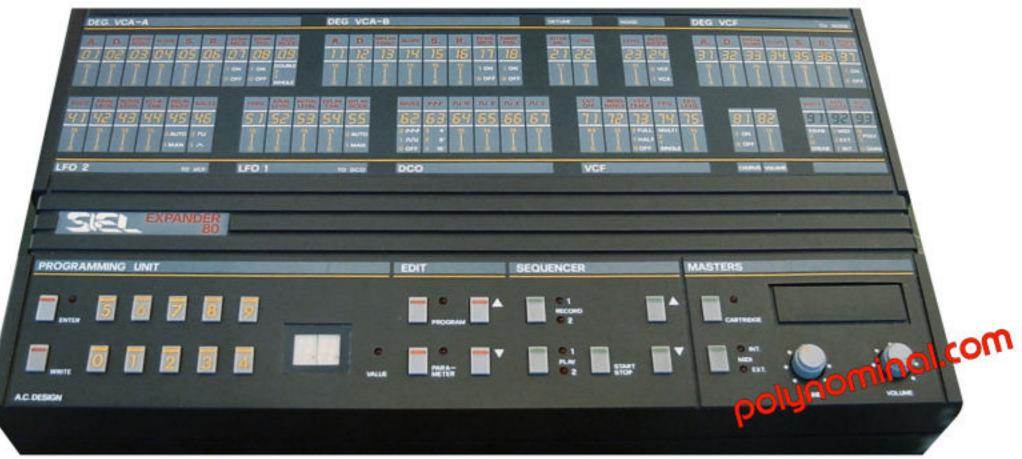
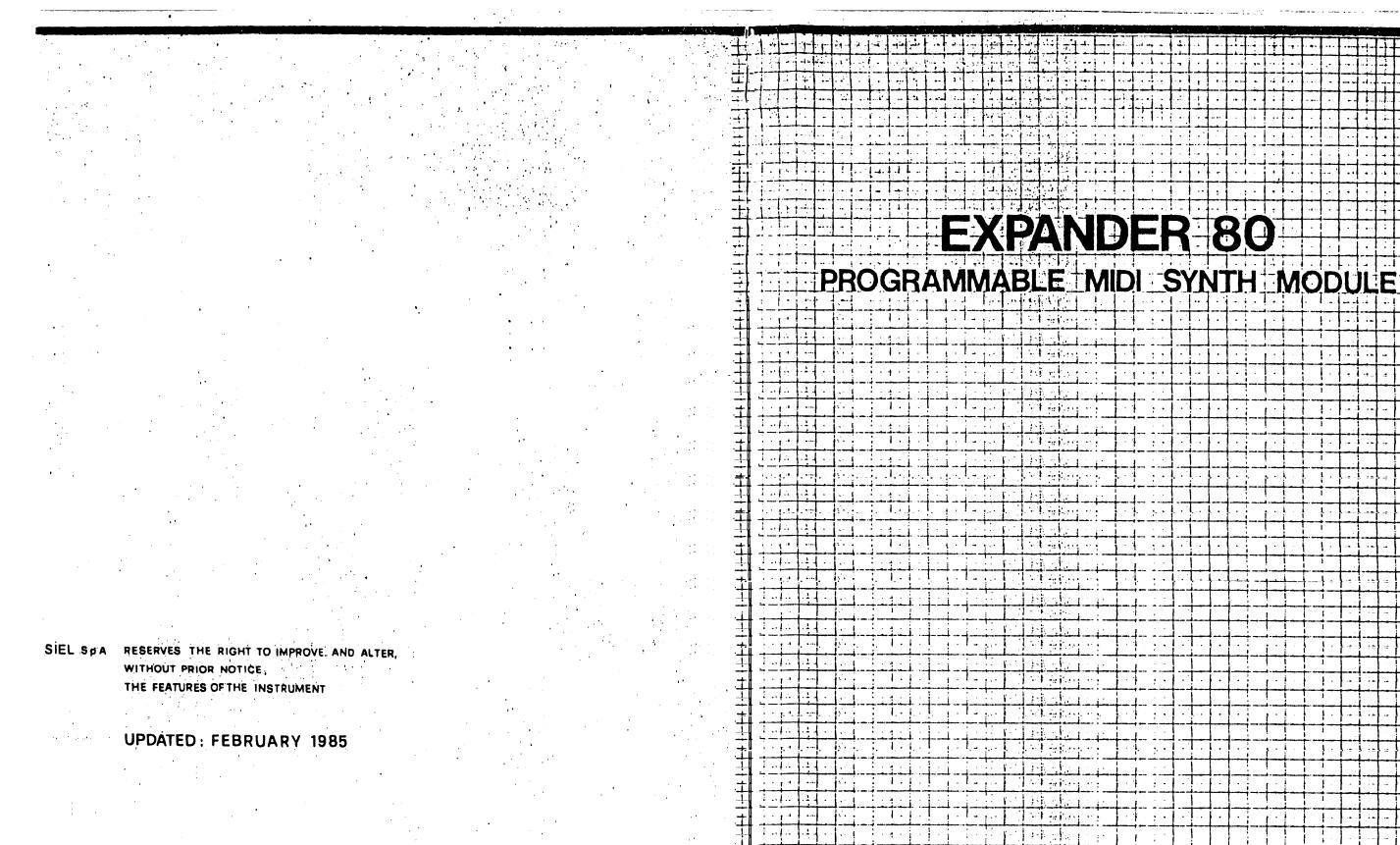
# JIEL JERVICE MANUAL JCHEMATICJ





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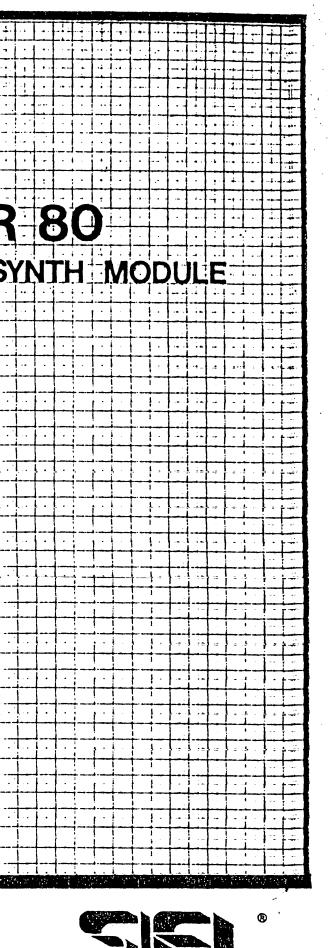
SCHEMATIC DIAGRAM

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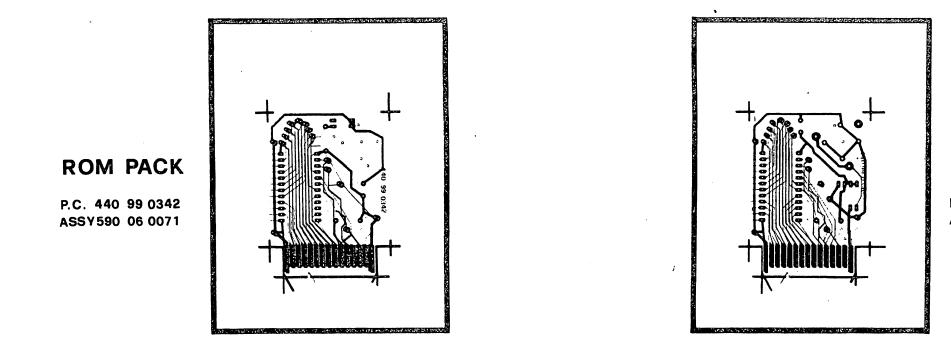
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## DISPLAY & SWITCHES BOARD

P.C. 440 99 0339 ASSY 590 05 0074

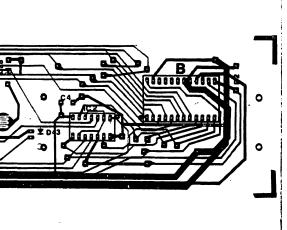
## CONTROLS

#### BOARD

P.C. 440 99 0338 ASSY 590 05 0076

## RAM PACK

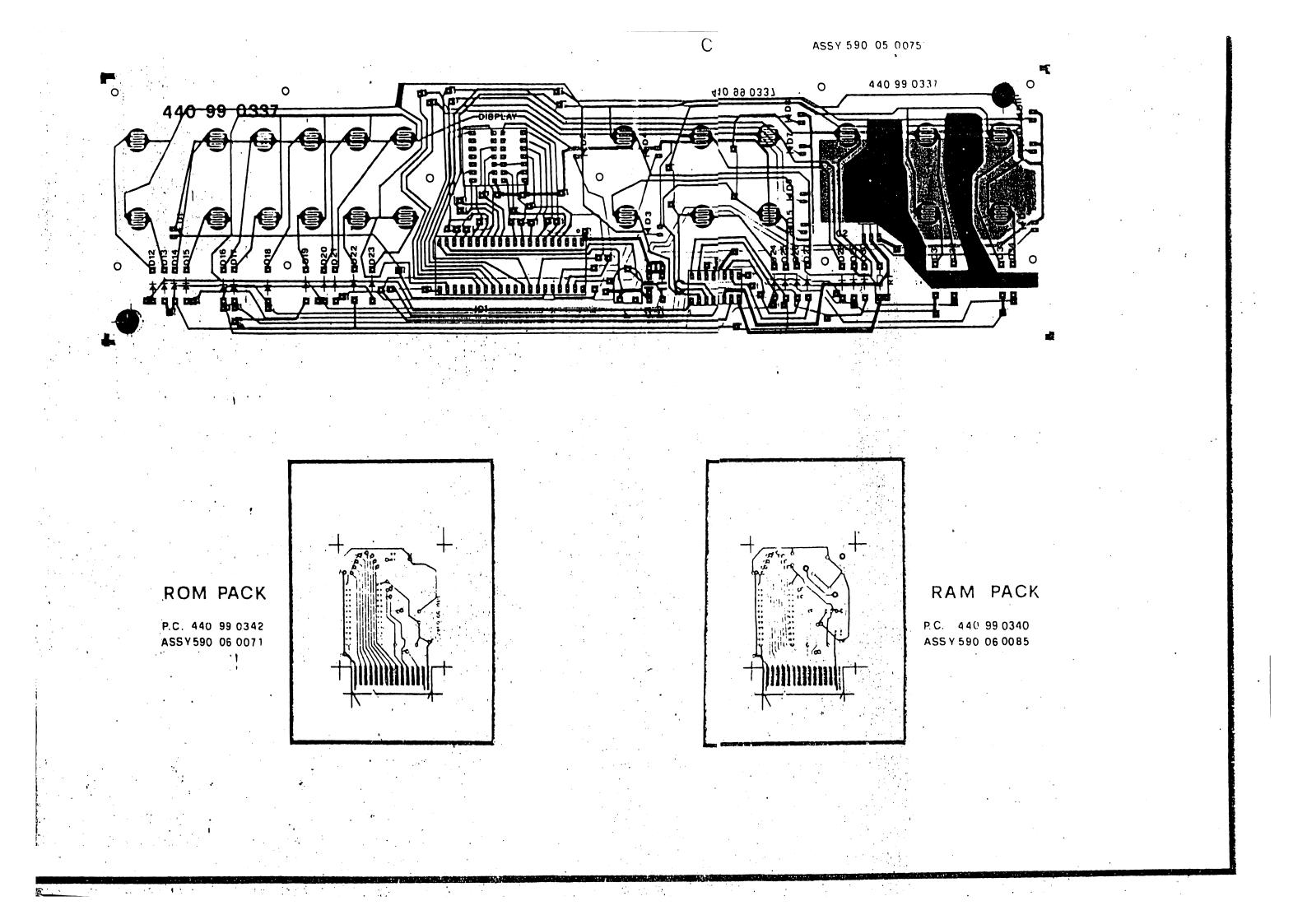
P.C. 440 99 0340 ASS Y 590 06 0085

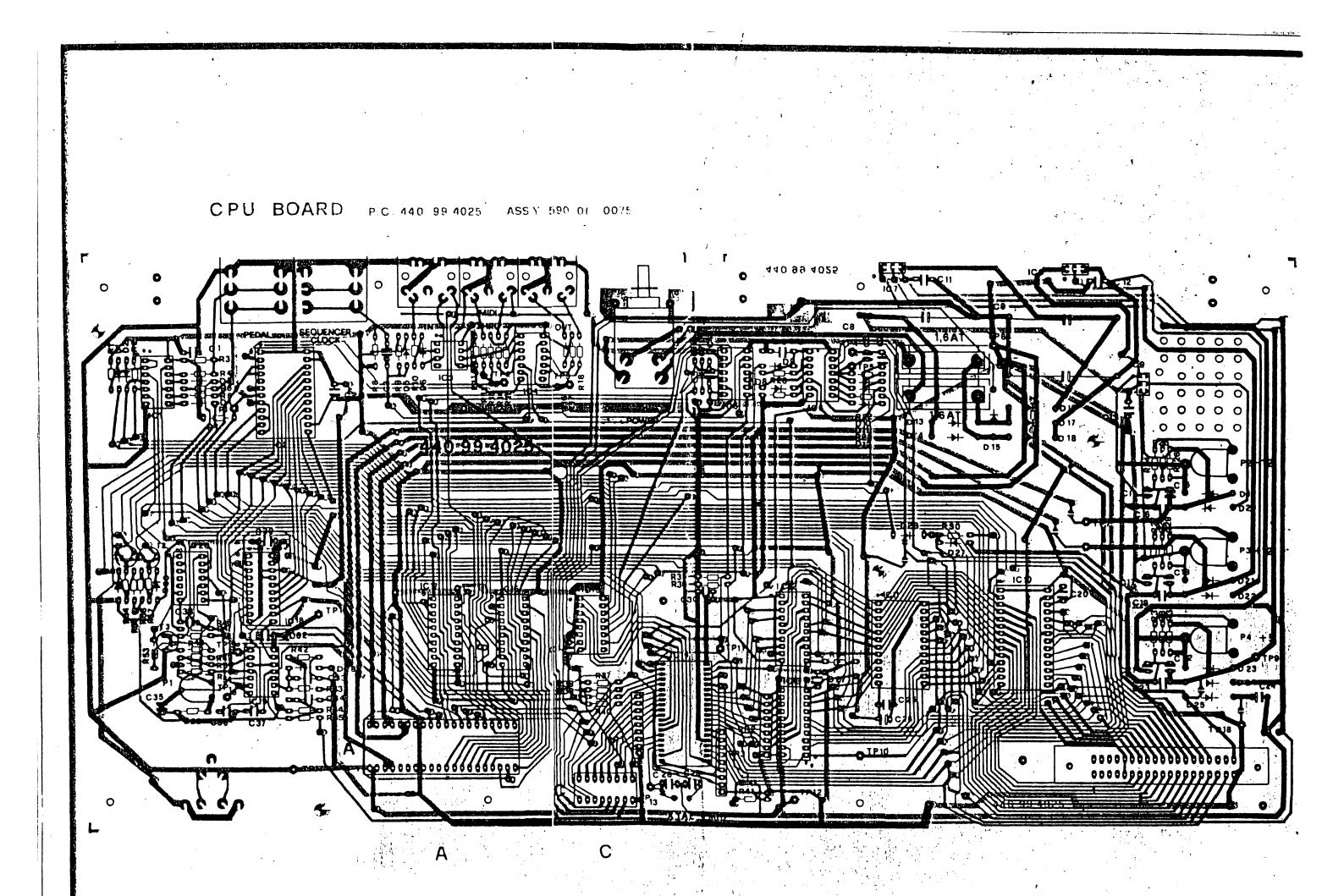


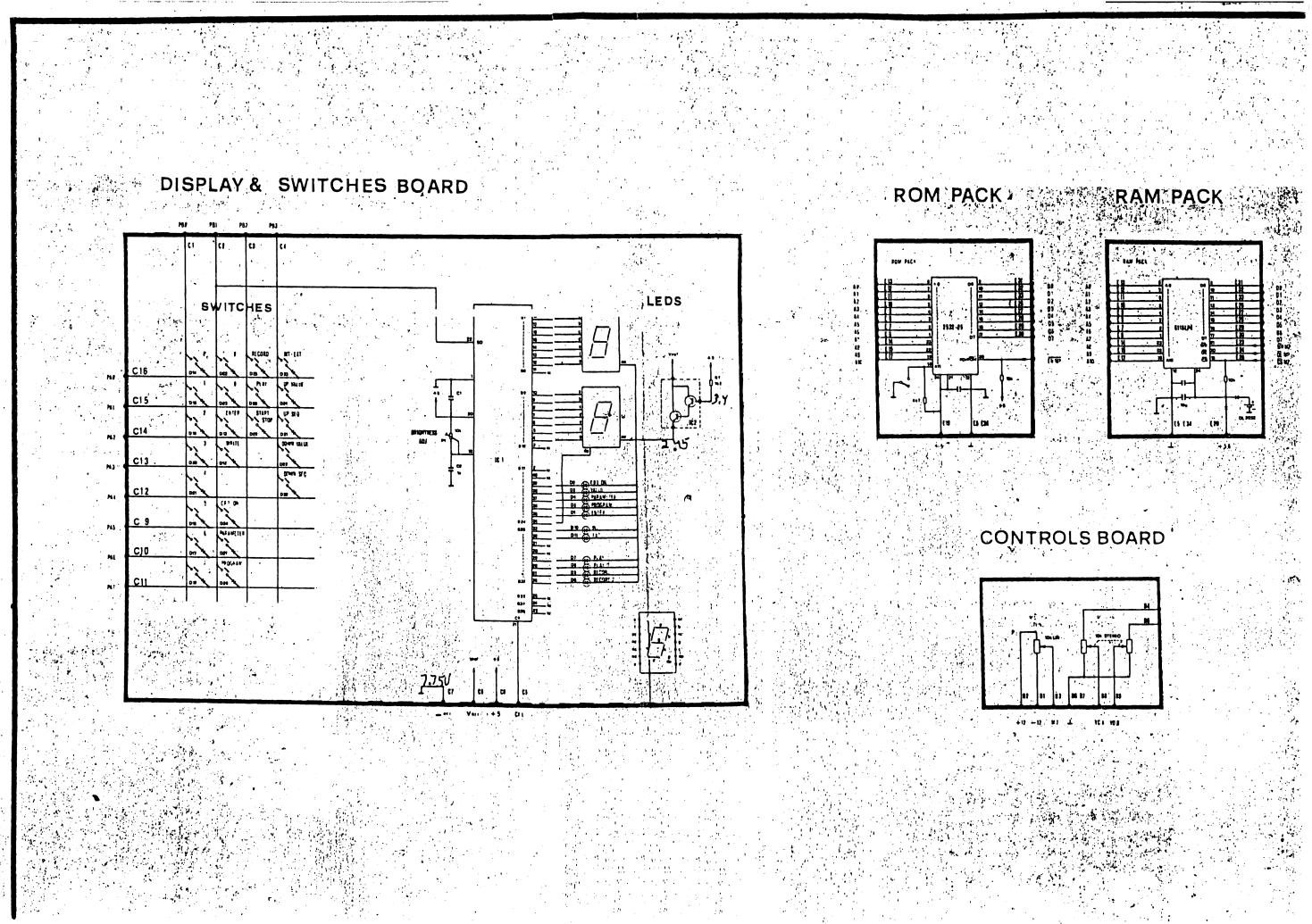
B



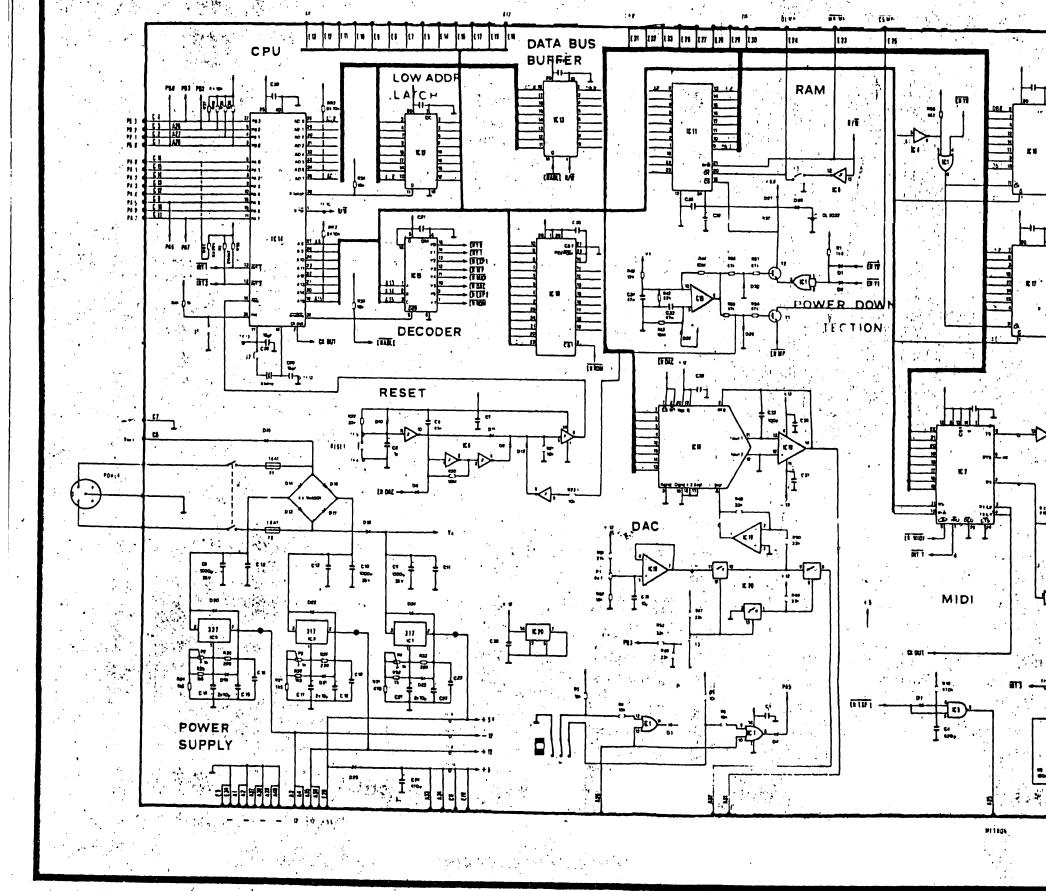
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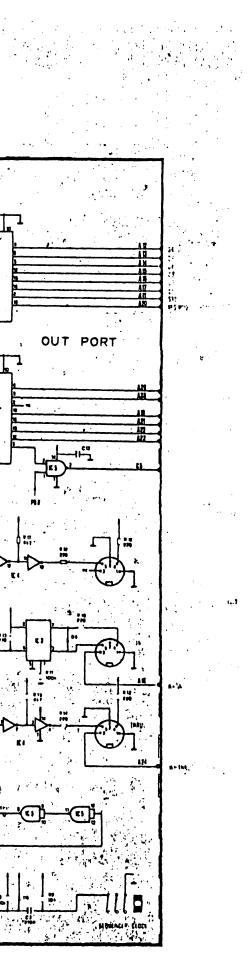


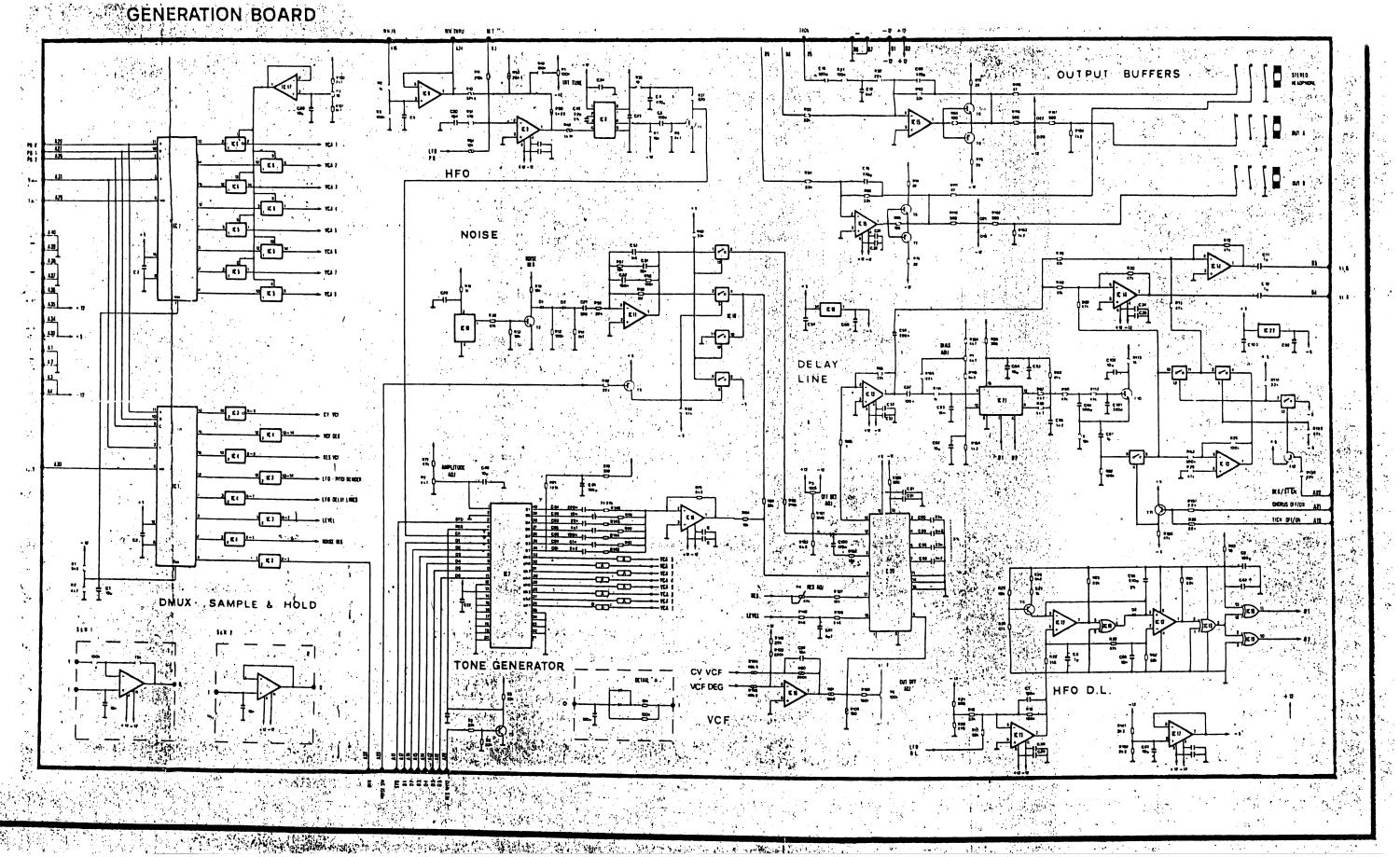
# CPU BOARD



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		S	
A State		P.C. 440.99.4025 CPU BOARD I.C.1 - 74HC32 I.C.2 - 66BE50 I.C.3 - 6N139 I.C.4 - 74LS04 I.C.5 - 74HC00	F.C. 440 GENERATI I.C.1 I.C.2 I.C.3 I.C.4 I.C.5
A C	All -	I.C. $6 - 74HC14$ I.C. $7 - LM 317 T$ I.C. $8 - LM 317 T$ I.C. $9 - LM 337$ I.C. $10 - 2564 - 35$ I.C. $11 - 6116 LP4$ I.C. $12 - 74HC373$ I.C. $13 - 74HC245$ I.C. $14 - TMS 7000$ I.C. $15 - 74HC138$	I.C.6 I.C.9 I.C.9 I.C.10- I.C.11- I.C.12- I.C.13- I.C.13- I.C.14- I.C.15-
	A A A A A A A A A A A A A A A A A A A	I.C.16- 74HC377 I.C.17- 74HC377 I.C.18- DAC 0832 I.C.19- TL 084 I.C.20- 4016	I,C,16- I,C,17- I,C,19- I,C,20- I,C,21- I,C,22-
		P.C. 440.99.0340	F.C. 440 DIGPLAY & SW I.C.1 - I.C.2 - Display F.C. 44
1. GENERATION BOARD		RAM PACK 6116 LP4 NOTES: - All condensers without value (ceramic condensers, - All resistors are 1/4 W,	RDM 253 specifica
2. CPU BOARD 3. DISPLAY & SWITCHES BOARD 4. CONTROLS BOARD	440 99 4026 440 99 4025 440 99 0337 440 99 0358	- All resistors whose symbol is mar are 1X precision resistors. - Upward arrows on CFU BDARD indica - Upward arrows on GENERATION BOARD UNLESS DTHERWISE INDICATED	ate a +5 vo

'eng '

12.1 Sec. 2 1 1 N. . 1. 1 75 . . . . 0.1 · . . F. .. 1 24 40.99.4026 10 TION BOARD - 4051 - 4051 - TL 084 - TL 084 - TL 084 de -- TL 084 - M 112 B1 - 66M 2031 . . 1 - TL 082 115 0- MM 5437 1- TL 082 1.1.1.1.1 2- LM 393 3- TL 082 4- TL 082 5- TL 082 6- TL 082 7- TL 082 1.4 8- 4016 9- 4070 0- 55M 2045 . 1- TDA 1022 2- 4016 40,99,0337 SWITCHES BOARD - M5451 - BDX 33 ay man 4810 A 440-99.0342 M PACK 1.17 532-25 À ation are 47 n n oblique stroke oltage, a +12 voltage. r. • •1 . . . A ...