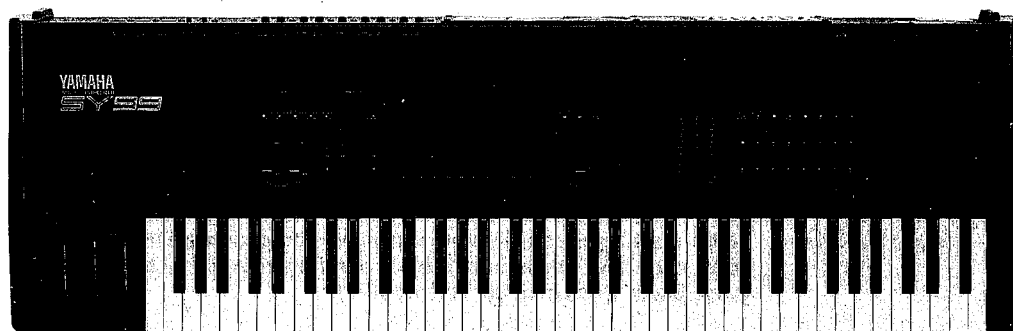


MUSIC SYNTHESIZER

SY99

SERVICE MANUAL



669
SY99

■ CONTENTS (目次)

SPECIFICATIONS (総合仕様).....	2
PANEL LAYOUT (パネルレイアウト).....	4
CIRCUIT BOARD LAYOUT (ユニットレイアウト).....	6
WIRING (結線図).....	8
BLOCK DIAGRAM (ブロックダイアグラム).....	12
DISASSEMBLY PROCEDURE (分解手順).....	14
LSI PIN DESCRIPTION (LSI端子機能表).....	21
IC BLOCK DIAGRAM (ICブロック図).....	29
CIRCUIT BOARDS (シート基板図).....	32
TEST PROGRAM (テストプログラム).....	54/79
ERROR MESSAGES (エラーメッセージ).....	102/105
MIDI DATA FORMAT.....	110
MIDI IMPLEMENTATION CHART.....	132
PARTS LIST	

IMPORTANT NOTICE

This manual has been provided for the use of authorized Yamaha Retailers and their service personnel. It has been assumed that basic service procedures inherent to the industry, and more specifically Yamaha Products, are already known and understood by the users, and have therefore not been restated.

WARNING: Failure to follow appropriate service and safety procedures when servicing this product may result in personal injury, destruction of expensive components and failure of the product to perform as specified. For these reasons, we advise all Yamaha product owners that all service required should be performed by an authorized Yamaha Retailer or the appointed service representative.

IMPORTANT: The presentation or sale of this manual to any individual or firm does not constitute authorization, certification, recognition of any applicable technical capabilities, or establish a principle-agent relationship of any form.

The data provided is believed to be accurate and applicable to the unit(s) indicated on the cover. The research, engineering, and service departments of Yamaha are continually striving to improve Yamaha products. Modifications are, therefore, inevitable and changes in specification are subject to change without notice or obligation to retrofit. Should any discrepancy appear to exist, please contact the distributor's Service Division.

WARNING: Static discharges can destroy expensive components. Discharge any static electricity your body may have accumulated by grounding yourself to the ground buss in the unit (heavy gauge black wires connect to this buss).

IMPORTANT: Turn the unit OFF during disassembly and parts replacement. Recheck all work before you apply power to the unit.

This product uses a lithium battery for memory back-up.

WARNING: Lithium batteries are dangerous because they can be exploded by improper handling. Observe the following precautions when handling or replacing lithium batteries.

- Leave lithium battery replacement to qualified service personnel.
- Always replace with batteries of the same type.
- When installing on the PC board, solder using the connection terminals provided on the battery cells. Never solder directly to the cells. Perform the soldering as quickly as possible.
- Never reverse the battery polarities when installing.
- Do not short the batteries.
- Do not attempt to recharge these batteries.
- Do not disassemble the batteries.
- Never heat batteries or throw them into fire.

ADVARSEL!

Lithiumbatteri. Eksplosionsfare.

Udskiftning må kun foretages af en sagkyndig, og som beskrevet i servicemanualen.

WARNING: CHEMICAL CONTENT NOTICE!

The solder used in the production of this product contains LEAD. In addition, other electrical/electronic and/or plastic (where applicable) components may also contain traces of chemicals found by the California Health and Welfare Agency (and possibly other entities) to cause cancer and/or birth defects or other reproductive harm.

DO NOT PLACE SOLDER, ELECTRICAL/ELECTRONIC OR PLASTIC COMPONENTS IN YOUR MOUTH FOR ANY REASON WHAT SO EVER!

Avoid prolonged, unprotected contact between solder and your skin! When soldering, do not inhale solder fumes or expose eyes to solder/flux vapor!

If you come in contact with solder or components located inside the enclosure of this product, wash your hands before handling food.

■ SPECIFICATIONS (総合仕様)

Tone generator: Realtime Convolution and Modulation (RCM)

AWM2: 16 bit linear waveform data, maximum 48 kHz sampling frequency

AFM: 6 operators, 45 algorithms, 3 feedback loops, 16 waveforms, modulation from AWM output

Filter: Time variant IIR (infinite impulse response) digital filters, 2 filters for each element (maximum of 8 filters per voice)

Maximum simultaneous notes: 16 (Voice mode), 32 (Multi mode)

Maximum simultaneous timbres: 1 (Voice mode), 16 (Multi mode)

Note assignment: Last note priority, DVA (dynamic voice allocation)

Keyboard: 76 notes, key velocity sensitivity, channel aftertouch (with zoned aftertouch)

DSP effects: 2 units, 63 effect types

Sequencer:

Tracks: 16 (15 tracks + 1 pattern track)

Songs: 10

Resolution: 1/96 of a quarter note (for internal clock), 1/24 of a quarter note (for MIDI sync)

Maximum simultaneous notes: 32

Capacity: approximately 27,000 notes

Patterns: 99

Recording: realtime/step/punch in

Memory:

Preset memory: 128 voices, 16 multis

Internal memory: 64 voices, 16 multis

Waveform memory: 4 Mwords (8 Mbytes), 267 sounds

MDR/sample memory: 512 kbytes (expandable to 1.5 Mwords)

Card slots: synthesizer data x 1, waveform data x 1

Disk: 3.5" floppy disk drive (720 kbyte formatted)

Controllers:

Wheels: PITCH, MODULATION 1, MODULATION 2

Slider: OUTPUT 1, OUTPUT 2, DATA ENTRY

Knobs: LCD contrast, click volume

Dial: data entry dial

Panel switches: MODE x 5, EDIT/COMPARE, COPY/SAVE, EF.BYPASS, SEQUENCER x 7, SHIFT, function x 8, EXIT, PAGE <>, JUMP/MARK, cursor $\Delta \nabla \leftarrow \rightarrow$, -1/NO, +1/YES, numeric keypad 0-9, ENTER, \square , MEMORY x 4, BANK x 4, voice select x 16,

Display:

LCD: 240 x 64 pixels (with backlight)

LED: red x 11, red/green x 21

Terminals:

Audio output: OUTPUT 1 (L/MONO, R), OUTPUT 2 (L, R), PHONES

Controller: BREATH, FOOT VOLUME, FOOT CONTROLLER, SUSTAIN, FOOT SWITCH
MIDI: IN, OUT, THRU

Power requirements:

UL, CSA: 120V

Europe, WG, Australia, BS: 220-240V

Power consumption:

UL, CSA: 35W

Europe, WG, Australia, BS: 35W

Dimensions:

1254(W) x 407(D) x 120(H) mm

Weight: 19.6 kg

Output level:

Headphones: +5.5dBm (150 Ω load)

Outputs: 0dBm (10k Ω load)

<機能>

音源:

音源形式:

- RCM音源
- AWM2: 16ビットリニア波形, サンプリング周波数 f 24/32/48kHz
1音(エレメント)につき1オクターブ12dBのデジタルフィルターを2個搭載
内蔵波形容量
- ROM部: 4メガワード(8MByte)
- RAM部: 256キロワード(512KByte) バッテリーバックアップ付拡張スロットに増設メモリーボードを差すことにより、最大1.5メガワード(3MByte)まで拡張可能
- AFM: 6オペレータ、45アルゴリズム、3系統フィードバック、16波形
AWM2の出力波形による変調が可能
1音(エレメント)につき1オクターブ12dBのデジタルフィルターを2個搭載

フィルター:

- 時変形デジタルフィルター×最大8/ボイス
- 各フィルターはLPF, HPFの切り替えができ、この組み合わせによりBPFやロールオフ24dBのLPFとしても使用可能
- レゾナンス可変で発振領域までカバー

最大同時発音数:

- AWM2: 16音+AFM: 16音

最大同時音色数:

- 16

発音形式:

- 後着優先、DVA

鍵盤:

- 76キー/イニシャル&チャンネルアフタータッチ付き

エフェクタータイプ:

- 高品位DSPを2基搭載(シリアルまたはパラレル接続)
- プログラム数63
- エフェクトパラメーターをリアルタイムにコントロール可(2系統)

シーケンサー:

トラック数:

- 16トラック(含むパターントラック1)

ソング:

- 10 (Next Song, Next Mode付)

分解能:

- 1/96 (内部クロック時)

最大同時発音数:

- 32

最大記憶音数:

- 約27000音

パターン数:

- 99

録音方式:

- リアルタイム/ステップ/パンチイン

<内部構成>

プリセットメモリー:

- ボイス: 128+マルチ: 16

インターナルメモリー:

- ボイス: 64+マルチ: 16

波形用メモリー:

- ROM: 4メガワード(8MByte)
PRESET 1=155, PRESET 2=112
- RAM: 512 KByte (RAM ボードの追加により最大3072 KByteまで拡張可)
MIDIサンプルダンプ、ディスク、ウェイブフォームカードより供給

カードスロット:

- 音色パラメータ用×1
MCD64: 1バンク
※1バンク: 64ボイス+16マルチ+1システム
- 波形用×1

3.5インチFDD:

- 1 (フォーマット時713KB)

<操作子>

Wheel:

- ピッチベンド、モジュレーション1、モジュレーション2
- スライダーボリューム:
- アウトプットボリューム1・2、データエントリー
- ロータリーボリューム:
- LCDコントロール、クリックボリューム
- ダイヤル:
- データエントリー
- パネルスイッチ:
- Mode(5)Voice, Multi, Song, Pattern, Utility
- Edit(2)Edit/Compare, Copy
- Effect bypass(1)
- Memory select(4)Preset 1, Preset 2, Internal, Card
- Bank select (4)A~D
- Program select(16)1~16
- Page(3)Page+, Page-, Jump/Mark
- テンキー(12)0~9, Enter, -
- Data Entry(2)Inc, Dec
- カーソル(4)←, →, ↑, ↓
- Function(10)function 1~8, Shift, Exit
- Sequencer(7)Run, Stop, Rec, Top, Rew, FF, Auto Locate

<ディスプレイ>

LCD:

- 240×64 Dots(バックライト付き)

LED:

- Red×11
- Red/Green×21

<接続端子>

音声出力:

- 4 Output1(L/Mix L/Mono, R/Mix R), Output2(L,R)

ヘッドフォン:

- 1

コントローラ:

- 5 Foot control, Foot volume, Foot switch, Sustain switch, Breath control

MIDI:

- 3 IN, OUT, THRU

<出力レベル>

ヘッドフォン:

- +5.5dBm (150Ω)

リア出力端子:

- 0dBm (10kΩ)

<電源(国内)>

電源電圧:

- 100V

消費電力:

- 25W

<寸法、重量>

寸法:

- 1254(W)×407(D)×120(H)(mm)

重量:

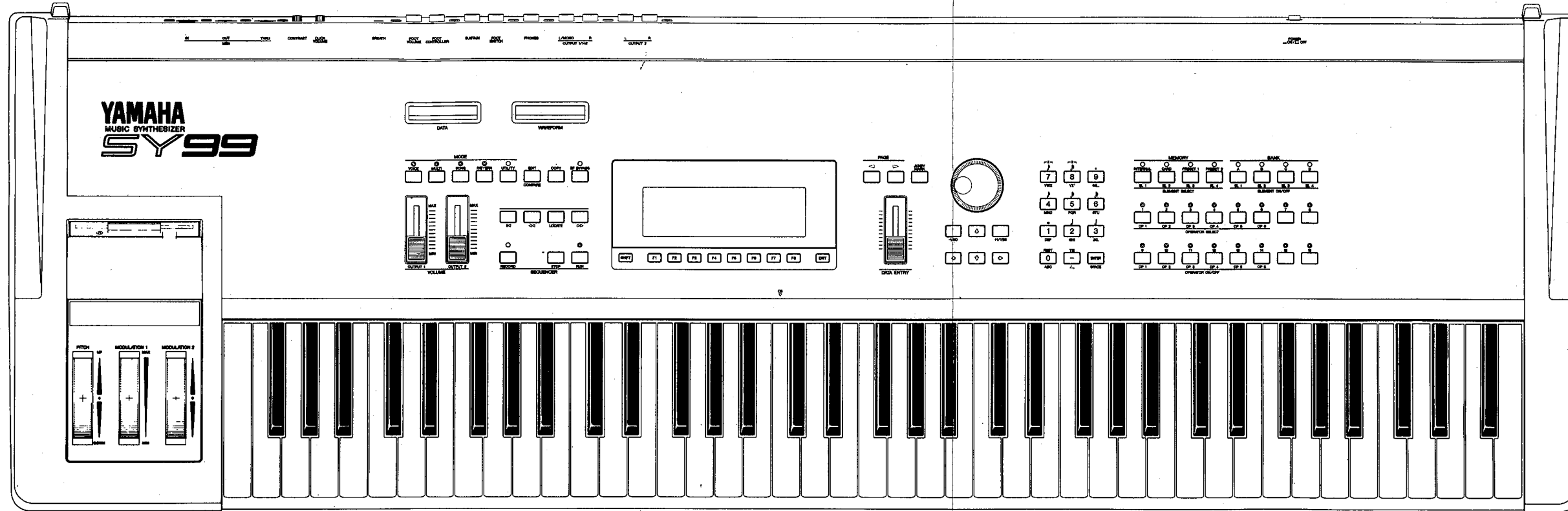
- 19.6kg

<付属品>

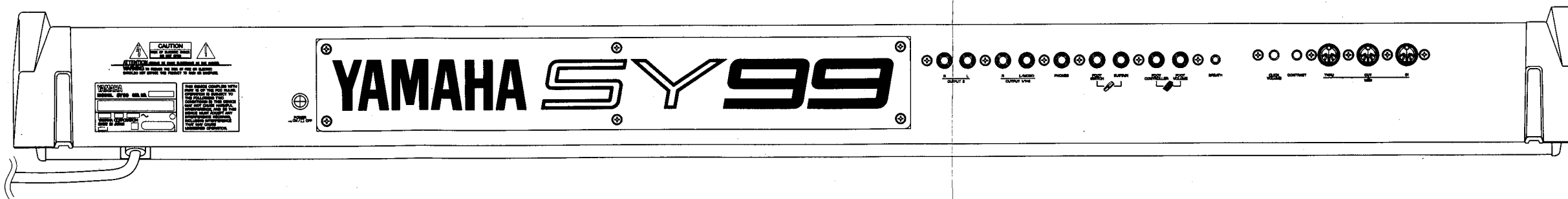
- デモディスク2枚(3.5インチフロッピーディスク)

■ PANEL LAYOUT (パネルレイアウト)

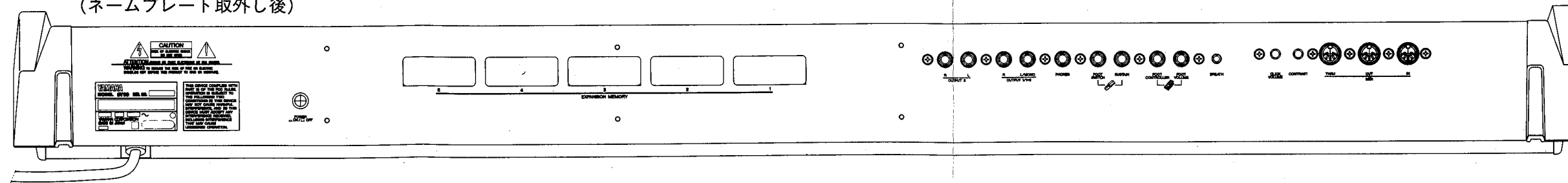
● Front Panel (フロントパネル)



● Rear Panel (リアパネル)

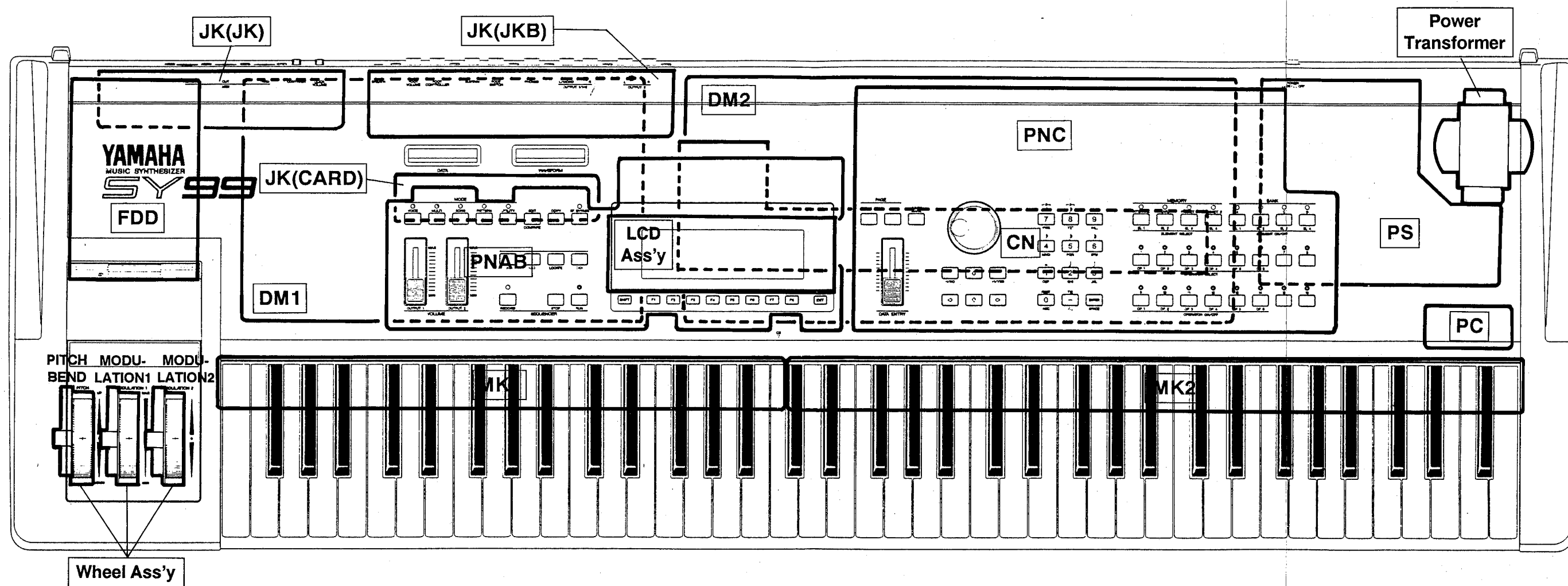


After the name plate has been removed ;
(ネームプレート取外し後)

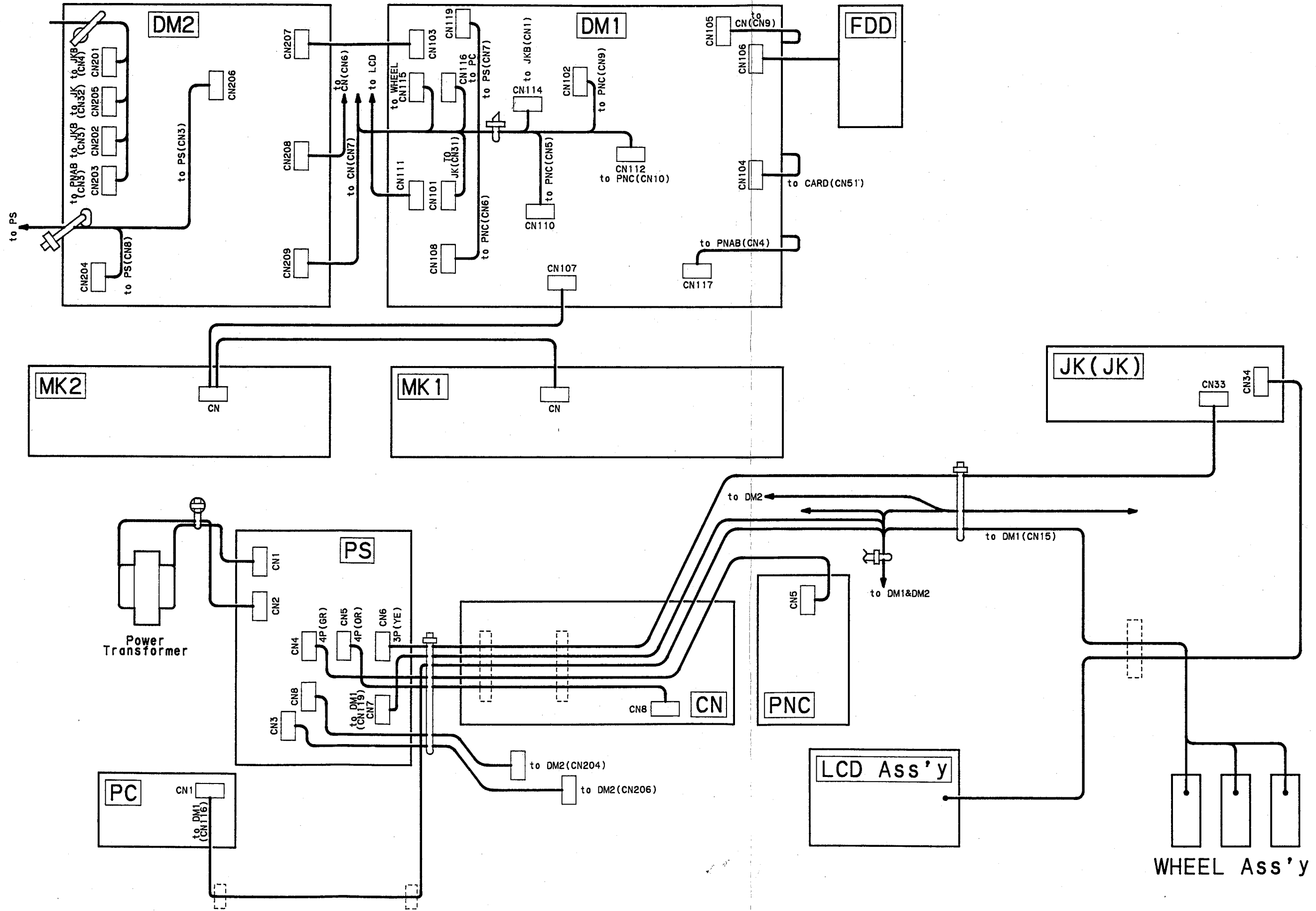


SY99

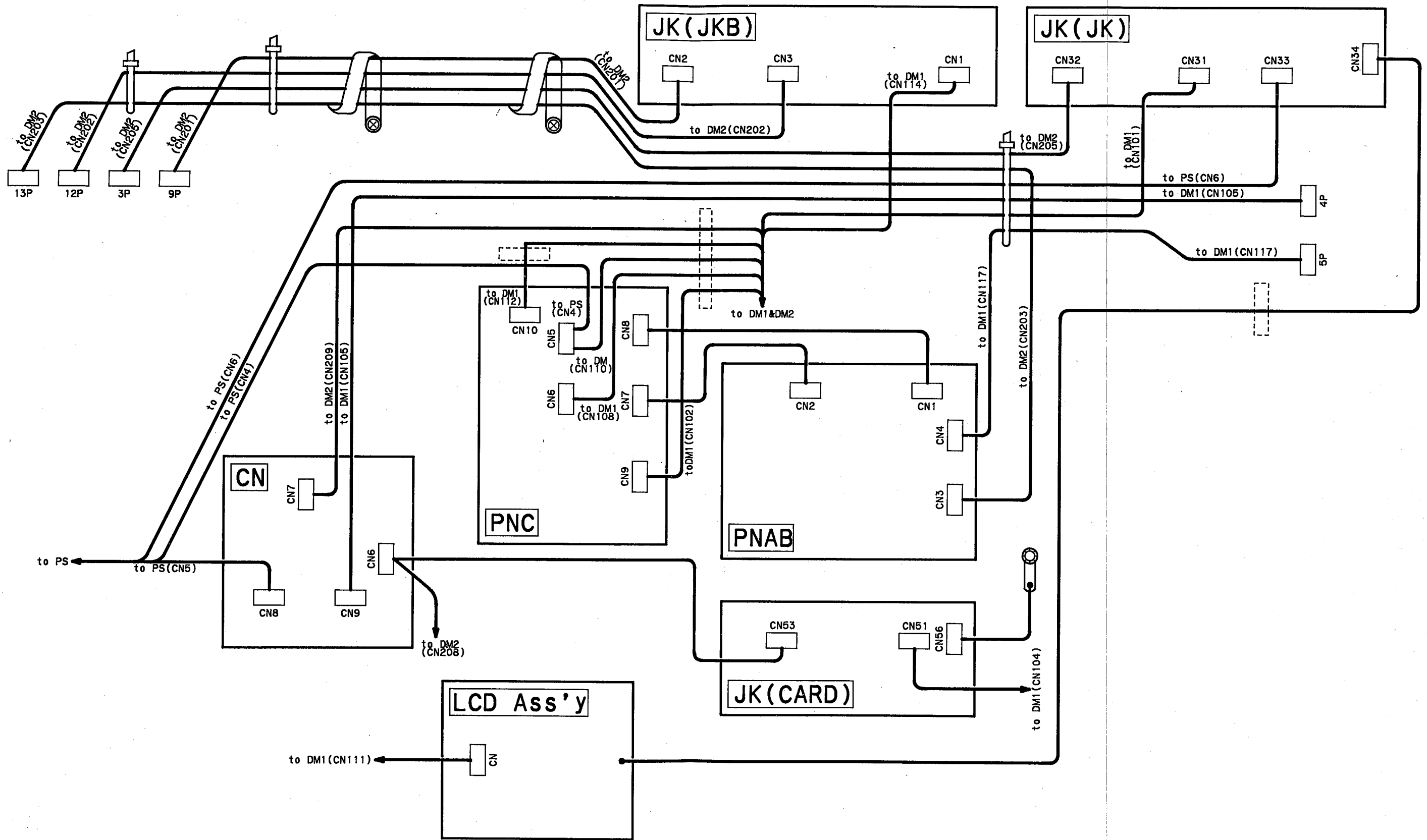
■CIRCUIT BOARD LAYOUT (ユニットレイアウト)



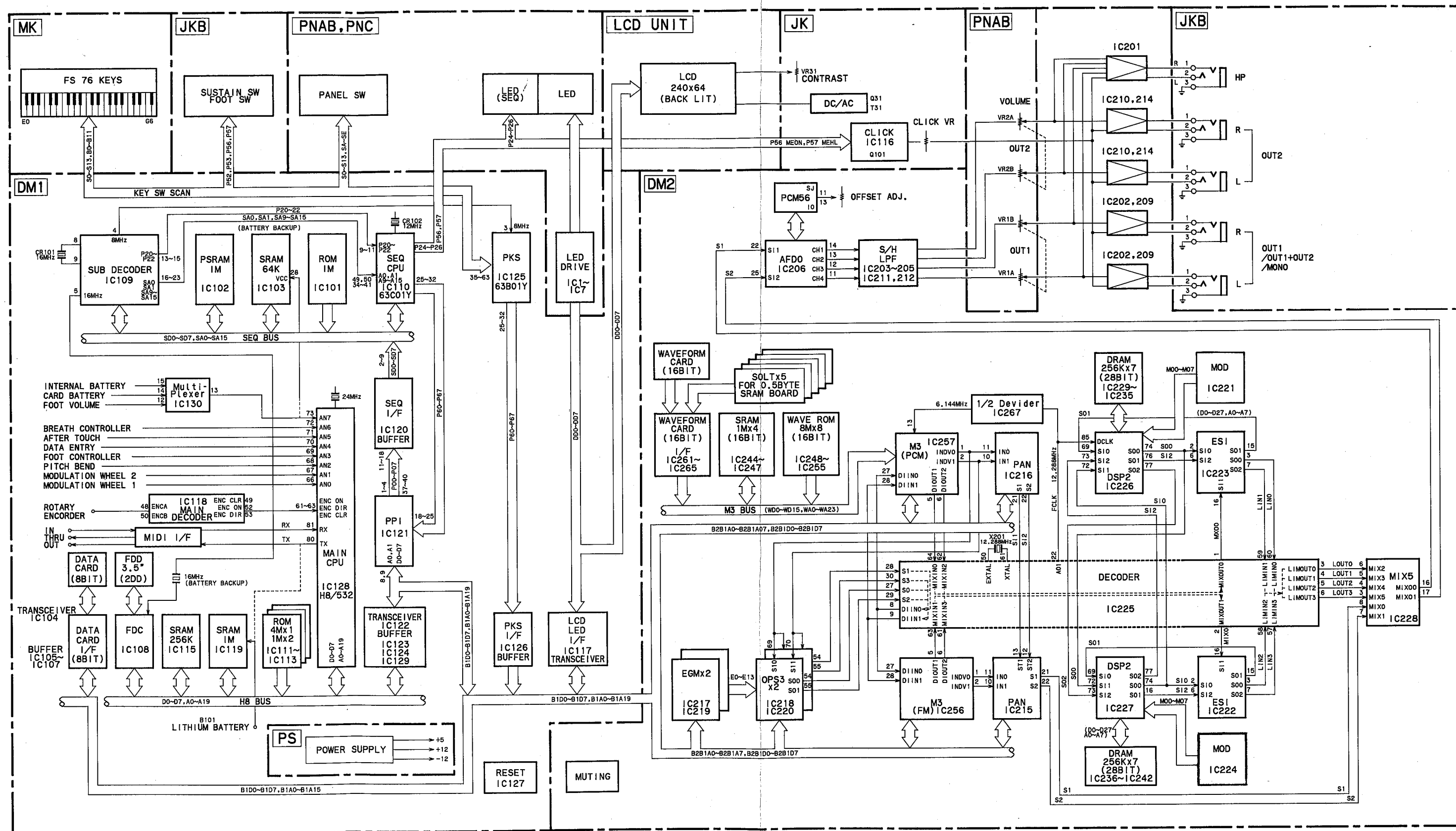
WIRING (結線図)



SY99



■BLOCK DIAGRAM (ブロックダイアグラム)



DISASSEMBLY PROCEDURE (分解手順)

1. Bottom Cover Assembly Removal

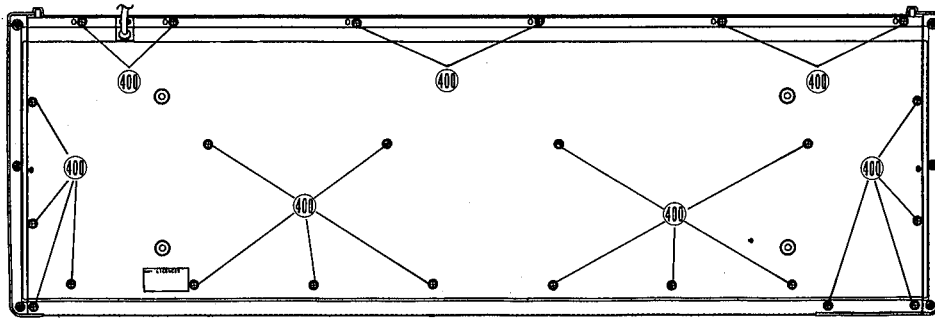
1-1. Remove the twenty-four (24) screws marked ㉹, the bottom cover assembly can be removed. (Fig. 1)

* This will give you access to the DM1, DM2, PS circuit boards, FDD assembly and wheel assembly.

1. 底板Ass'yの外し方

1-1. ㉹のネジ24本を外して底板Ass'yを取り外します。

(図 1)



㉹ : 4.0 x 8 Bonding Tapping Screw (ボンディングタッピングネジ)

(Fig. 1)

2. DM1 Circuit Board Removal

2-1. Remove the bottom cover assembly. (see procedure 1)

2-2. Remove the six (6) screws marked ㉺, the DM1 circuit board can be raised. (Fig. 2)

After the connectors have been disconnected, the DM1 circuit board can be taken out of the unit completely.

2. DM1シートの外し方

2-1. 底板Ass'yを外します。(1項参照)

2-2. ㉺のネジ6本を外しコネクタを抜いて、DM1シートを取り外します。(図 2)

3. DM2 Circuit Board Removal

3-1. Remove the bottom cover assembly. (see procedure 1)

3-2. Remove the six (6) screws marked ㉻, the DM2 circuit board can be raised. (Fig. 2)

After the connectors have been disconnected, the DM2 circuit board can be taken out of the unit completely.

3. DM2シートの外し方

3-1. 底板Ass'yを外します。(1項参照)

3-2. ㉻のネジ6本を外しコネクタを抜いて、DM2シートを取り外します。(図 2)

4. Power Supply Assembly Removal

4-1. Remove the bottom cover assembly. (see procedure 1)

4-2. Remove the screw marked ㉼ to remove the AC panel. (Fig. 2)

4-3. The power supply assembly can be removed by removing the four (4) screws marked ㉽ and disconnecting the connectors. (Fig. 2)

4. 電源Ass'yの外し方

4-1. 底板Ass'yを外します。(1項参照)

4-2. ㉼のネジ1本を外し、ACパネルを外しておきます。(図 2)

4-3. ㉽のネジ4本を外しコネクタを抜いて、電源Ass'yを取り外します。(図 2)

5. Power Transformer Removal

- 5-1. Remove the bottom cover assembly. (see procedure 1)
- 5-2. Remove the two (2) screws marked ㉓ and disconnect the connectors, then remove the power transformer. (Fig. 2)

6. FDD Assembly Removal

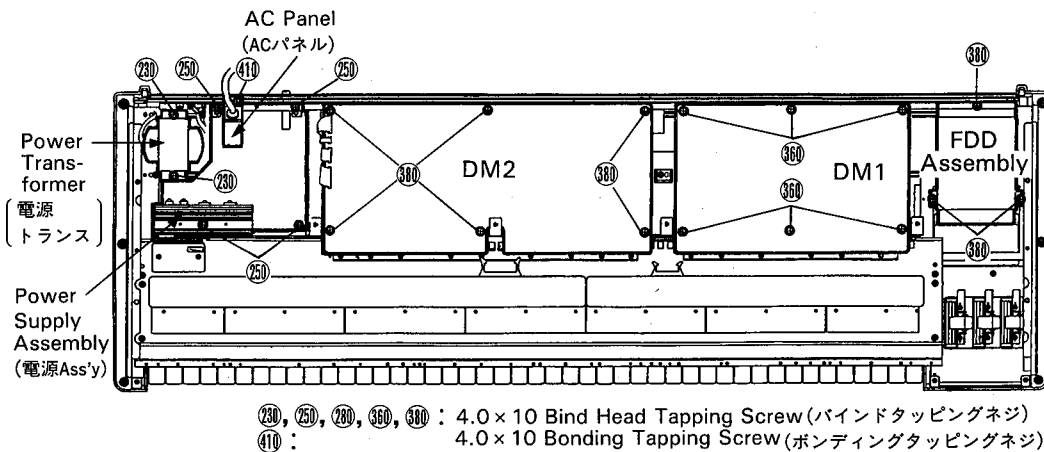
- 6-1. Remove the bottom cover assembly. (see procedure 1)
- 6-2. Remove the three (3) screws marked ㉓ and disconnect the connectors, the FDD assembly can be taken out of the SY99 unit. (Fig. 2)
- 6-3. To remove the FDD bracket from the FDD assembly, remove the four (4) screws marked ㉔. (Fig. 3)

5. 電源トランスの外し方

- 5-1. 底板Ass'yを外します。(1項参照)
- 5-2. ㉓のネジ2本を外しコネクタを抜いて、電源トランスを取り外します。(図2)

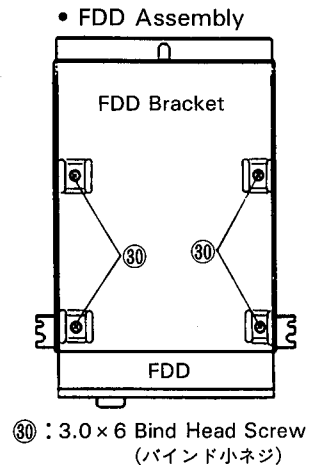
6. FDD Ass'yの外し方

- 6-1. 底板Ass'yを外します。(1項参照)
- 6-2. ㉓のネジ3本を外しコネクタを抜いて、FDD Ass'yを取り外します。(図2)
- 6-3. FDD金具は、㉔のネジ4本を外して取り外します。(図3)



㉓, ㉔, ㉕, ㉖, ㉗, ㉘, ㉙ : 4.0×10 Bind Head Tapping Screw (バインドタッピングネジ)
㉚ : 4.0×10 Bonding Tapping Screw (ボンディングタッピングネジ)

(Fig. 2)



㉔ : 3.0×6 Bind Head Screw (バインド小ネジ)

(Fig. 3)

7. Keyboard Assembly Removal

- 7-1. Remove the bottom cover assembly. (see procedure 1)
- 7-2. Remove the DM1 and DM2 circuit boards. (see procedures 2 and 3)
- 7-3. Remove the isolation sheet located under the DM2 circuit board.
- 7-4. The keyboard assembly can be removed by removing the four (4) screws marked ㉑, four (4) screws marked ㉒, eight (8) screws marked ㉓ and six (6) screws marked ㉔. (Fig. 4)

7. 鍵盤Ass'yの外し方

- 7-1. 底板Ass'yを外します。(1項参照)
- 7-2. DM1シートとDM2シートを外します。(2、3項参照)
- 7-3. DM2シートの下側にある、絶縁シートを外します。
- 7-4. ㉑のネジ4本と㉒のネジ4本、㉓のネジ8本、㉔のネジ6本を外し、MKシートのコネクタを抜いて、鍵盤Ass'yを取り外します。(図4)

8. JK (JK, JKB, CARD) circuit boards Removal

- 8-1. Remove the bottom cover assembly. (see procedure 1)
- 8-2. Remove the DM1 circuit board. (see procedure 2)
- 8-3. Raise the DM1 shield sheet up to this side. (Fig. 4)

8. JK (JK, JKB, CARD)シートの外し方

- 8-1. 底板Ass'yを外します。(1項参照)
- 8-2. DM1シートを外します。(2項参照)
- 8-3. DM1シールドシートを手前に起こします。(図4)

8-4. JK (JK) circuit board removal

8-4-1. Remove the FDD assembly. (see procedure 6)

8-4-2. Remove the four (4) screws marked (290a) and one (1) screw marked (295) on the rear panel and disconnect the connector, the JK (JK) circuit board can be removed. (Fig. 5 and Fig. 6)

8-5. JK (JKB) circuit board removal

8-5-1. Remove the six (6) screws marked (290b) on the rear panel, the JK (JKB) circuit board can be removed. (Fig. 5 and Fig. 6)

8-6. JK (CARD) circuit board removal

8-6-1. There are four (4) screws marked (270), three (3) on the CARD circuit board and one on the lug terminal. After these screws have been removed, the JK (CARD) circuit board can be removed. (Fig. 5)

8-4. JK(JK)シートの外し方

8-4-1. FDD Ass'yを外します。(6項参照)

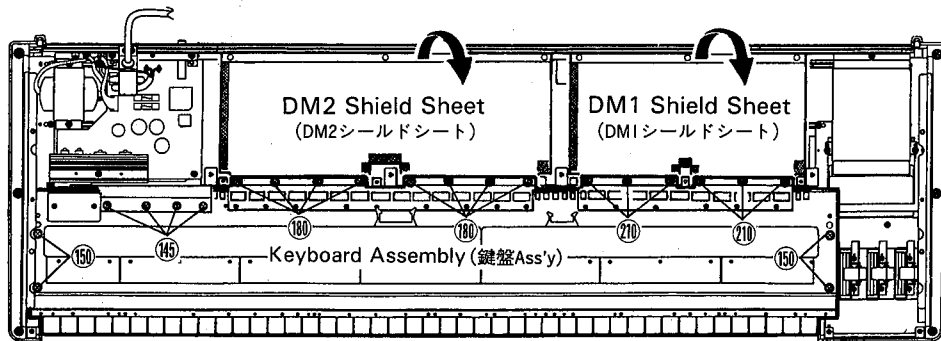
8-4-2. リア側よりJK(JK)シートを止めている(290a)のネジ4本と(295)のネジ1本を外し、コネクタを抜いて、JK(JK)シートを取り外します。(図5、6)

8-5. JK(JKB)シートの外し方

8-5-1. リア側よりJK(JKB)シートを止めている(290b)のネジ6本を外して、JK(JKB)シートを取り外します。(図5、6)

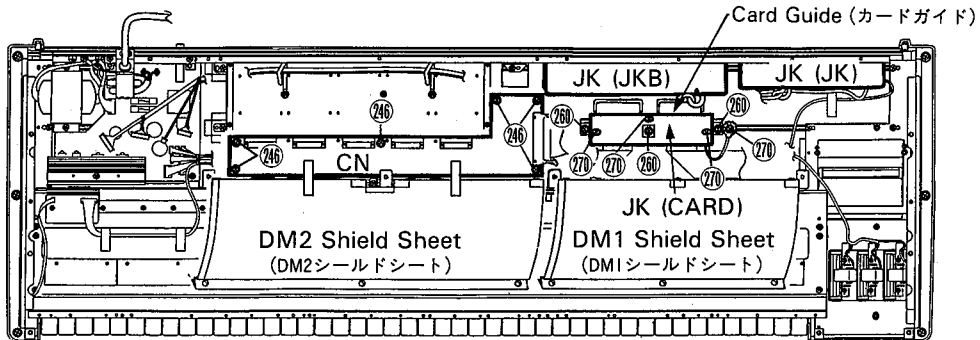
8-6. JK(CARD)シートの外し方

8-6-1. (270)のネジ4本(CARDシート:3本、ラグ端子:1本)を外し、コネクタを抜いて、JK(CARD)シートを取り外します。(図5)



(145) : 3.0 x 6 Bind Head Screw (バインド小ネジ) (150) : 4.0 x 16 Bind Head Tapping Screw (バインドタッピングネジ)
 (180, 210) : 4.0 x 10 Bind Head Tapping Screw (バインドタッピングネジ)

(Fig. 4)

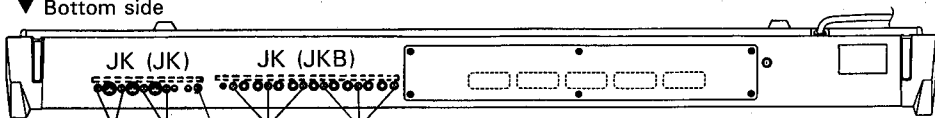


(246) : 3.0 x 6 Bind Head Screw (バインド小ネジ) (260, 270) : 4.0 x 10 Bind Head Tapping Screw (バインドタッピングネジ)

(Fig. 5)

• Rear view

▼ Bottom side



▲ Top side (290a, 290b) : 4.0 x 10 Bonding Tapping Screw (ボンディングタッピングネジ)

(295) : 4.0 x 10 Bonding Head Screw (ボンディング小ネジ)

(Fig. 6)

9. CN Circuit Board Removal

- 9-1. Remove the bottom cover assembly. (see procedure 1)
- 9-2. Remove the DM1 and DM2 circuit boards. (see procedures 2 and 3)
- 9-3. Remove the isolation sheet located under the DM2 circuit board.
- 9-4. Raise the DM1 and DM2 shield sheets up to this side. (Fig. 4)
- 9-5. Remove the six (6) screws marked ㉞ and disconnect the connector, then remove the CN circuit board. (Fig. 5)

10. Slot Unit Removal

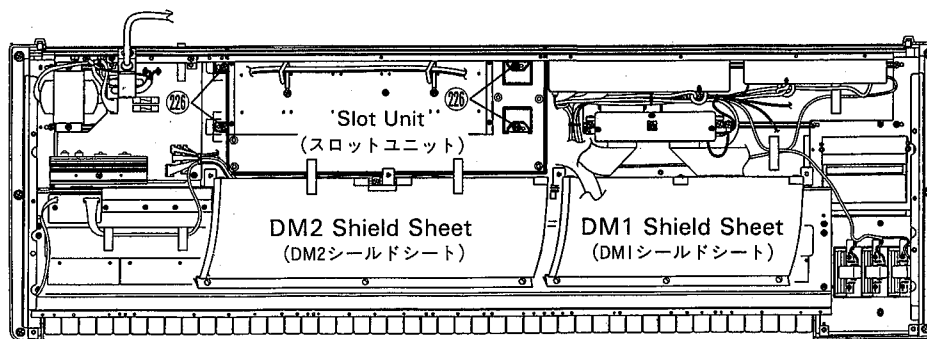
- 10-1. Remove the bottom cover assembly. (see procedure 1)
- 10-2. Remove the DM1 and DM2 circuit boards. (see procedures 2 and 3)
- 10-3. Remove the CN circuit board. (see procedure 9)
- 10-4. To remove the slot unit, remove the four (4) screws marked ㉞ (Fig. 7)

9. CNシートの外し方

- 9-1. 底板Ass'yを外します。(1項参照)
- 9-2. DM1シートとDM2シートを外します。(2、3項参照)
- 9-3. DM2シートの下側にある、絶縁シートを外します。
- 9-4. DM1シールドシートとDM2シールドシートを手前に起こします。(図4)
- 9-5. ㉞のネジ6本を外しコネクターを抜いて、CNシートを取り外します。(図5)

10. スロットユニットの外し方

- 10-1. 底板Ass'yを外します。(1項参照)
- 10-2. DM1シートとDM2シートを外します。(2、3項参照)
- 10-3. CNシートを外します。(9項参照)
- 10-4. ㉞のネジ4本を外して、スロットユニットを取り外します。(図7)



㉞ : 4.0 × 10 Bind Head Tapping Screw (バインドタッピングネジ)

(Fig. 7)

11. PNAB and PNC Circuit Boards Removal

- 11-1. Pull out the knobs on the control panel.
- 11-2. Remove the bottom cover assembly. (see procedure 1)
- 11-3. Remove the DM1 and DM2 circuit boards. (see procedures 2 and 3)
- 11-4. Remove the power supply assembly. (see procedure 4)
- 11-5. Remove the keyboard assembly. (see procedure 7)
- 11-6. Remove the sixteen (16) screws marked ㉞ to remove the center angle bracket with the DM1, DM2 and MK shield sheets. (Fig. 8)
- 11-7. Remove the CN circuit board. (see procedure 9)
- 11-8. Remove the slot unit. (see procedure 10)

11. PNABシートとPNCシートの外し方

- 11-1. パネル表側より、スライドボリューム類のつまみを抜き取っておきます。
- 11-2. 底板Ass'yを外します。(1項参照)
- 11-3. DM1シートとDM2シートを外します。(2、3項参照)
- 11-4. 電源Ass'yを外します。(4項参照)
- 11-5. 鍵盤Ass'yを外します。(7項参照)
- 11-6. ㉞のネジ16本を外して、DM1シールドシート、DM2シールドシート、MKシールドシートと一緒にセンターアングルを外します。(図8)
- 11-7. CNシートを外します。(9項参照)
- 11-8. スロットユニットを外します。(10項参照)

11-9. PNAB circuit board removal

11-9-1. Remove the three (3) screws marked ②⑥ to remove the card guide with the JK (CARD) circuit board. (Fig. 5)

11-9-2. After the six (6) screws marked ①⑩ have been removed, the PNAB circuit board can be removed. (Fig. 9)

* The PNAB circuit board is connected to the PNC circuit board with connector assembly.

11-10. PNC circuit board removal

11-10-1. Remove the two (2) screws marked ②③ to remove the PNC shield sheet. (Fig. 9)

11-10-2. After the six (6) screws marked ②⑦ have been removed, the PNC circuit board can be removed. (Fig. 10)

11-9. PNABシートの外し方

11-9-1. ②⑥のネジ3本を外して、JK(CARD)シートと一緒にカードガイドを取り外します。(図5)

11-9-2. ①⑩のネジ6本を外して、PNABシートを取り外します。(図9)

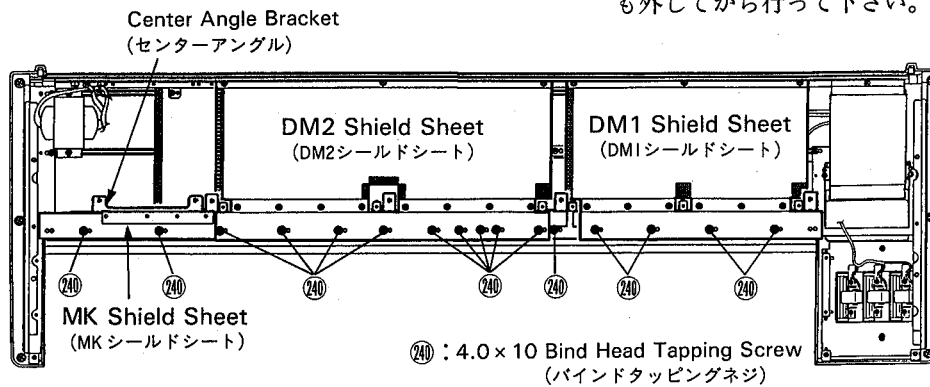
※コネクタも一緒に外す場合は、PNCシートも外してから行って下さい。

11-10. PNCシートの外し方

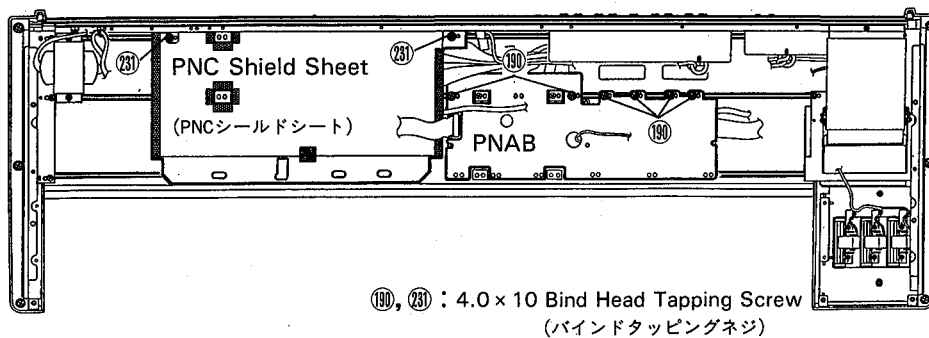
11-10-1. ②③のネジ2本を外して、PNCシールドシートを外します。(図9)

11-10-2. ②⑦のネジ6本を外して、PNCシートを取り外します。(図10)

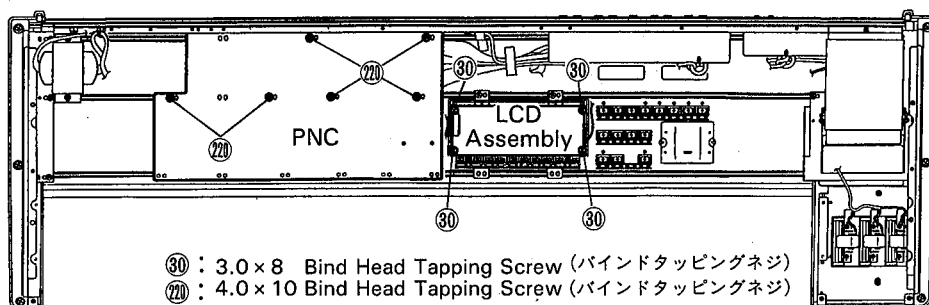
※コネクタも一緒に外す場合は、PNABシートも外してから行って下さい。



(Fig. 8)



(Fig. 9)



(Fig. 10)

12. LCD Assembly Removal

- 12-1. Remove the bottom cover assembly. (see procedure 1)
- 12-2. Remove the DM1 and DM2 circuit boards. (see procedures 2 and 3)
- 12-3. Remove the power supply assembly. (see procedure 4)
- 12-4. Remove the keyboard assembly. (see procedure 7)
- 12-5. Remove the CN circuit board. (see procedure 9)
- 12-6. Remove the slot unit. (see procedure 10)
- 12-7. Remove the PNAB circuit board. (see procedure 11-9)
- 12-8. The LCD assembly can be removed by removing the four (4) screws marked ③⑩. (Fig. 10)

13. Wheel Assembly Removal

- 13-1. Remove the bottom cover assembly. (see procedure 1)
- 13-2. After the six (6) screws marked ③⑩ have been removed, the wheel assembly can be removed. (Fig. 11)

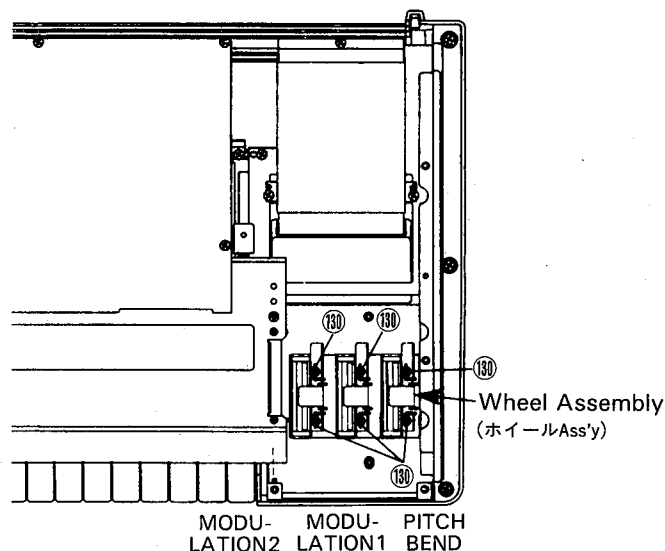
12. LCD Ass'yの外し方

- 12-1. 底板Ass'yを外します。(1項参照)
- 12-2. DM1シートとDM2シートを外します。(2、3項参照)
- 12-3. 電源Ass'yを外します。(4項参照)
- 12-4. 鍵盤Ass'yを外します。(7項参照)
- 12-5. CNシートを外します。(9項参照)
- 12-6. スロットユニットを外します。(10項参照)
- 12-7. PNABシートを外します。(11-9項参照)
- 12-8. ③⑩のネジ4本を外して、LCD Ass'yを取り外します。

(図10)

13. ホイールAss'yの外し方

- 13-1. 底板Ass'yを外します。(1項参照)
- 13-2. ③⑩のネジ6本を外して、ホイールAss'yを取り外します。(図11)



③⑩ : 3.0 × 8 Bonding Tapping Screw (ボンディングタッピングネジ)

(Fig. 11)

14. Rotary Encoder Knob Removal

- 14-1. Remove the bottom cover assembly. (see procedure 1)
- 14-2. Remove the DM1 and DM2 circuit boards. (see procedures 2 and 3)
- 14-3. Remove the power supply assembly. (see procedure 4)
- 14-4. Remove the keyboard assembly. (see procedure 7)
- 14-5. Remove the CN circuit board. (see procedure 9)
- 14-6. Remove the slot unit. (see procedure 10)
- 14-7. Remove the PNC circuit board. (see procedure 11-10)
- 14-8. Pull out the rotary encoder knob on the PNC circuit board.

14. ロータリーエンコーダツマミ(データエントリーツマミ)の外し方

- 14-1. 底板Ass'yを外します。(1項参照)
- 14-2. DM1シートとDM2シートを外します。(2、3項参照)
- 14-3. 電源Ass'yを外します。(4項参照)
- 14-4. 鍵盤Ass'yを外します。(7項参照)
- 14-5. CNシートを外します。(9項参照)
- 14-6. スロットユニットを外します。(10項参照)
- 14-7. PNCシートを外します。(11-10項参照)
- 14-8. PNCシートからロータリーエンコーダツマミを取り外します。

LSI PIN DESCRIPTION (LSI端子機能表)

• H8/532 (X1722D00) CPU

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	XTAL	I	Clock	43	A8	O	Address bus
2	Vss	I	Ground	44	A9	O	
3	P10/ ϕ	O	System clock	45	A10	O	
4	P11/E	O	Enable	46	A11	O	
5	P12	O	(Bus acknowledge)	47	A12	O	
6	P13	I	(Bus request)	48	A13	O	
7	P14/WAIT	I	Wait	49	A14	O	
8	P15/IRQ0	I	Interrupt request 0	50	A15	O	
9	P16/IRQ1	I	Interrupt request 1	51	A16	O	
10	P17	O	8-bit timer output	52	A17	O	
11	AS	O	Address strobe	53	A18	O	Power supply
12	R/W	O	Read/Write	54	A19	O	
13	DS	O	Data strobe	55	Vcc		Port 7
14	RD	O	Read control	56	P70	I/O	
15	WR	O	Write control	57	P71	I/O	
16	Vcc		Power supply	58	P72	I/O	Port 8
17	MD0	I	Mode control	59	P73	I/O	
18	MD1	I					
19	MD2	I					
20	STBY	I		Standby	60	P74	I/O
21	RES	I	Reset	61	P75	I/O	Ground
22	NMI	I	Non-maskable interrupt	62	P76	I/O	
23	NC		Ground	63	P77	I/O	Analog ground
24	Vss			64	Vss		
25	D0	I/O	Data bus	65	AVss		Analog power supply
26	D1	I/O					
27	D2	I/O					
28	D3	I/O					
29	D4	I/O					
30	D5	I/O					
31	D6	I/O					
32	D7	I/O		74	AVcc		
33	A0	O	Address bus	75	P90	I/O	Port 9
34	A1	O					
35	A2	O					
36	A3	O					
37	A4	O					
38	A5	O					
39	A6	O					
40	A7	O		76	P91	I/O	
41	Vss		Ground	77	P92	I/O	
42	Vss				78	P93	I/O
				79	P94	I/O	(Transmit data)
				80	TXD	I/O	
				81	RXD	I	(Receive data)
				82	P97	I/O	
				83	Vss		Ground
				84	EXTAL	I	
							Clock

• HD63C01Y0F64P (XF148A00) CPU

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	Vss	I	Ground	33	Vcc		DC Supply (+5V)
2	XTAL	I	Clock (8MHz)	34	A15	O	
3	EXTAL	I					
4	MP0	I	Mode program	35	A14	O	Address bus
5	MP1	I					
6	RES	I	Reset	36	A13	O	
7	STBY	I	Stand-by mode signal	37	A12	O	
8	NMI	I	Non-maskable interrupt	38	P11	O	Ground
9	P20	I/O	Port 2	39	P10	O	
10	P21	I/O					
11	P22	I/O					
12	P23	I/O					
13	P24	I/O					
14	P25	I/O					
15	P26	I/O					
16	P27	I/O	Port 5	40	A9	O	Address bus
17	P50	I/O					
18	P51	I/O					
19	P52	I/O					
20	P53	I/O	Port 6	41	A8	O	Data bus
21	P54/ \bar{S}	I/O					
22	P55/OS	I/O					
23	P56	I/O					
24	P57	I/O					
25	P60	I/O					
26	P61	I/O					
27	P62	I/O	Bus available	42	Vss		
28	P63	I/O		Load instruction resistor	43	A7	O
29	P64	I/O	Read/Write control		44	A6	O
30	P65	I/O		Write	45	A5	O
31	P66	I/O	Read		46	A4	O
32	P67	I/O		Enable	47	A3	O
					48	A2	O
				49	A1	O	
				50	A0	O	
				51	D7	I/O	
				52	D6	I/O	
				53	D5	I/O	
				54	D4	I/O	
				55	D3	I/O	
				56	D2	I/O	
				57	D1	I/O	
				58	D0	I/O	
				59	BA	O	
				60	LIR	O	
				61	R/W	O	
				62	WR	O	
				63	RD	O	
				64	E	O	

• **YM3029 (XF237A00) AFDO (Floating Point Converter)**

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	DVDD		Digital power supply (+5V)	15	SHA	I	Sample and hold input (Channel A)
2	LE	O	Latch enable	16	EXG		Exponent ground
3	DAB	O	Channel A/B data output	17	EXG		
4	SYW	I	Sync pulse	18	EXI	I	Exponent input
5	CLK	I	Clock	19	EXO	O	Exponent output
6	$\phi 1$	O	Clock for DAC	20	AVSS		Analog power supply (-5V)
7	DGND		Digital ground	21	AVDD		Analog power supply (+5V)
8	AVDD		Analog power supply (+5V)	22	SI1	I	Serial data input 1 (Channel A)
9	AVSS		Analog power supply (-5V)	23	VLA0	I	Volume level select (Channel A)
10	SHB	I	Sample and hold input (Channel B)	24	VLA1	I	
11	CH4	O	Output (Channel 4)	25	SI2	I	Serial data input 2 (Channel B)
12	CH3	O	Output (Channel 3)	26	VLB0	I	Volume level select (Channel B)
13	CH2	O	Output (Channel 2)	27	VLB1	I	
14	CH1	O	Output (Channel 1)	28	4/2	I	Channel number select (4 or 2-channel)

• **YM3422B (XE862B00) ESI**

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	Vss		Ground	9	M0	I	Mode select
2	SI0	I	Serial data input 0	10	M1	I	
3	SO0	O	Serial data output 0	11	M2	I	
4	BC0	I	Clock CH0	12	M3	I	
5	WC0	I	W-clock CH1	13	WC1	I	W-clock CH1
6	SI2	I	Serial data input 2	14	BC1	I	Clock CH1
7	SO2	O	Serial data output 2	15	SO1	O	Serial data output 1
8	VDD		Power supply	16	SI1	I	Serial data input 1

• **YM3807 (XA902001) MOD (Modulation Signal Generator)**

Pin No.	Name	I/O	Function	Pin No.	Name	I/O	Function	
1	NC			24	Vss		Power supply ground	
2	MDSIO	I	Inputs data to add to the waveform data inside MOD	23	CDO	O	CD interface serial data output	
3	MDSI1	I			22	CDI	I	CD interface serial data input
4	MDSO0	O		Outputs MOD internal waveform data with the same data format as MDSIO.	21	NC		
5	MDSO1	O			20	XCLK	I	CD interface transmission clock input
6	MOD0	O	Outputs waveform data for all channels inside MOD.	19	XMD	I	Selects 1/16 mode (asynchronous) or 1/1 mode (synchronous) for the CD interface	
7	MOD1	O			18	$\overline{\text{CRS}}$	I	CD counter reset
8	MOD2	O			17	CLK	I	Clock
9	MOD3	O			16	$\overline{\text{IC}}$	I	Initial clear
10	MOD4	O			15	$\overline{\text{SYW}}$	I	Sync signal input. One 64th of the master clock.
11	MOD5	O	Outputs waveform data for all channels inside MOD.	14	MOD7	O		
12	VDD			Power supply (+5V)	13	MOD6	O	

• **YM6007** (XF164A00) DSP 2 (Digital Signal Processor)

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION	
1	D18	I/O	Data bus	65	NC			
2	D17	I/O		66	CDO	O	Serial control data output	
3	D16	I/O		67	CDI	I	Serial control data input	
4	NC		Power supply	68	V _{DD}		Power supply	
5	V _{DD}			69	SIO	I	Serial data input (28bit × 4ch)	
6	D15	I/O		70	NC			
7	NC		Data bus	71	NC			
8	NC			72	SI1	I	Serial data input (28bit × 4ch)	
9	D14	I/O		73	SI2	I	Serial data input (28bit × 2ch)	
10	NC			74	SO0	O	Serial data output (28bit × 4ch)	
11	D13	I/O		75	NC			
12	D12	I/O		76	SO1	O	Serial data output (28bit × 4ch)	
13	D11	I/O		77	SO2	O	Serial data output (28bit × 2ch)	
14	NC			78	SCLK	O	Clock (64bit/sample)	
15	D10	I/O		79	NC			
16	NC			80	NC			
17	NC		81	NC				
18	D9	I/O	82	NC				
19	D8	I/O	83	TRGO	O	Trigger output		
20	D7	I/O	84	TRGI	I	Trigger input		
21	NC		85	DCLK	I	Clock (Master clock × 2)		
22	D6	I/O	86	SYW	I	Synch. pulse		
23	D5	I/O	87	IC	I	Initial clear		
24	NC		88	NC				
25	D4	I/O	89	MRQ	O	Control data receive		
26	NC		90	MDA0	I	Serial mode: Internal register select Parallel mode: CDI, CDO data format select		
27	D3/A15	I/O	91	NC				
28	NC		92	NC				
29	D2/A14	I/O	93	MDA1	I			
30	D1/A13	I/O	Data bus	94	MDA2	I		
31	D0/A12	I/O		95	NC			
32	NC		96	MWE	I	MD pin input strobe		
33	CAS/A11	O	Column address strobe	97	MOS	I	MD pin output strobe	
34	RAS/A10	O	Row address strobe	98	MOS	I	Communication mode select (0: serial 1: parallel)	
35	A9	O	Address bus	99	DM7	I/O	Parallel data	
36	A8	O			100	DM6		I/O
37	V _{SS}			Ground	101	NC		
38	A7	O		102	NC			
39	NC			103	DM5	I/O		
40	A6	O		104	NC			
41	NC			105	DM4	I/O		
42	A5	O		106	NC			
43	NC		Address bus	107	DM3	I/O		
44	A4	O			108	DM2		I/O
45	A3	O			109	DM1	I/O	
46	NC			110	DM0	I/O		
47	NC			111	NC			
48	NC			112	NC			
49	NC			113	D27	I/O		
50	NC			114	NC			
51	NC			115	D26	I/O		
52	A2	O		116	NC			
53	A1	O		117	D25	I/O		
54	A0	O		118	D24	I/O		
55	WE	O	Write control	119	NC			
56	OE	O	Output enable	120	D23	I/O		
57	NC			121	NC			
58	DBOE	I	Memory data bus output enable	122	NC			
59	NC			123	D22	I/O		
60	TIM1	O	Timing pulse	124	V _{SS}			
61	TST	O	Test pin	125	NC			
62	CRS	I/O	Reset (Serial mode: I Parallel mode: O)	126	D21	I/O		
63	NC			127	D20	I/O		
64	XCLK	I	Shift clock for serial control data	128	D19	I/O		

• **YM7102 (XG996A00) PAN (Panning Processor)**

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	A0	I	Address bus	41	L8/ACC8	O	L channel data
2	D7	I/O		42	L9/ACC9	O	
3	D6	I/O		43	L10/ACC10	O	
4	D5	I/O		44	L11/ACC11	O	
5	D4	I/O		45	L12/ACC12	O	
6	D3	I/O		46	L13/ACC13	O	
7	D2	I/O		47	L14/ACC14	O	
8	D1	I/O		48	L15/ACC15	O	
9	D0	I/O		49	R0/ACC16	O	
10	IN1	I	50	R1/ACC17	O		
11	INO	I	51	R2/ACC18	O		
12	SI2	I	52	R3/ACC19	O		
13	SI1	I	53	R4	O		
14	TEGSS	I	54	R5	O		
15	TEGS2	I	55	R6	O		
16	TEGS1	I	56	R7	O		
17	TEGSO	I	57	R8	O		
18	NC		58	R9	O		
19	CDO	O	59	R10	O	Test pin	
20	CRS	O	60	R11	O		
21	S1	O	61	R12	O		
22	S2	O	62	R13	O		
23	SYW	O	63	R14	O		
24	DSPCLK	O	64	R15	O		
25	MODE	I	65	NC			
26	IC	I	66	TTIM	I		
27	SYNC	I	67	TEG1	I		
28	ϕ_M	I	68	TRD	I		
29	V _{se}		69	CS2	I	Chip select	
30	V _{ss}		70	CS1	I		
31	V _{DD}		71	V _{DD}		Power supply	
32	V _{DD}		72	CS0	I		
33	L0/ACC0	O	73	A7	I	Address bus	
34	L1/ACC1	O	74	A6	I		
35	L2/ACC2	O	75	A5	I		
36	L3/ACC3	O	76	A4	I		
37	L4/ACC4	O	77	A3	I		
38	L5/ACC5	O	78	A2	I		
39	L6/ACC6	O	79	A1	I		
40	L7/ACC7	O	80				

• **μ PD71055C (XB361001) PPI (Programmable Peripheral Interface)**

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	PO3	I/O	Port 0	21	P13	I/O	Port 2
2	PO2	I/O		22	P14	I/O	
3	PO1	I/O		23	P15	I/O	
4	PO0	I/O		24	P16	I/O	
5	RD	I	Read control	25	P17	I/O	DC Supply
6	CS	I	Chip Select	26	V _{DD}		
7	GND		DC Supply (0V)	27	D7	I/O	
8	A1	I	Port address	28	D6	I/O	Data bus
9	A0	I		29	D5	I/O	
10	P27	I/O		30	D4	I/O	
11	P26	I/O	31	D3	I/O		
12	P25	I/O	32	D2	I/O		
13	P24	I/O	33	D1	I/O		
14	P20	I/O	34	D0	I/O		
15	P21	I/O	Port 2	35	RESET	I	Reset
16	P22	I/O		36	WR	I	
17	P23	I/O		37	PO7	I/O	
18	P10	I/O	Port B	38	PO6	I/O	Port 0
19	P11	I/O		39	PO5	I/O	
20	P12	I/O		40	PO4	I/O	

• **YM7103 (XG993A00) EGM2 (Envelope Generator)**

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	A0	I	Address bus	41	NC		Key on data
2	D7	I/O		42	KON	O	
3	D6	I/O		43	E0	O	
4	D5	I/O		44	E1	O	
5	D4	I/O		45	E2	O	
6	D3	I/O		46	E3	O	
7	D2	I/O		47	E4	O	
8	D1	I/O		48	E5	O	
9	D0	I/O		49	E6	O	
10	NC		50	E7	O		
11	TST10	O	51	E8	O	Envelope data, Pitch data (portament), Pitch envelope data	
12	TST9	O	52	E9	O		
13	TST8	O	53	E10	O		
14	TST7	O	54	E11	O		
15	TST6	O	55	E12	O		
16	TST5	O	56	E13	O		
17	TST4	O	57	NC			
18	TST3	O	58	NC			
19	TST2	O	59	NC			
20	TST1	O	60	NC			
21	TST0	O	61	NC			
22	ϕ M0	O	62	NC			
23	XTAL	O	63	NC			
24	EXTAL	I	64	NC			
25	IC	I	65	NC			
26	SYO	O	66	NC			
27	SYI	I	67	NC			
28	ϕ MI	I	68	NC			
29	Vss		69	NC			
30	Vss		70	NC			
31	NC		71	TRD	I	Test pin	
32	Vdd		72	Vdd		Power supply	
33	NC		73	CS0	I	Chip select	
34	NC		74	CS1	I		
35	NC		75	CS2	I		
36	TEGS2	I	76	A5	I	Address bus	
37	TEGS1	I	77	A4	I		
38	TEGS0	I	78	A3	I		
39	TS01	O	79	A2	I		
40	TS00	O	80	A1	I		

• **WD37C65C-JM00 (XH129B00) FDC (Floppy Disk Controller)**

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	RD	I	Read control	23	XT2	I	XTAL osc. in
2	WR	I	Write control	24	DRV	O	Drive type
3	CS	I	Chip select	25	XT1	O	XTAL osc. drive
4	AO	I	Register select	26	XT1	I	XTAL osc. in
5	DACK	I	DMA acknowledge	27	PCVAL	I	Precompensation value
6	TC	I	Terminal Count	28	HS	O	Head select (Side select)
7	DB0	I/O	Data bus	29	WE	O	Write enable
8	DB1	I/O		30	WD	O	Write data
9	DB2	I/O		31	DIRC	O	Direction control
10	DB3	I/O		32	STEP	O	Step pulse
11	DB4	I/O		33	DS1	O	Drive select 1
12	DB5	I/O		34	Vss		Ground
13	DB6	I/O		35	DS2	O	Drive select 2
14	DB7	I/O		36	MO1/DS3	O	Motor ON 1/Drive select 3
15	DMA	O	Direct memory access request	37	MO2/DS4	O	Motor ON 2/Drive select 4
16	IRQ	O	Interrupt request	38	HDL	O	Head loaded
17	DCHGEN	I	Disk change enable	39	RPM/RWC	O	Revolutions per minute/Reduced write current
18	LDOR	I	Load operations register	40	DCHG	I	Disk change
19	LDCR	I	Load control register	41	WP	I	Write protected
20	RST	I	Reset	42	TROO	I	Track 00 signal
21	RDD	I	Read disk data	43	IDX	I	Index
22	XT2	O	XTAL osc. drive	44	Vcc		Power supply

• YM7107 (XG994A00) OPS3 (FM Tone Generator)

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	A0	I	Address bus	41	DA8	O	D/A signal (straight binary)
2	D7	I/O		42	DA9	O	
3	D6	I/O		43	DA10	O	
4	D5	I/O	44	DA11	O		
5	D4	I/O	45	DA12	O		
6	D3	I/O	46	DA13	O		
7	D2	I/O	47	DA14	O		
8	D1	I/O	48	DA15	O		
9	D0	I/O	49	SH1	O	(MSB)	
10	E13	I	50	SH2	O	Sample and hold	
11	E12	I	51	SC0	O	Channel distribution	
12	E11	I	52	SC1	O		
13	E10	I	53	SC2	O		
14	E9	I	54	SO0	O	Serial data (2 compl. 16bits LSB first)	
15	E8	I	55	SO1	O		
16	E7	I	56	NC			
17	E6	I	57	NC		Envelope data, Pitch envelope data, Pitch data	
18	E5	I	58	NC			
19	E4	I	59	NC			
20	E3	I	60	NC			
21	E2	I	61	NC			
22	E1	I	62	NC			
23	E0	I	63	NC			
24	KON	I	64	NC			
25	IC	I	65	NC			
26	NC		66	NC			
27	SYNC	I	67	NC		Phase reset for phase accumulator initial clear	
28	ϕ_M	I	68	V _{SS}			
29	V _{SS}		69	SIO	I	Serial data	
30	V _{SS}		70	SI1	I		
31	V _{DD}		71	NC		Power supply	
32	V _{DD}		72	V _{DD}			
33	DA0	O	73	CS0	I		
34	DA1	O	74	CS1	I	Chip select	
35	DA2	O	75	CS2	I		
36	DA3	O	76	A4	I	Address bus	
37	DA4	O	77	A3	I		
38	DA5	O	78	A2	I		
39	DA6	O	79	A1	I		
40	DA7	O	80	V _{SS}		Ground	

• HD63B01Y0RM21P (XG950C00) CPU (PKS)

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	V _{SS}		Ground	33	V _{CC}		DC Supply (+5V)
2	XTAL	I		34	P47	O	
3	EXTAL	I	Clock (8MHz)	35	P46	O	
4	MP0	I		36	P45	O	
5	MP1	I	Mode program	37	P44	O	Port 4
6	RES	I		38	P43	O	
7	STBY	I	Reset	39	P42	O	
8	NMI	I	Stand-by mode signal	40	P41	O	
9	P20	I/O	Non-maskable interrupt	41	P40	O	
10	P21	I/O	Port 2	42	V _{SS}		Ground
11	P22	I/O		43	P17	O	
12	P23	I/O		44	P16	O	
13	P24	I/O		45	P15	O	Port 1
14	P25	I/O		46	P14	O	
15	P26	I/O		47	P13	O	
16	P27	I/O		48	P12	O	
17	P50	I/O	49	P11	O	Port 5	
18	P51	I/O	50	P10	O		
19	P52	I/O	51	P37	I/O		
20	P53	I/O	52	P36	I/O		
21	P54	I/O	53	P35	I/O	Port 3	
22	P55	I/O	54	P34	I/O		
23	P56	I/O	55	P33	I/O		
24	P57	I/O	56	P32	I/O	Port 6	
25	P60	I/O	57	P31	I/O		
26	P61	I/O	58	P30	I/O		
27	P62	I/O	59	P74	O		
28	P63	I/O	60	P73	O	Port 7	
29	P64	I/O	61	P72	O		
30	P65	I/O	62	P71	O		
31	P66	I/O	63	P70	O		
32	P67	I/O	64	E	O	Enable	

• YM7119 (XG995A00) M3 (AWM Tone generator & Digital Filter)

PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	INDV0	O	Individual output 0 (8 channels)	65	WA8	O	Wave memory address bus
2	INDV1	O	Individual output 1 (8 channels)	66	WA9	O	
3	OPZ	I	MELIN input select (⊕OPZ, ⊙PAN)	67	WA10	O	
4	DIOU0	O	Stereo output (L & R)	68	WA11	O	
5	DIOU1	O	Assignable output (ch.0 & ch.4)	69	WA12	O	
6	DIOU2	O	Assignable output (ch.1 & ch.5)	70	WA13	O	
7	DIOU3	O	Assignable output (ch.2 & ch.6)	71	WA14	O	
8	DIOU4	O	Assignable output (ch.3 & ch.7)	72	NC		
9	MELIN	I	MEL formatted signal input	73	WA15	O	
10	LSB/MSB	I	Individual output mode select	74	WA16	O	
11	TTPAD0	I/O	(⊕ MSB first, ⊙ LSB first)	75	WA17	O	
12	TTPAD1	I/O		76	WA18	O	
13	NC		Test pin	77	WA19	O	
14	TTPAD2	I/O		78	WA20	O	
15	TTPAD3	I/O		79	WA21	O	
16	TTPAD4	I/O		80	WA22	O	
17	TTPAD5	I/O		81	WA23	O	
18	NC			82	A0	I	
19	TTPAD6	I/O		83	A1	I	
20	TTPAD7	I/O		84	A2	I	
21	NC			85	A3	I	
22	TTPAD8	I/O		86	A4	I	
23	TTPAD9	I/O		87	A5	I	
24	NC		88	D0	I/O		
25	TTPAD10	I/O	Individual input 0 (8 channels)	89	NC		
26	TTPAD11	I/O		Individual input 1 (8 channels)	90	D1	I/O
27	DIIN0	I			91	D2	I/O
28	DIIN1	I			92	D3	I/O
29	WDO	I/O			93	D4	I/O
30	WD1	I/O			94	D5	I/O
31	WD2	I/O			95	D6	I/O
32	WD3	I/O	96		D7	I/O	
33	NC		97	S/HSCO	I		
34	WD4	I/O	98	S/HSC1	I		
35	WD5	I/O	99	S/HSC2	I		
36	WD6	I/O	100	S/HSC3	I		
37	WD7	I/O	Wave memory data	101	S/HEN	O	
38	WD8	I/O		102	S/H0	O	
39	WD9	I/O		103	S/H1	O	
40	NC			104	S/H2	O	
41	NC			105	S/HRCA	I	
42	WD10	I/O		106	S/HRCB	I	
43	WD11	I/O		107	IC	I	
44	NC			108	Vss		
45	WD12	I/O		109	XTAL	O	
46	WD13	I/O		110	EXTAL	I	
47	WD14	I/O		111	NC		
48	Vss			112	FCLKOUT	O	
49	VDD			113	FCLKIN	I	
50	WD15	I/O		114	NC		
51	MSBW	O	115	CLK3	O		
52	LSBW	O	116	VDD			
53	OE	O	117	SYWIN	I		
54	ODD/EVEN	I	118	CLKMEL	O		
55	SINGLE/DUAL	I	119	NC			
56	WA0	O	120	DACLE	O		
57	WA1	O	121	SYWOUT	O		
58	WA2	O	122	SYW64	O		
59	WA3	O	123	IRQ	O		
60	WA4	O	124	CS	I		
61	WA5	O	125	R/W	I		
62	WA6	O	126	CHPIN	I		
63	WA7	O	127	CHPOUT	O		
64	NC		128	KSYNC	I		

• TMC57800N (XG662A00) MIX5 (Mixer)

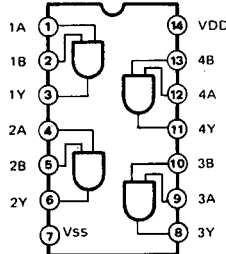
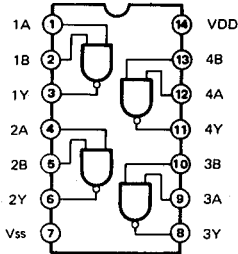
PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	MX17	I	Voice data input	11	SYM	I	Synch pulse
2	MX16	I		12	CDO	O	Control data output
3	MX15	I		13	XCLK	I	CDI and CDO in/out clock
4	MX14	I		14	CDI	I	Control data input
5	MX13	I		15	CRS	I	Counter reset for CDI and CDO
6	MX12	I		16	MX00	O	Voice data output
7	MX11	I		17	MX01	O	
8	MX10	I		18	MX02	O	
9	CLK	I	Master clock	19	MX03	O	Power supply
10	Vss	I	Ground	20	VDD	O	

• LC9116A-483 (XI364A00) <TW80> Decoder

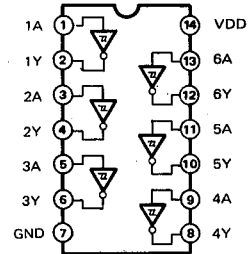
PIN NO.	NAME	I/O	FUNCTION	PIN NO.	NAME	I/O	FUNCTION
1	MX00	O	Mix output (24-bit limit) MEL (A) (B)	33	Vss	O	Ground
2	MX01	O		34	RSTB	O	Reset output
3	LIMO0	O	Limiter output (22-bit limit) MEL	35	WA17	I	Wave address bus
4	LIMO1	O		36	WA18	I	
5	LIMO2	O		37	WA19	I	
6	LIMO3	O		38	WA20	I	
7	Vss	O	Ground	39	WA21	I	
8	CO1	O	OR-output (A) (B) (C)	40	WA22	I	Decoder output to WAVE CARD
9	CO2	O		41	WA23	I	
10	CO3	O		42	CARDB	O	Decoder output to WAVE RAM
11	AI4	I	Buffer input	43	RAM2B	O	
12	AI3	I		44	RAM1B	O	
13	AI2	I		45	ROM4B	O	Decoder output to WAVE ROM
14	AI1	I	46	ROM3B	O		
15	BI1	I	Inverting input	47	ROM2B	O	
16	BO1	O	Inverting output	48	ROM1B	O	
17	Vss	O	Ground	49	Vss	O	Ground
18	SYW1	I	Synch. signal	50	EXTAL	I	Clock (12.288 MHz)
19	CLK	I	Clock	51	XTAL	I	
20	PST1B	I	Reset input	52	Vss	O	Ground
21	PST2B	I		53	FCLK	O	Clock (12.288 MHz)
22	AO1	O	Buffer output	54	LCLK2	O	Clock (6.144 MHz)
23	AO2	O		55	LCLK1	O	Clock (6.144 MHz)
24	VDD	O	Power supply	56	VDD	O	Power supply
25	AO3	O	Buffer output	57	LIM13	I	Limiter input (24-bit) MEL
26	AO4	O		58	LIM12	I	
27	S0	I	OR-input (A)	59	LIM11	I	
28	S1	I		60	LIM10	I	
29	S2	I	OR-input (B)	61	MX13	I	Mix input (B)
30	S3	I		62	MX12	I	
31	S4	I	OR-input (C)	63	MX11	I	Mix input (A)
32	S5	I		64	MX10	I	

IC BLOCK DIAGRAM (ICブロック図)

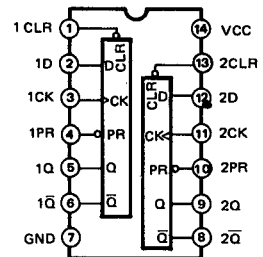
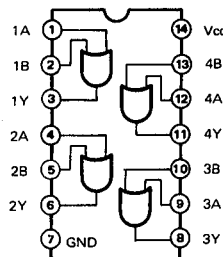
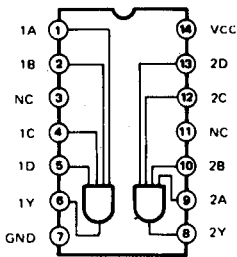
- **TC74HC00AF-TP1** (XD655A00) • **TC74HC08AF-TP1**
Quad 2 Input NAND Quad 2 Input AND



- **SN74HC14N** (IR001450)
• **SN74LS14N** (IG049650)
Hex Inverter

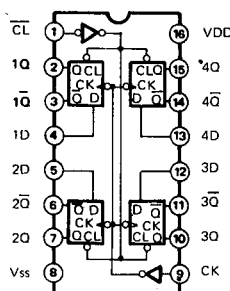
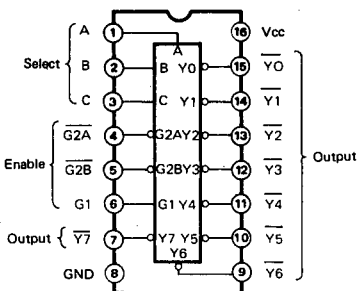


- **TC74HC21AF** (XJ622A00) • **SN74ALS32N** (XA055001) • **SN74HC74NSR** (XC726001)
Dual 4 Input AND Quad 2 Input OR Dual D-Type Flip-Flop

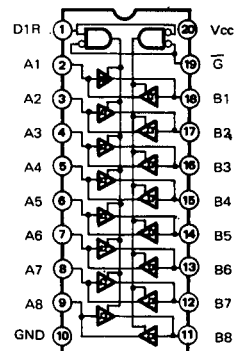


INPUTS				OUTPUTS	
PR	CLR	CLK	D	Q	Q-bar
L	H	X	X	H	L
H	L	X	X	L	H
L	L	X	X	H	H
H	H	↑	H	H	L
H	H	↑	L	L	H
H	H	L	X	Q ₀	Q ₀ -bar

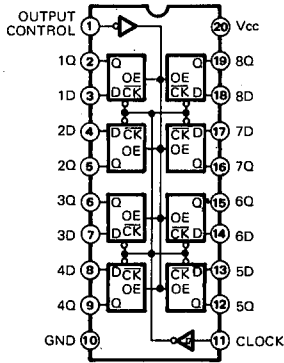
- **SN74ALS138N** (IG149600) • **TC74HC175AF-TP1**
• **SN74HC138NSR** (XD835A00) (XD658A00)
• **TC74HC138AP** (IR013800) Quad D-Type Flip-Flop
3 to 8 Demultiplexer



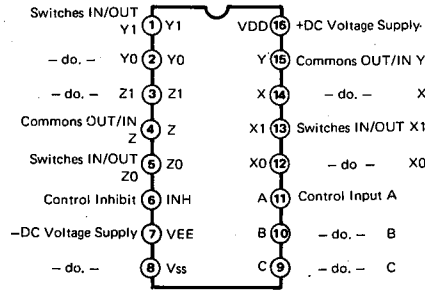
- **SN74ALS245ANSR** (XE057A00)
• **SN74LS245ANSR** (XH779A00)
• **TC74AC245P** (XH608A00)
• **TC74HC245AP** (IR024500)
• **TC74HC245F-T1** (XD603A00)
Octal 3-State Bus Transceiver



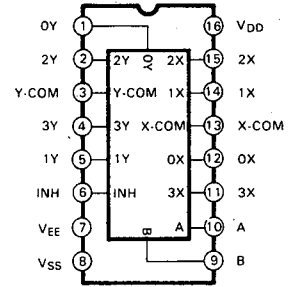
- **SN74HC273N (IR027350)**
Octal D-Type Flip-Flop



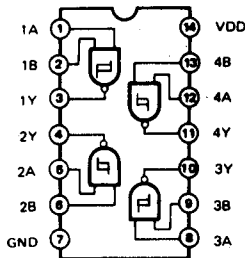
- **TC74HC4051AF (XJ623A00)**
Single 8-ch. Multiplexer/Demultiplexer



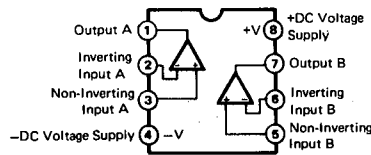
- **TC74HC4052AP (IR405200)**
Differential 4-Channel Multiplexer/Demultiplexer



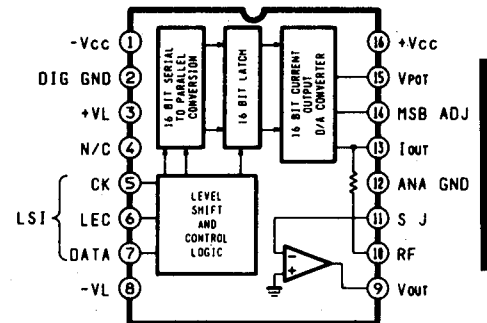
- **TC4093BP (IG043300)**
Quad 2-Input NAND Schmitt Trigger



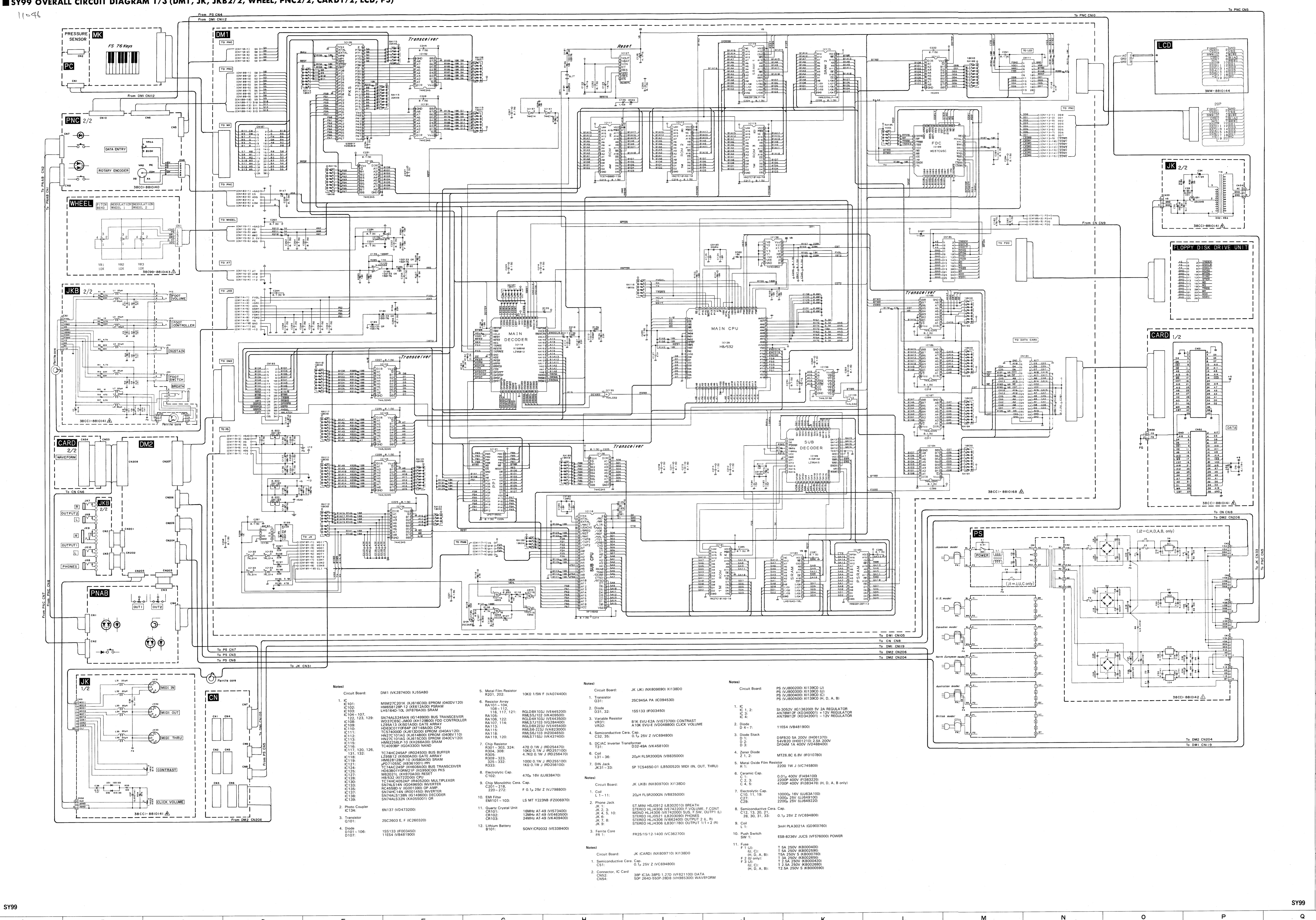
- **M5238P R610 (XJ748A00)**
Dual Operational Amplifier



- **PCM56P-Y (XH690A00)**
Digital Analog Converter



SY99



- Notes:**
- 1. DM1 (VX287400) XJ25000
 - 2. DM1 (VX287400) XJ25000
 - 3. DM1 (VX287400) XJ25000
 - 4. DM1 (VX287400) XJ25000
 - 5. DM1 (VX287400) XJ25000
 - 6. DM1 (VX287400) XJ25000
 - 7. DM1 (VX287400) XJ25000
 - 8. DM1 (VX287400) XJ25000
 - 9. DM1 (VX287400) XJ25000
 - 10. DM1 (VX287400) XJ25000
 - 11. DM1 (VX287400) XJ25000
 - 12. DM1 (VX287400) XJ25000

- Notes:**
- 1. JKB2 (VX287400) XJ25000
 - 2. JKB2 (VX287400) XJ25000
 - 3. JKB2 (VX287400) XJ25000
 - 4. JKB2 (VX287400) XJ25000
 - 5. JKB2 (VX287400) XJ25000
 - 6. JKB2 (VX287400) XJ25000
 - 7. JKB2 (VX287400) XJ25000
 - 8. JKB2 (VX287400) XJ25000
 - 9. JKB2 (VX287400) XJ25000
 - 10. JKB2 (VX287400) XJ25000
 - 11. JKB2 (VX287400) XJ25000
 - 12. JKB2 (VX287400) XJ25000

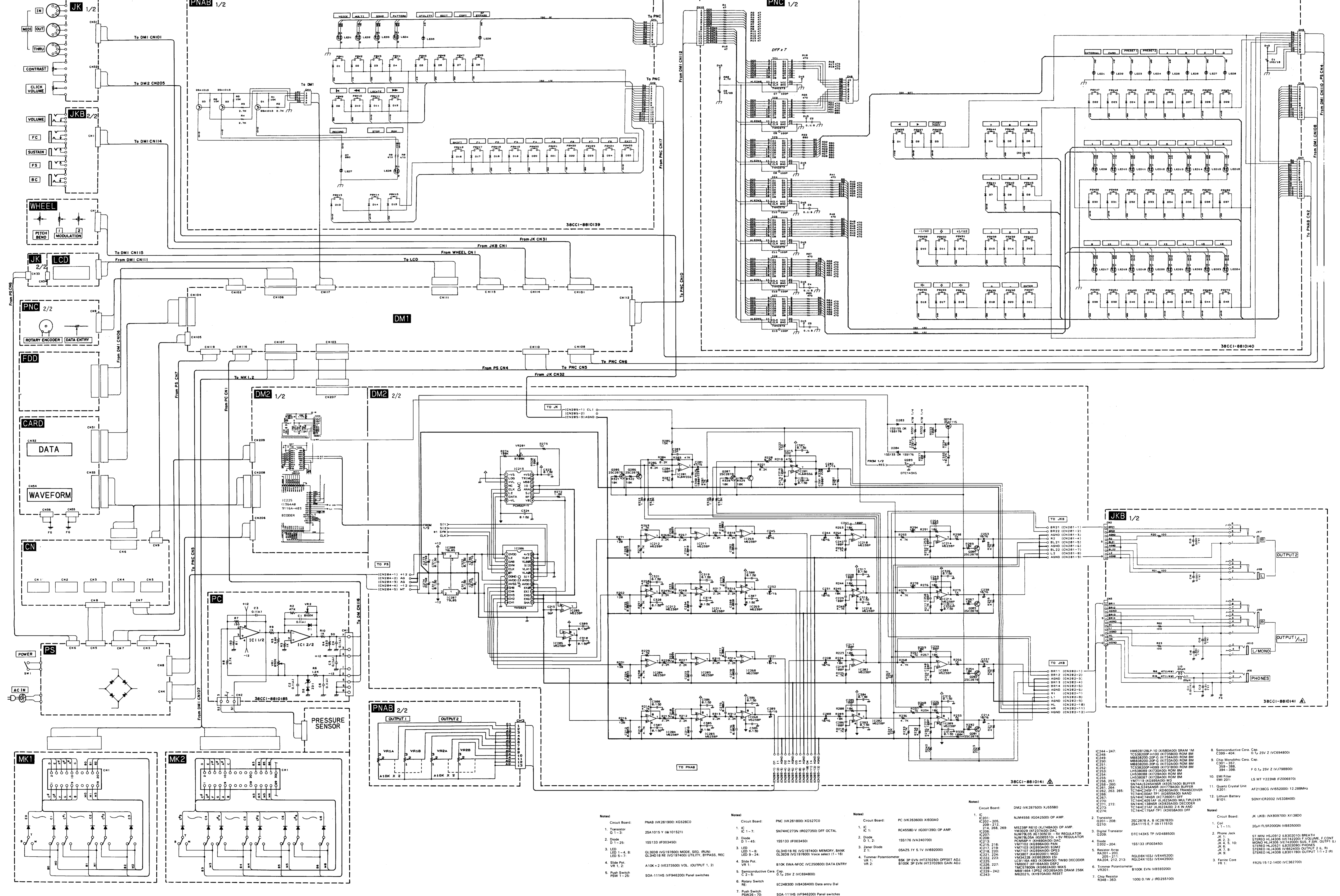
- Notes:**
- 1. PNC2 (VX287400) XJ25000
 - 2. PNC2 (VX287400) XJ25000
 - 3. PNC2 (VX287400) XJ25000
 - 4. PNC2 (VX287400) XJ25000
 - 5. PNC2 (VX287400) XJ25000
 - 6. PNC2 (VX287400) XJ25000
 - 7. PNC2 (VX287400) XJ25000
 - 8. PNC2 (VX287400) XJ25000
 - 9. PNC2 (VX287400) XJ25000
 - 10. PNC2 (VX287400) XJ25000
 - 11. PNC2 (VX287400) XJ25000
 - 12. PNC2 (VX287400) XJ25000

- Notes:**
- 1. WHEEL (VX287400) XJ25000
 - 2. WHEEL (VX287400) XJ25000
 - 3. WHEEL (VX287400) XJ25000
 - 4. WHEEL (VX287400) XJ25000
 - 5. WHEEL (VX287400) XJ25000
 - 6. WHEEL (VX287400) XJ25000
 - 7. WHEEL (VX287400) XJ25000
 - 8. WHEEL (VX287400) XJ25000
 - 9. WHEEL (VX287400) XJ25000
 - 10. WHEEL (VX287400) XJ25000
 - 11. WHEEL (VX287400) XJ25000
 - 12. WHEEL (VX287400) XJ25000

- Notes:**
- 1. CARD1 (VX287400) XJ25000
 - 2. CARD1 (VX287400) XJ25000
 - 3. CARD1 (VX287400) XJ25000
 - 4. CARD1 (VX287400) XJ25000
 - 5. CARD1 (VX287400) XJ25000
 - 6. CARD1 (VX287400) XJ25000
 - 7. CARD1 (VX287400) XJ25000
 - 8. CARD1 (VX287400) XJ25000
 - 9. CARD1 (VX287400) XJ25000
 - 10. CARD1 (VX287400) XJ25000
 - 11. CARD1 (VX287400) XJ25000
 - 12. CARD1 (VX287400) XJ25000

- Notes:**
- 1. LCD (VX287400) XJ25000
 - 2. LCD (VX287400) XJ25000
 - 3. LCD (VX287400) XJ25000
 - 4. LCD (VX287400) XJ25000
 - 5. LCD (VX287400) XJ25000
 - 6. LCD (VX287400) XJ25000
 - 7. LCD (VX287400) XJ25000
 - 8. LCD (VX287400) XJ25000
 - 9. LCD (VX287400) XJ25000
 - 10. LCD (VX287400) XJ25000
 - 11. LCD (VX287400) XJ25000
 - 12. LCD (VX287400) XJ25000

SY99 OVERALL CIRCUIT DIAGRAM 3/3 (DM2 2/2, PNAB, PNC1/2, PC, JKB 1/2, MK1, MK2)



MUSIC SYNTHESIZER

SY99

PARTS LIST

■CONTENTS (目次)

ELECTRICAL PARTS (電気部品)	1
OVERALL ASSEMBLY (総組立)	5
UPPER CASE ASSEMBLY (上ケースAss'y)	7
WHEEL ASSEMBLY (ホイールAss'y)	9
KEYBOARD ASSEMBLY (鍵盤Ass'y)	10
POWER SUPPLY ASSEMBLY (電源Ass'y)	11
FDD ASSEMBLY (FDD Ass'y)	12
DISPLAY ASSEMBLY (ディスプレイAss'y)	13
RAM BOARD (SY拡張メモリーボード) SYEMBO5	14

Note) DESTINATION ABBREVIATIONS

J : Japanese model	A : Australian model
U : U.S. model	E : European model
C : Canadian model	D : German model
X : General model	B : British model
M : South African model	I : Indonesian model
H : North European model	

ELECTRICAL PARTS (電気部品)

Ref. No.	Part No.	Description	部品名	Remarks	ランク	
	VK287400	Circuit Board	DM1	DM1シート	SY99	66
	VK287500	Circuit Board	DM2	DM2シート		90
	VK261900	Circuit Board	PNAB	PNABシート		19
	VK261800	Circuit Board	PNC	PNCシート		23
	VJ875400	Circuit Board	JK	JKシート		
	NX809690	Circuit Board	JK(JK)	JK(JK)シート		
	NX809700	Circuit Board	JK(JKB)	JK(JKB)シート		
	NX809710	Circuit Board	JK(CARD)	JK(CARD)シート		
	VK666500	Circuit Board	CN	CNシート		
	NA810850	Circuit Board	MK1	MK1シート		07
	NA810860	Circuit Board	MK2	MK2シート		08
	VK263600	Circuit Board	PC	PCシート		08
	VJ800200	Circuit Board	PS	PSシート	J	19
	VJ800300	Circuit Board	PS	PSシート	U	20
	VJ800400	Circuit Board	PS	PSシート	C	20
	VJ800500	Circuit Board	PS	PSシート	H,D,A,B	20
	VK287400	Circuit Board	DM1	DM1シート		66
	IG001390	IC	RC4558D-V	IC	OP AMP	03
	XH970A00	IC	M62021L	IC	RESET	04
	IG043300	IC	TC4093BP	IC	NAND	05
	IG049650	IC	SN74LS14N	IC	INVERTER	05
	IR001450	IC	SN74HC14N	IC	INVERTER	05
	XA055001	IC	SN74ALS32N	IC	OR	03
	IG149600	IC	SN74ALS138N	IC	DECODER	04
	IG149900	IC	SN74ALS245AN	IC	BUS TRANSCEIVER	07
	IR024500	IC	TC74HC245AP	IC	BUS BUFFER	07
	XH608A00	IC	TC74AC245P	IC	BUS TRANSCEIVER	05
	IR405200	IC	TC74HC4052AP	IC	MULTIPLEXER	03
	XI722D00	IC	H8/532	IC	CPU	
	XF148A00	IC	HD63C01Y0F64P	IC	CPU	09
	XG950C00	IC	HD63B01Y0RM21P	IC	PKS	08
	XB361001	IC	μ PD71055C	IC	PPI	06
	XH129B00	IC	WD37C65C-JM00	IC	FDD CONTROLLER	15
	XI500A00	IC	LZ95B12	IC	GATE ARRAY	05
	XI501A00	IC	LZ95A13	IC	GATE ARRAY	05
	XE612A00	IC	HM658128P-12	IC	PSRAM	17
	XF876A00	IC	LH5164D-10L	IC	SRAM	08
	XH266A00	IC	HM62256LP-10	IC	SRAM	13
	XI580A00	IC	HM628128LP-10	IC	SRAM	22
	XJ613D00	IC	TC574000D	IC	EPROM(040AV120)	
	XJ614B00	IC	HN27C101AG	IC	EPROM(040BV110)	
	XJ615C00	IC	HN27C101AG	IC	EPROM(040CV120)	
	XJ616C00	IC	M5M27C201K	IC	EPROM(040DV120)	
	VD473200	Photo Coupler	6N137	フォトカプラ		05
	IC260320	Transistor	2SC2603 E,F	トランジスタ		01
	IF003450	Diode	1SS133	ダイオード		01
	VB481900	Diode	11ES4	ダイオード		01
	VA074400	Metal Film Resistor	10KΩ 1/5W F	金属皮膜抵抗		01
	HZ004650	Resistor Array	RMLS6J103	抵抗アレイ		02
	VA823000	Resistor Array	RMLS6-223J	抵抗アレイ		01
	VE443500	Resistor Array	RGLD4X103J	抵抗アレイ		01
	VE445200	Resistor Array	RGLD8X103J	抵抗アレイ		01
	VE445400	Resistor Array	RGLD8X223J	抵抗アレイ		01
	VG284400	Resistor Array	RMLS7J103	抵抗アレイ		01
	VK409500	Resistor Array	RMLS5J102	抵抗アレイ		01
	VK437400	Resistor Array	RMLS7153J	抵抗アレイ		01
	RD254470	Chip Resistor	47Ω 0.1W J	チップ抵抗		01
	RD255100	Chip Resistor	100Ω 0.1W J	チップ抵抗		01
	RD256100	Chip Resistor	1KΩ 0.1W J	チップ抵抗		01
	RD256470	Chip Resistor	4.7KΩ 0.1W J	チップ抵抗		01
	RD257100	Chip Resistor	10KΩ 0.1W J	チップ抵抗		01
	UJ838470	Electrolytic Cap.	470μ 16V	チケミコン		01
	VJ798800	Chip Monolithic Cera. Cap.	F 0.1μ 25V Z	チップ積層セラコン		01
	FZ006970	EMI Filter	LS MT Y223NB	LCフィルタ EMI		02
	VE463500	Quartz Crystal Unit	12MHz AT-49	水晶振動子		03
	VI573400	Quartz Crystal Unit	16MHz AT-49	水晶振動子		03
	VK409400	Quartz Crystal Unit	24MHz AT-49	水晶振動子		03
	VE338400	Lithium Battery	SONY/CR2032	リチウム電池		03
	VH930600	Metal Fitting, Earth		アース金	(3pcs)	01
	VK287500	Circuit Board	DM2	DM2シート		90
	IG042500	IC	NJM4556	IC	OP AMP.	04
	XJ748A00	IC	M5238P R610	IC	OP AMP.	03
	IG065510	IC	NJM78L05A	IC	+5V REGULATOR	03
	IG130500	IC	NJM79L05	IC	-5V REGULATOR	03
	XH970A00	IC	M62021L	IC	RESET	04
	XD655A00	IC	TC74HC00AF-TP1	IC	NAND	01
	XJ622A00	IC	TC74HC21AF	IC	2-4 IN AND	01

*New Parts (新規部品)

ランク : Japan only

Ref. No.	Part No.	Description	部品名	Remarks	ランク	
	XC726001	IC	SN74HC74NSR	I C	DDF	03
	XD835A00	IC	SN74HC138NSR	I C	DECODER	02
	XD658A00	IC	TC74HC175AF-TP1	I C	DDF	02
	XD603A00	IC	TC74HC245F-T1	I C	TRANSCEIVER	04
	XE057A00	IC	SN74ALS245ANSR	I C	BUFFER	05
	XH779A00	IC	SN74LS245ANSR	I C	BUFFER	03
	XJ623A00	IC	TC74HC4051AF	I C	MULTIPLEXER	02
	XG662A00	IC	TMC57800N	I C	MIX5	07
	XH690A00	IC	PCM56P-Y	I C	DAC	07
	XI364A00	IC	LC9116A-483	I C	TW80 DECODER	07
	XF237A00	IC	YM3029	I C	DAC	09
	XE862B00	IC	YM3422B	I C	ESI	05
	XA902001	IC	YM3807	I C	WOD	09
	XF164A00	IC	YM6007	I C	DSP2	18
	XG996A00	IC	YM7102	I C	PAN	10
	XG993A00	IC	YM7103	I C	EGM2	13
	XG994A00	IC	YM7107	I C	OPS3	13
	XG995A00	IC	YM7119	I C	M3	18
	XD265A00	IC	MB81464-12PSZ	I C	DRAM 256K	08
	XI580A00	IC	HM628128LP-10	I C	SRAM 1M	22
	XI728A00	IC	LH538087	I C	ROM 8M	13
	XI729A00	IC	LH538088	I C	ROM 8M	13
	XI730A00	IC	LH538089	I C	ROM 8M	13
	XI731B00	IC	TC538200P-H099	I C	ROM 8M	13
	XI732A00	IC	MB838200-20P-G	I C	ROM 8M	13
	XI733A00	IC	MB838200-20P-G	I C	ROM 8M	13
	XI734A00	IC	MB838200-20P-G	I C	ROM 8M	13
	XI735B00	IC	TC538200P-H100	I C	ROM 8M	13
	IA111510	Transistor	2SA1115 E,F	トランジスタ		01
	IC287820	Transistor	2SC2878 A,B	トランジスタ		01
	VD488500	Digital Transistor	DTC143XS TP	デジタルトランジスタ		03
	IF003450	Diode	1SS133	ダイオード		01
	VE443500	Resistor Array	RGLD4X103J	抵抗アレイ		01
	VE445200	Resistor Array	RGLD8X103J	抵抗アレイ		01
	RD255100	Chip Resistor	100Ω 0.1W J	チップ抵抗		01
	VB593200	Trimmer Potentiometer	B100K EVN	半導体可変抵抗		01
	VC694800	Semiconductive Cera. Cap.	0.1μ 25V Z	半導体セラコン		01
	VJ798800	Chip Monolithic Cera. Cap.	F 0.1μ 25V Z	チップセラコン		01
	FZ006970	EMI Filter	LS MT Y223NB	水晶体フィルタ		02
	VI552000	Quartz Crystal Unit	AF2138CG	水晶振動子	12.288MHz	03
	VE338400	Lithium Battery	SONY/CR2032	リチウム電池		03
	VH930600	Metal Fitting, Earth		アース金具	(3pcs)	01
	VK261900	Circuit Board	PNAB	P N A B シート		19
	IA101521	Transistor	2SA1015 Y	トランジスタ		01
	IF003450	Diode	1SS133	ダイオード		01
	VG197400	LED	GL3HD18 RE	LED	UTI, BYPASS, RECO	01
	VG197600	LED	GL3ED8	2色LED	MODE, RUN (5pcs)	01
	VH812000	LED Spacer	× 8	LEDスペーサー	(LED 1-6)	02
	VH812100	LED Spacer	× 4	LEDスペーサー	(LED 7, 8)	01
	VE373500	Slide Pot.	A10K × 2	二連スライドボリューム	VOL. (OUTPUT 1, 2)	03
	VF946200	Push Switch	SOA-111HS	プッシュスイッチ	(25pcs)	01
	VK261800	Circuit Board	PNC	P N C シート		23
	IR027350	IC	SN74HC273N	I C	DDF OCTAL	05
	IF003450	Diode	1SS133	ダイオード		01
	VG197400	LED	GL3HD18 RE	LED	MEMORY, BANK (8p	01
	VG197600	LED	GL3ED8	2色LED	voice sel. 1-16)	01
	VH812000	LED Spacer	× 8	LEDスペーサー	(3pcs)	02
	VC250600	Slide Pot.	B10K EWA-NFOC	スライドボリューム	DATA ENTRY	03
	VC694800	Semiconductive Cera. Cap.	0.1μ 25V Z	半導体セラコン		01
	VB436400	Rotary Switch	EC24B30D	ロータリースイッチ	Data entry Dial	07
	VF946200	Push Switch	SOA-111HS	プッシュスイッチ	(45pcs)	01
	VJ875400	Circuit Board	JK	J K シート		
	NX809690	Circuit Board	JK (JK)	J K (J K) シート		
	NX809700	Circuit Board	JK (JKB)	J K (J K B) シート		
	NX809710	Circuit Board	JK (CARD)	J K (C A R D) シート		
	IC094530	Transistor	2SC945A PA	トランジスタ		01
	IF003450	Diode	1SS133	ダイオード		01
	VD048800	Variable Resistor	A10K EVU-E	ロータリーボリューム	CLICK VOLUME	02
	VI573700	Variable Resistor	B1K EVU-E2A	ロータリーボリューム	CONTRAST	02
	VC694800	Semiconductive Cera. Cap.	0.1μ 25V Z	半導体セラコン		01
	VK458100	DC/AC Inverter Transformer	D32-49A	D C / A C インバータトランス		07
	VB835000	Coil	20μ H FL5R200QN	コイル		01
	LB203090	Phone Jack	STEREO HLJ0521	ホンジャック	PHONES	02
	LB301780	Phone Jack	STEREO HLJ4308	ホンジャック	OUTPUT1/1+2 (R)	03
	LB302010	Phone Jack	ST. MINI HSJ0912	ホンジャック	BREATH	02
	VE742000	Phone Jack	MONO HLJ4306	ホンジャック	SUS, F. SW. OUT1 (L)	02

*New Parts (新規部品)

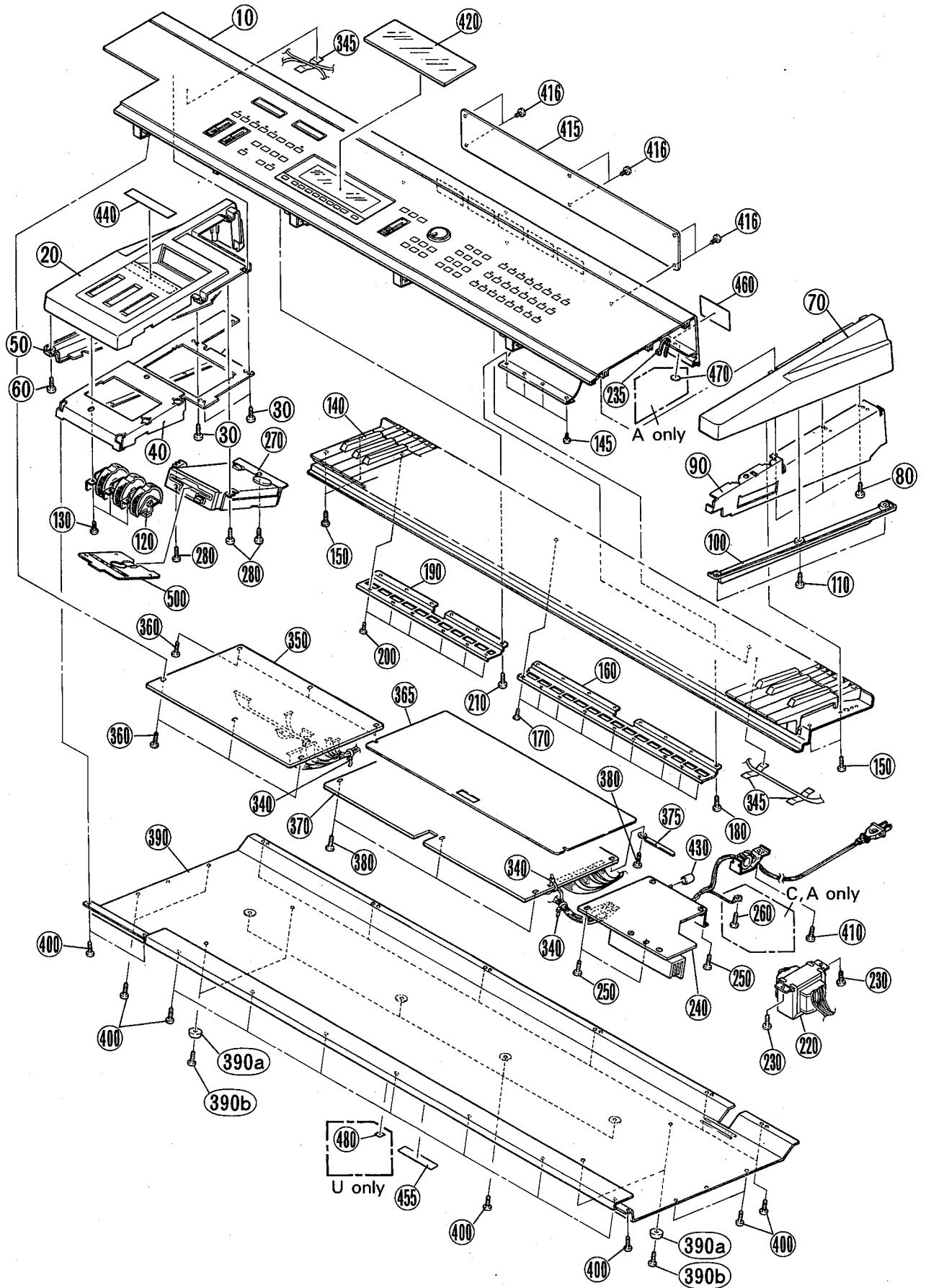
ランク: Japan only

Ref. No.	Part No.	Description	部品名	Remarks	ランク	
	VE742200	Phone Jack	STEREO HLJ4306	ホーンジャック	F.VOLUME,F.CONT	02
	VI662400	Phone Jack	STEREO. HLJ4306	ホーンジャック	OUTPUT2 (L/R)	02
	LB500520	DIN Jack	5P TCS4650-01	DINジャック	MIDI(IN,OUT,THR)	03
	VF821100	Connector, IC Card	IC3A-38PS-1.27D	ICカード用コネクタ	38P DATA	06
	VH985300	Connector, IC Card	264D-550P-28D8	ICカード用コネクタ	50P WAVEFORM	08
	VC362700	Ferrite Core	FR25/15/12-1400	フェライトコア	2pcs	04
	CB069250	Cord Clamper	BK-1	束線止め	3pcs	01
	AA833840	Angle Bracket, C.B.		シートアングル		01
	VH812200	Angle Bracket-H, Jack		J Kアングル(H)		03
	VH812300	Angle Bracket-M, Jack		J Kアングル(M)		02
	ED040066	Bind Head Screw	4.0×6 ZMC2Y	バインド小ネジ		01
*	VK666500	Circuit Board	CN	CNシート		
	IR013800	IC	TC74HC138AP	IC	DECODER-8	05
	VF169200	Resistor Array	RMLS5 J 473	抵抗アレイ		01
	VC694800	Semiconductive Cera. Cap.	0.1μ 25V Z	半導体セラコン		01
	VD542700	EMI Filter	DSS306-93F223Z1	LCフィルタ EMI		01
	LB903400	Connector HIF	40P SE	HIFコネクタ		07
	VL347400	Connector FX2	52P SE	FX2コネクタ	EXPANSION M.(5p)	04
	NA810850	Circuit Board	MK1	MK1シート		07
	IF003450	Diode	1SS133	ダイオード		01
	NA810860	Circuit Board	MK2	MK2シート		08
	IF003450	Diode	1SS133	ダイオード		01
	LB920300	Connector	30P SE	コネクタ		06
	ED040066	Bind Head Screw	4.0×6 ZMC2Y	バインド小ネジ		01
	VK263600	Circuit Board	PC	PCシート		08
	IG001390	IC	RC4558D-V	IC	OP AMP.	03
	VA240700	Diode	1SS176	ダイオード		01
	VI692000	Zener Diode	05A Z5.1V 5.1V	ツェナーダイオード		01
	HT370250	Trimmer Potentiometer	B5K 3P EVN	半固定抵抗	OFFSET ADJ.	02
	HT370260	Trimmer Potentiometer	B100K 3P EVN	半固定抵抗	GAIN ADJ.	02
	VJ800200	Circuit Board	PS	PSシート	J	19
	VJ800300	Circuit Board	PS	PSシート	U	20
	VJ800400	Circuit Board	PS	PSシート	C	20
	VJ800500	Circuit Board	PS	PSシート	H,D,A,B	20
	IG136200	IC	SI-3052V	IC	5V, 2A REGULATOR	06
	XD340001	IC	AN78M12F	IC	+12V REGULATOR	03
	XD342001	IC	AN79M12F	IC	-12V REGULATOR	03
	VB481900	Diode	11ES4	ダイオード		01
	IH001210	Diode Stack	S4VB20	ダイオードスタック	2.6A 200V	04
	IH001370	Diode Stack	D5FB20 5A 200V	ダイオードスタック		05
	VD488400	Diode Stack	DF04M 1A 400V	ダイオードスタック		02
	IF010780	Zener Diode	MTZ6.8C 6.8V	ツェナーダイオード		01
	VC745800	Metal Oxide Film Resistor	220Ω 1W J	酸化金属皮膜抵抗		01
	FI383220	Ceramic Cap.	2200P 400V	規格認定コン		01
	FI383470	Ceramic Cap.	4700P 400V	規格認定コン	H,D,A,B only	01
	FI494100	Ceramic Cap.	0.01μ 400V	規格認定コン		01
	UJ63A100	Electrolytic Cap.	10000μ 16V	ケミコン		04
	UJ649100	Electrolytic Cap.	1000μ 25V	ケミコン		02
	UJ649220	Electrolytic Cap.	2200μ 25V	ケミコン		03
	VC694800	Semiconductive Cera. Cap.	0.1μ 25V Z	半導体セラコン		01
	GD900760	Coil	3mH PLA3021A	コイル		06
	VF576000	Push Switch	ESB-8236V JUCS	プッシュスイッチ	POWER	03
	KB000400	Fuse	T 5A 250V	ヒューズ	J	01
	KB000420	Fuse	T 2.5A 250V	ヒューズ	J	01
	KB002590	Fuse	T 5A 250V	ヒューズ	U,C	02
	KB002650	Fuse	T 3A 250V	ヒューズ	U	03
	KB002680	Fuse	T 2.5A 250V	ヒューズ	H,D,A,B	03
	KB000690	Fuse	T 2.5A 250V S	ヒューズ	U,C	02
	KB000780	Fuse	T5A 250V S	ヒューズ	H,D,A,B	02
	LB201530	Fuse Holder	PC-FH1	ヒューズホルダー	(4pcs, U:6pcs)	01
	VA855400	Terminal		PC用カラゲ端子	(2pcs)	01
	IL000680	Insulation Sheet	BFG-20	放熱シート	(2pcs)	01
	--	Heat Sink		放熱板		
	VK355400	Angle Bracket, Pw. Switch		P Sアングル		05
	E1030106	Bind Head Tapping Screw	3.0×10 ZMC2Y	バインドタッピングネジ	(6pcs)	01
	E1040106	Bind Head Tapping Screw	4.0×10 ZMC2Y	バインドタッピングネジ	(2pcs)	01
	VK372800	LCD Assembly		LCD Ass'y		
	VF931200	LCD	DMF5005NYL-EW	液晶ディスプレイ		28
	VK588200	Variable Resistor	10K RK1241110	ロータリーボリューム	PITCH BEND	03
	HS412160	Variable Resistor	10K K161100S	ロータリーボリューム	MODULATION 1	03
	VI666700	Variable Resistor	10K RK1631110	ロータリーボリューム	MODULATION 2	03

Ref. No.	Part No.	Description	部 品 名	Remarks	ランク
	VK423800	Floppy Disk Driver D357B 045600	フロッピーディスクドライブ	3.5"	24
	XI246A00	Power Transformer	電源トランス	J	12
	XI247A00	Power Transformer	電源トランス	U, C	12
	XJ576A00	Power Transformer	電源トランス	H, D, A, B	12
	VD279200	AC Cord 7A 2.5m	電源コード	J	04
	VD279400	AC Cord 10A 2.5m	電源コード	U	08
	VD279500	AC Cord 10A 2.5m	電源コード	C	07
	VD280400	AC Cord 2.5A 2.5m	電源コード	H, D	06
	VD279700	AC Cord 7.5A 2.5m	電源コード	A	06
	VH890400	AC Cord 6A 2.5m	電源コード	B	08

*New Parts (新規部品)

OVERALL ASSEMBLY (総組立)

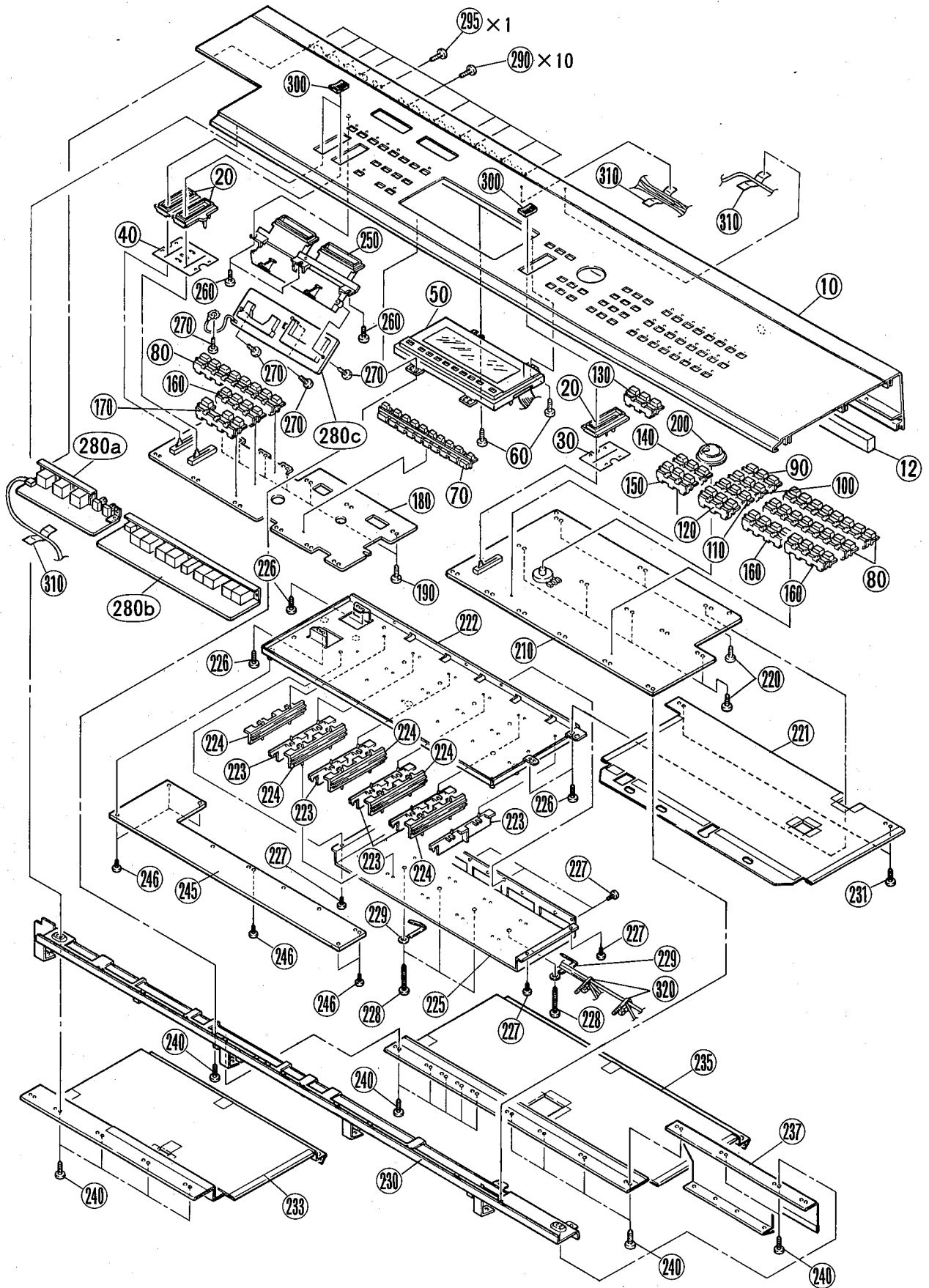


SY99

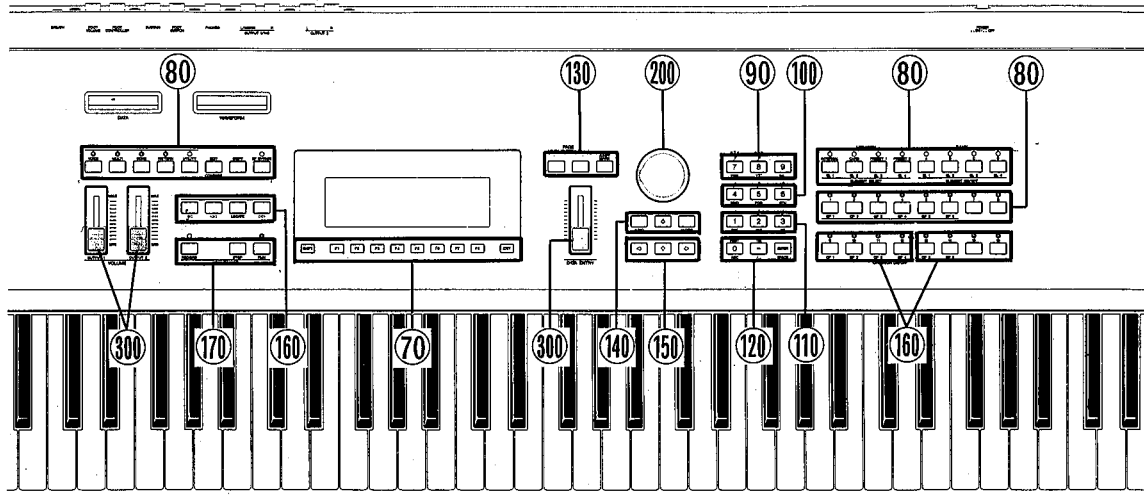
Ref. No.	Part No.	Description	部品名	Remarks	ランク
10	--	Overall Assembly	総組立	SY99	
20	VH809500	Upper Case Assembly	上ケース Ass'y		
30	VJ254100	End Block	拍子木 (左)		09
40	VH809700	Bonding Tapping Screw	ボンディングタッピングネジ	(4pcs)	01
50	VH809900	Shield Plate	シールド板 (左)		07
60	VJ254100	Side Board	側板 (左)		04
70	VH809200	Bonding Tapping Screw	ボンディングタッピングネジ	(3pcs)	01
80	VJ254100	End Block	拍子木 (右)		07
90	VH325400	Bonding Tapping Screw	ボンディングタッピングネジ	(3pcs)	01
100	VH809800	Shield Plate	シールド板 (右)		05
110	VJ254100	Side Board	側板 (右)		04
120	VK350400	Bonding Tapping Screw	ボンディングタッピングネジ	(3pcs)	01
130	EZ000460	Wheel Assembly	ホイール Ass'y		12
140	EZ030066	Bonding Tapping Screw	ボンディングタッピングネジ	(6pcs)	01
145	ED030066	Keyboard Assembly	鍵盤 Ass'y		52
150	EI340166	Bind Head Screw	バインド小ネジ	(4pcs)	01
160	VK350600	Bind Head Tapping Screw	バインドタッピングネジ	(4pcs)	01
170	ED030066	Angle Bracket-B, Earth	アースアングル (B)		06
180	EI040106	Bind Head Screw	バインド小ネジ	(9pcs)	01
190	VK350700	Bind Head Tapping Screw	バインドタッピングネジ	(8pcs)	01
200	ED030066	Angle Bracket-C, Earth	アースアングル (C)		05
210	EI040106	Bind Head Screw	バインド小ネジ	(6pcs)	01
220	XI246A00	Power Transformer	電源トランス	(6pcs)	01
220	XI247A00	Power Transformer	電源トランス	J	12
220	XI247A00	Power Transformer	電源トランス	U,C	12
230	EI040106	Power Transformer	電源トランス	H,D,A,B	12
235	CB069250	Bind Head Tapping Screw	バインドタッピングネジ	(2pcs)	01
240	--	Cord Clamper	束線止め		01
250	EI040106	Power Supply Assembly	電源 Ass'y		
260	EI040106	Bind Head Tapping Screw	バインドタッピングネジ	(4pcs)	01
270	--	Bind Head Tapping Screw	バインドタッピングネジ		
280	EI040106	FDD Assembly	FDD Ass'y		
340	CB069250	Bind Head Tapping Screw	バインドタッピングネジ	(3pcs)	01
345	--	Cord Clamper	束線止め	(6pcs)	01
350	VK287400	Adhesive Tape	粘着テープ	(5pcs)	
350	VK287400	Circuit Board	DM1 シート		66
360	EI040106	Bind Head Tapping Screw	バインドタッピングネジ	(6pcs)	01
365	--	Isolation Sheet	絶縁シート		
370	VK287500	Circuit Board	DM2 シート		90
375	CB040540	Cord Clamper	束線止め		01
380	EI040106	Bind Head Tapping Screw	バインドタッピングネジ	(6pcs)	01
390	VK351700	Bottom Cover Assembly	底板 Ass'y		19
390a	CB801270	Foot	ゴム足	(4pcs)	01
390b	ED340106	Bind Head Screw	バインド小ネジ	(4pcs)	01
400	VJ254100	Bonding Tapping Screw	ボンディングタッピングネジ	(24pcs)	01
410	VJ254100	Bonding Tapping Screw	ボンディングタッピングネジ		01
415	VL413300	Name Plate	ネームプレート		11
416	ED340066	Bind Head Screw	バインド小ネジ	(6pcs)	01
420	VH811600	LCD Filter	保護板		06
430	CB825380	Push Button	プッシュボタン	POWER	01
440	--	Caution Label	注意書		
455	--	Exciter Label	エキサイターラベル		
460	--	Name Label	銘板		
470	--	Earth Mark	アースマーク	A only	
480	--	Graphic Mark	グラフィックマーク	U only	
500	--	Head Protection Sheet	ダミーディスク		
	--	* ACCESSORIES	* 付属品		
	--	Plug Cover	プラグカバー		
	XI692A00	Floppy Disk	書込済み F D / B	J, A	
	XI693A00	Floppy Disk	書込済み F D / P	U, C	
	XI694A00	Floppy Disk	書込済み F D / G	H, D, B	
	--	Floppy Disk	書込済み F D	(Chick Korea)	

SY99

■ UPPER CASE ASSEMBLY (上ケースAss'y)



SY99



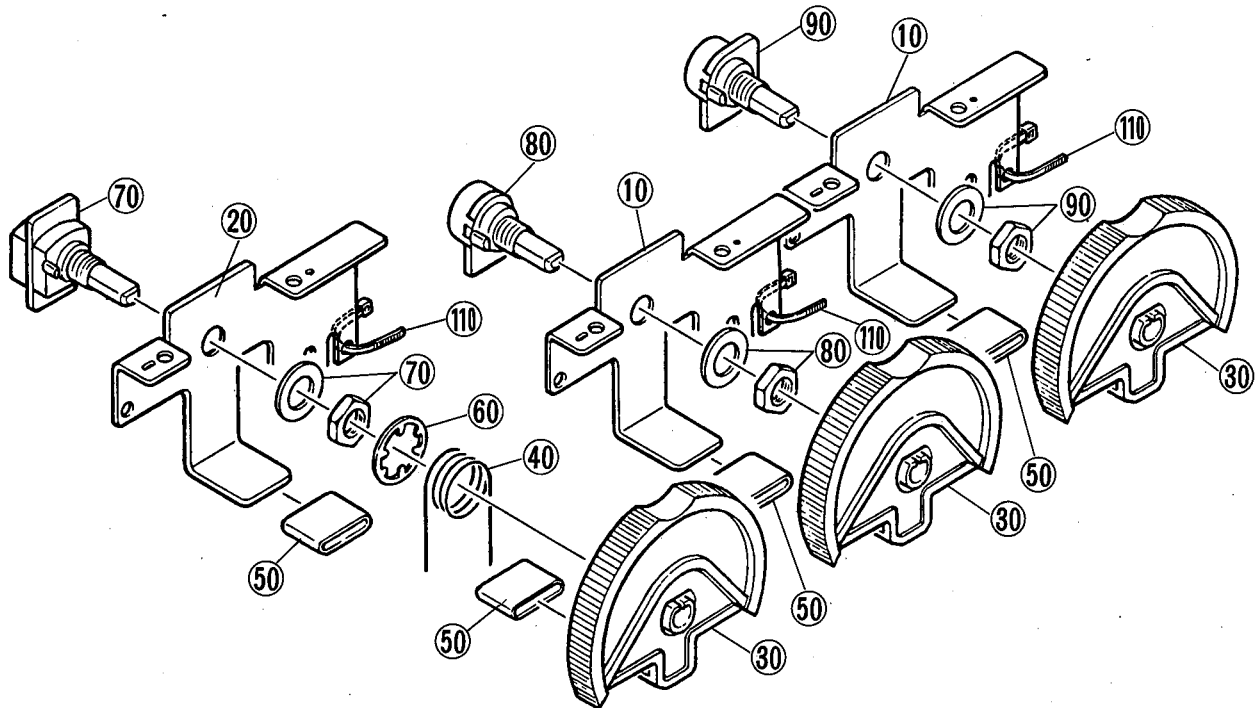
Ref No.	Part No.	Description	部品名	Remarks	ランク	
10	VK352700	Upper Case Assembly	上ケース Ass'y	SY99	30	
12	VL413600	Control Panel	コントロールパネル		05	
20	VH810000	Spacer	スペーサー	(3pcs)	02	
30	VH810100	Escutcheon, Slide Volume	スライトボリュームスキャッション		01	
40	VH810200	Dust Proof Cloth-1	防塵クロス(1)		01	
50	--	Dust Proof Cloth-2	防塵クロス(2)		01	
60	EJ040106	Display Assembly	ディスプレイ Ass'y	(2pcs)	01	
70	EJ040106	Bind Head Tapping Screw	ハインドタッピングネジ		03	
80	VH811500	Function Key	ファンクションキー	(3pcs)	02	
90	VH810400	Knob-A	ノブ A (8連)		02	
100	VI524400	Knob-B	ノブ B (3連)	VWX, YZ', * &	02	
110	VI524500	Knob-C	ノブ C (3連)	MNO, PQR, STU	02	
120	VI524600	Knob-D	ノブ D (3連)	DEF, GHI, JKL	02	
130	VI524700	Knob-E	ノブ E (3連)	ABC, /., .SPACE	02	
140	VI524300	Knob-F	ノブ F (3連)	<, >, JUMP/MARK	01	
150	VI534600	Knob-G	ノブ G (3連)	-1/NO, +1/YES	03	
160	VI537200	Knob-H	ノブ H (3連)		03	
170	VH810500	Knob-I	ノブ I (3連)		01	
180	VH810600	Knob-J	ノブ J (4連)	(3pcs)	01	
190	VH810800	Knob-K	ノブ K (飛3連)		01	
200	VK261900	Circuit Board	PNAB	PNA Bシート	19	
210	EJ040106	Bind Head Tapping Screw	ハインドタッピングネジ	(6pcs)	01	
220	VI250800	Rotary Knob	ロータリーツマミ	Data entry Dial	01	
221	VK261800	Circuit Board	PNC	PNCシート	23	
222	EJ040106	Bind Head Tapping Screw	ハインドタッピングネジ	(6pcs)	01	
223	--	Shield Sheet PNC	PNCシールドシート		09	
224	VL413100	Slot Unit Base	ユニットベース		03	
225	VL413400	Rail-A	レール(A)	(5pcs)	03	
226	VL413500	Rail-B	レール(B)	(5pcs)	06	
227	VL413200	Slot Unit Cover	ユニットカバー		01	
228	EJ040106	Bind Head Tapping Screw	ハインドタッピングネジ	(4pcs)	01	
229	ED030066	Bind Head Screw	バインド小ネジ	(8pcs)	01	
230	ED340256	Bind Head Screw	バインド小ネジ	(4pcs)	01	
231	CB817510	Cord Clamper	S-14B	束線止め	(2pcs)	01
232	VK353100	Angle Bracket	センターアングル		10	
233	EJ040106	Bind Head Tapping Screw	ハインドタッピングネジ	(2pcs)	01	
234	--	Shield Sheet DM1	DM1シールドシート			
235	--	Shield Sheet DM2	DM2シールドシート			
236	--	Shield Sheet MK	MKシールドシート			
237	EJ040106	Bind Head Tapping Screw	ハインドタッピングネジ	(16pcs)	01	
238	VK666500	Circuit Board	CN	CNシート		
239	ED030066	Bind Head Screw	バインド小ネジ	(6pcs)	01	
240	VH812800	Card Guide	カードガイド		06	
241	EJ040106	Bind Head Tapping Screw	ハインドタッピングネジ	(3pcs)	01	
242	EJ040106	Bind Head Tapping Screw	ハインドタッピングネジ	(3pcs)	01	
243	VJ875400	Circuit Board	JK	JKシート		
244a	NX809690	Circuit Board	JK(JK)	JK(JK)シート		
244b	NX809700	Circuit Board	JK(JKB)	JK(JKB)シート		
244c	NX809710	Circuit Board	JK(CARD)	JK(CARD)シート		
245	VJ254100	Bonding Tapping Screw	ボンディングタッピングネジ	(10pcs)	01	
246	ED340196	Bind Head Screw	バインド小ネジ	(1pcs)	01	
247	VB774000	Knob	ツマミ	VOL, D. ENTRY(3pc)	01	
248	--	Adhesive Tape	粘着テープ	(3pcs)	01	
249	CR069250	Cord Clamper	BK-1	束線止め	(3pcs)	

*New Parts (新規部品)

ランク : Japan only

SY99

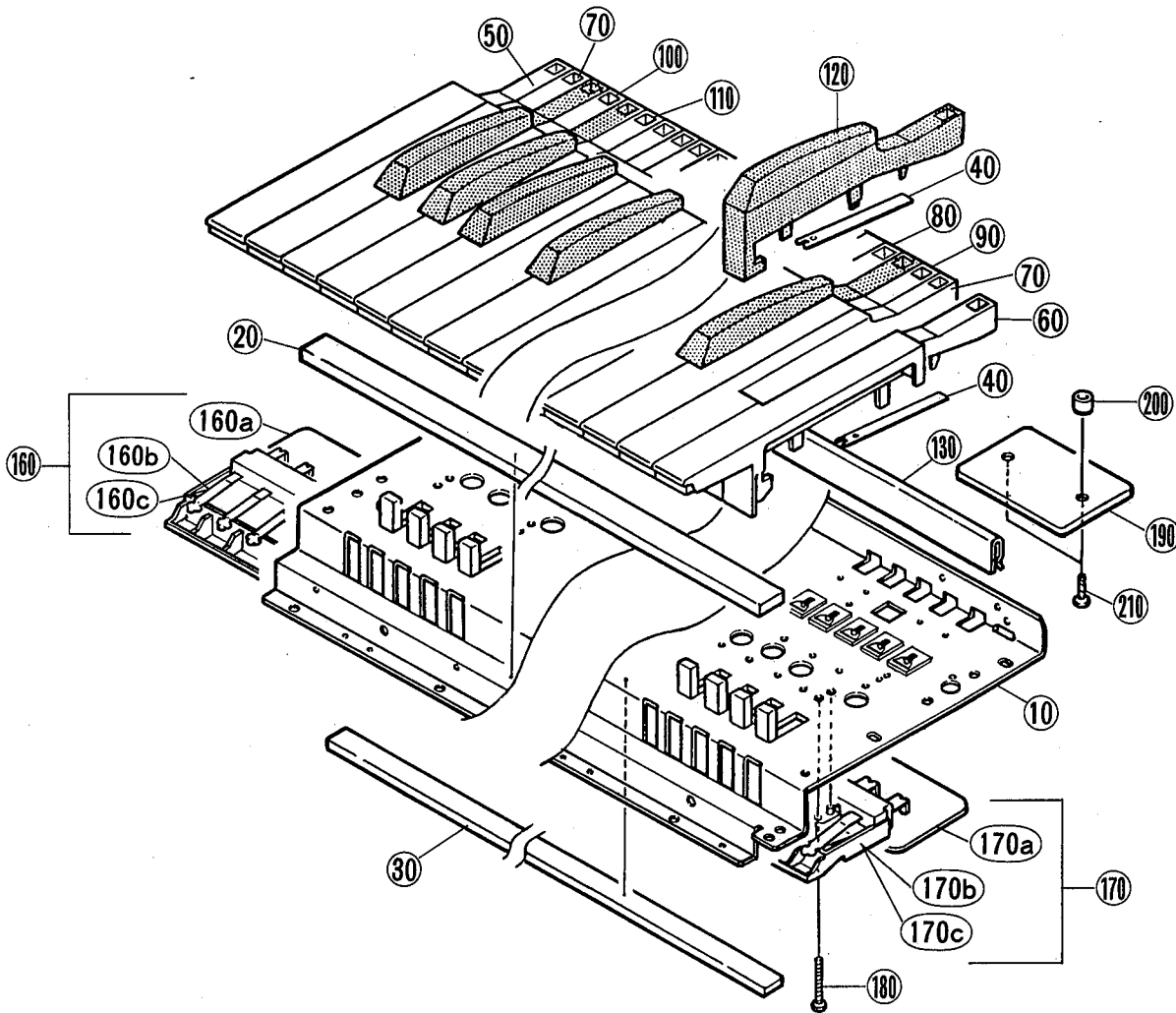
■ WHEEL ASSEMBLY (ホイール Ass'y)



Ref. No.	Part No.	Description	部品名	Remarks	ランク
		Wheel Assembly	ホイール Ass'y	SY99	
10	VF536800	Frame	フレーム	MOD. 1,2 (2pcs)	01
20	VJ187600	Frame-A	フレーム(A)	PITCH BEND	02
30	VF537400	Wheel	ホイール	(3pcs)	02
40	VC792800	Spring	リターン スプリング	PITCH BEND	01
50	CB819020	Wheel Tube	ホイールチューブ	(4pcs)	02
60	EW600110	Wheel Ring	CS形止め輪	PITCH BEND	01
70	VK588200	Variable Resistor	ロータリーボリューム	PITCH BEND	03
80	HS412160	Variable Resistor	ロータリーボリューム	MODULATION 1	03
90	VI666700	Variable Resistor	ロータリーボリューム	MODULATION 2	03
110	CB069250	Cord Clammer	束線止め	(3pcs)	01

SY99

KEYBOARD ASSEMBLY (鍵盤 Ass'y)

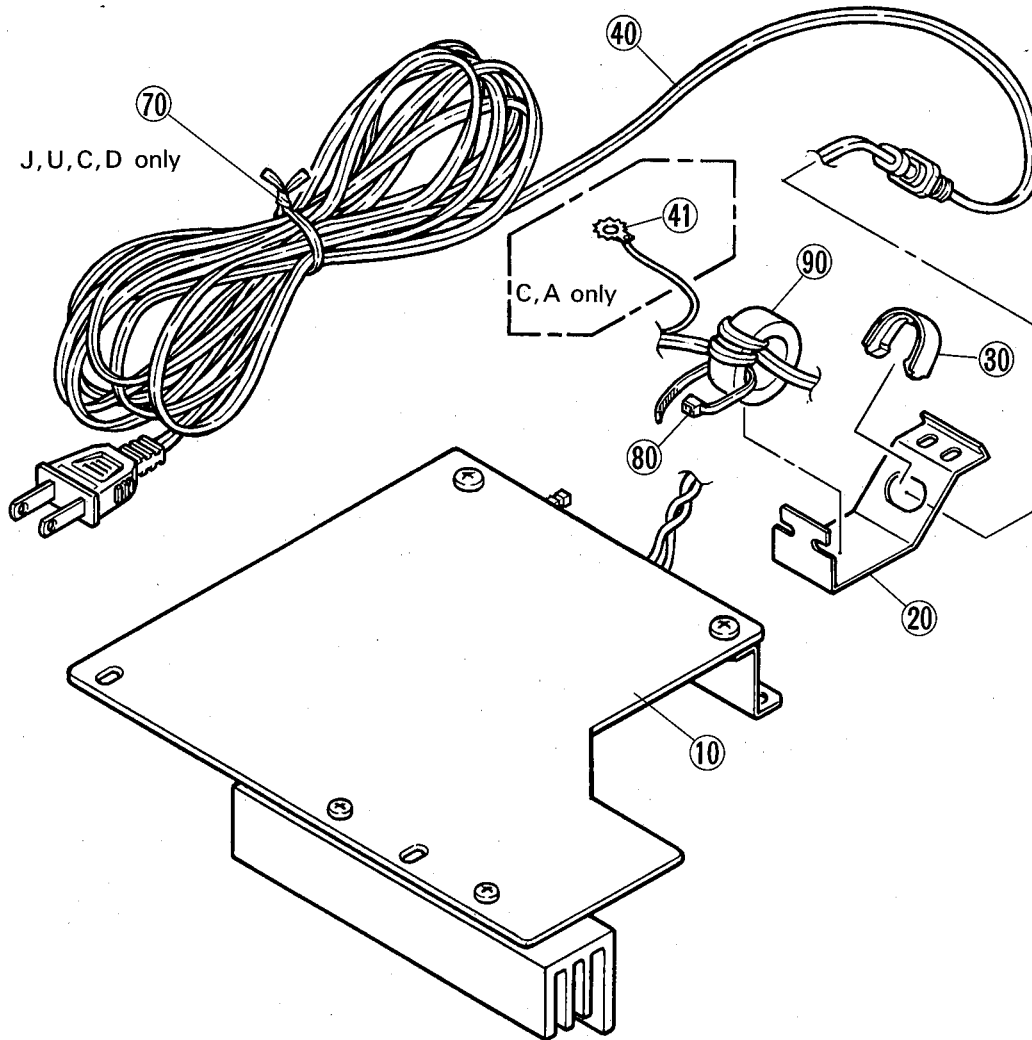


Ref. No.	Part No.	Description		部品名	Remarks	ランク
*	VK325400	Keyboard Assembly	FS E76	鍵盤 Ass'y	SY99	52
10		MK Frame	FS E76.	MK フレーム		
20	VK735000	PC Sensor		P C センサー		11
30	VK659200	Felt	WH 1027×6×3	フェルト (白)		04
40	AA055430	Key Spring		鍵 パネ	(76pcs)	02
50	NB826010	White Key Assembly	E'	白 鍵 Ass'y		04
60	NB826020	White Key Assembly	G'	白 鍵 Ass'y		04
70	NB107540	White Key Assembly	C, F	白 鍵 Ass'y	(13pcs)	03
80	NB107550	White Key Assembly	D	白 鍵 Ass'y	(6pcs)	03
90	NB107560	White Key Assembly	B, E	白 鍵 Ass'y	(12pcs)	03
100	NB107570	White Key Assembly	G	白 鍵 Ass'y	(6pcs)	03
110	NB107580	White Key Assembly	A	白 鍵 Ass'y	(6pcs)	03
120	NB107600	Black Key Assembly		黒 鍵 Ass'y	(31pcs)	03
130	VK325600	Stopper	FS E76	ストッパー		04
160	NB825940	Key Switch Unit	FS E76	M K スイッチユニット		17
160a	NA810850	Circuit Board	MK1	M K シート		07
160b	NB107130	Key Switch Assembly	9I FS	スイッチ Ass'y		08
160c	NB107120	Key Switch Assembly	12Q FS	スイッチ Ass'y	(2pcs)	08
170	NB825950	Key Switch Unit	FS E76	M K スイッチユニット		19
170a	NA810860	Circuit Board	MK2	M K シート		08
170b	NB107120	Key Switch Assembly	12Q FS.	スイッチ Ass'y	(3pcs)	08
170c	NB107150	Key Switch Assembly	7G FS	スイッチ Ass'y		08
180	ED330166	Bind Head Screw	3.0×16 FCM3BL	バインド小ネジ	(20pcs)	01
190	VK263600	Circuit Board	PC	P C シート		08
200	VA032600	Spacer		スペーサー	(2pcs)	01
210	ED030126	Bind Head Screw	3.0×12 ZMC2Y	バインド小ネジ	(2pcs)	01

*New Parts (新規部品)

ランク : Japan only

■POWER SUPPLY ASSEMBLY (電源Ass'y)



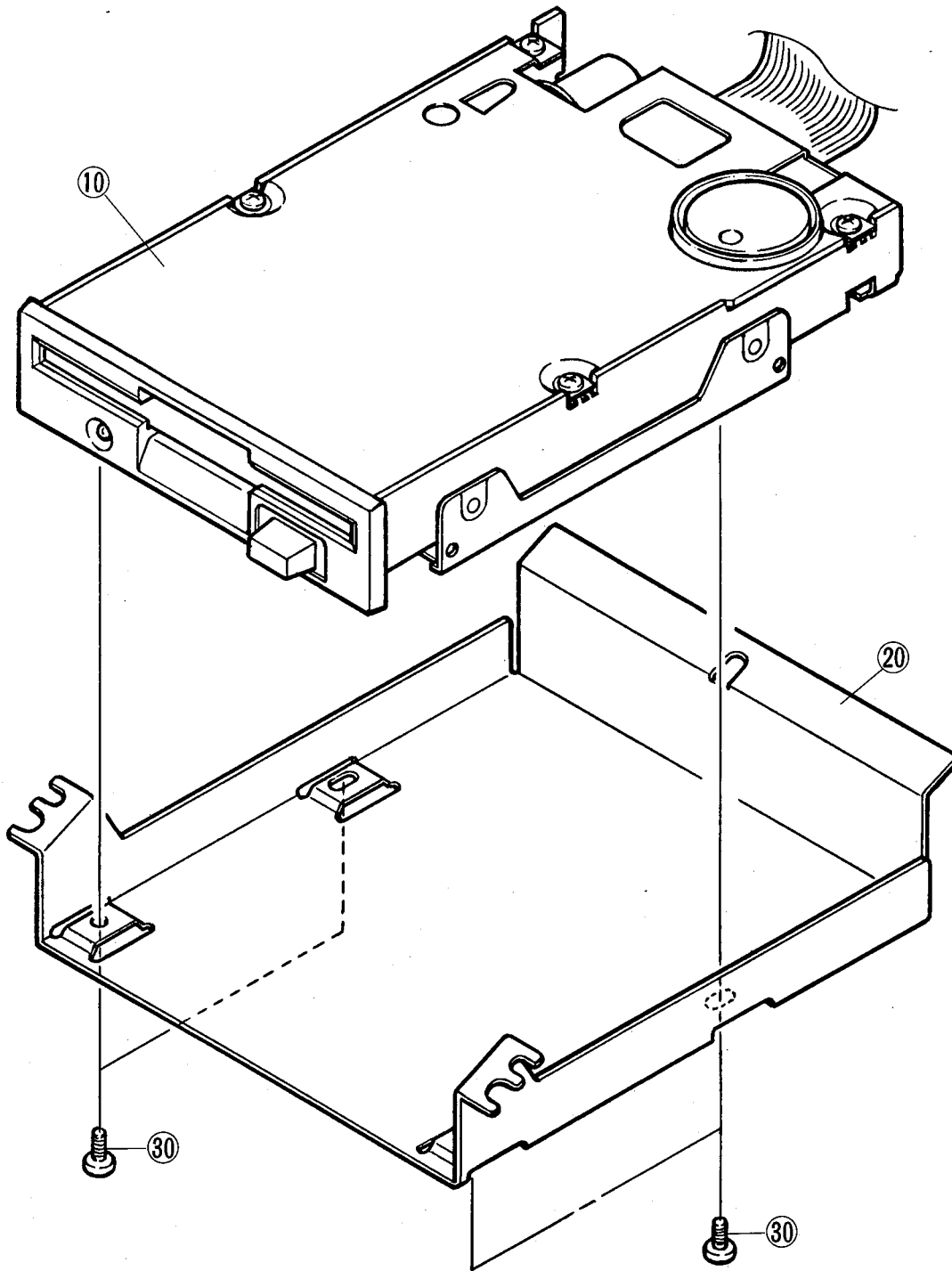
Ref. No.	Part No.	Description	部品名	Remarks	ランク
		Power Supply Assembly	電源 Ass'y	SY99	
10	VJ800200	Circuit Board	PSシート	J	19
10	VJ800300	Circuit Board	PSシート	U	20
10	VJ800400	Circuit Board	PSシート	C	20
10	VJ800500	Circuit Board	PSシート	H,D,A,B	20
20	VH812600	AC Panel	ACパネル	J	02
20	VI319500	AC Panel	ACパネル	U	02
20	VI319600	AC Panel	ACパネル	C	02
20	VI319700	AC Panel	ACパネル	H,D,A,B	02
30	CB811230	Cord Strain Relief	コードストッパー	U	02
30	CB806850	Cord Strain Relief	コードストッパー	C	02
30	CB072750	Cord Strain Relief	コードストッパー	H,D,B	01
30	CB032840	Cord Strain Relief	コードストッパー	A	01
40	VD279200	AC Cord	電源コード	J	04
40	VD279400	AC Cord	電源コード	U	06
40	VD279500	AC Cord	電源コード	C	07
40	VD280400	AC Cord	電源コード	H,D	06
40	VD279700	AC Cord	電源コード	A	06
40	VH890400	AC Cord	電源コード	B	08
41	LA003690	Lug Terminal	グラ端子	C,A only	01
70	--	Cord Clamper	ビニタ止め	J,U,C,A only	
80	CB069250	Cord Clamper	東線止め		01
90	VC362700	Ferrite Core	フェライトコア		04

*New Parts (新規部品)

ランク : Japan only

SY99

■ FDD ASSEMBLY (FDD Ass'y)

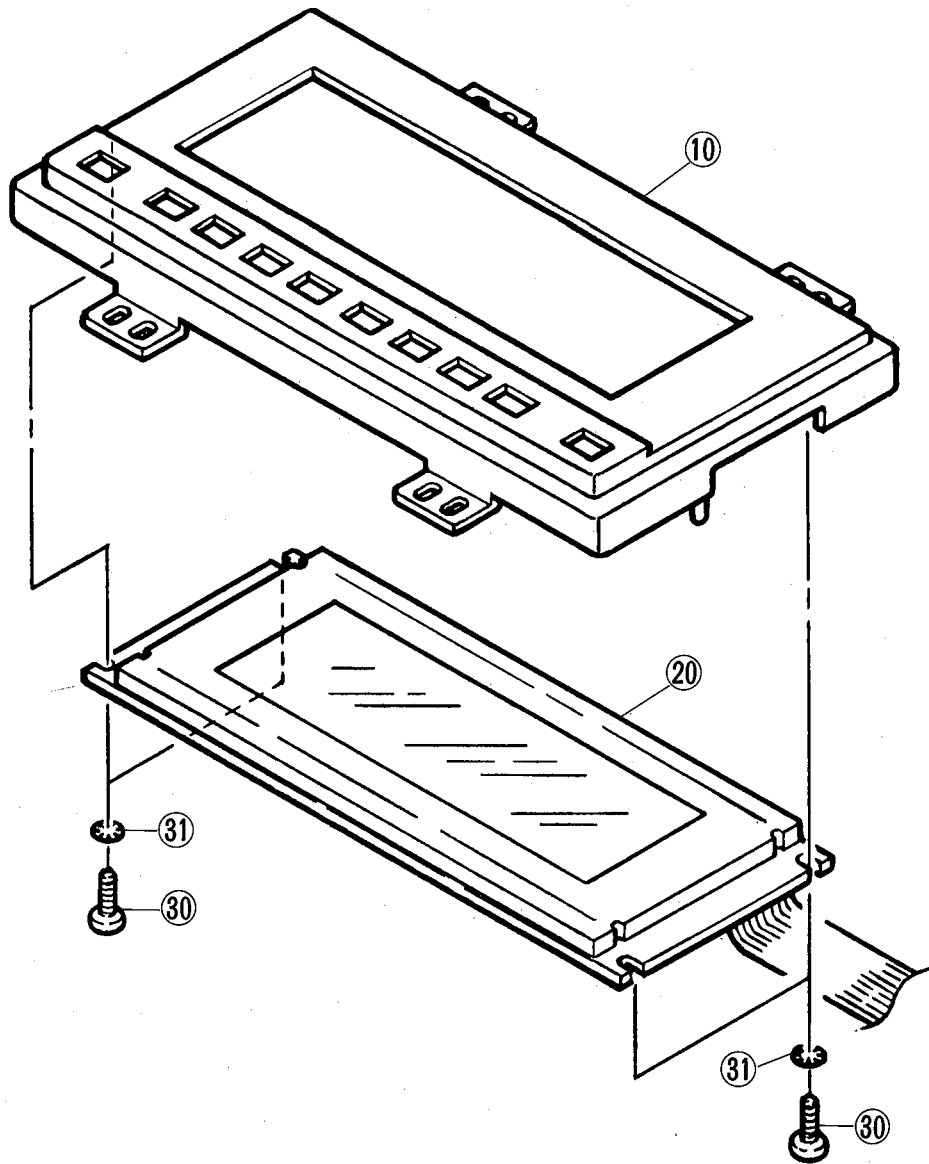


Ref. No.	Part No.	Description	部品名	Remarks	ランク
* 10	VK423800	FDD Assembly Floppy Disk Driver	FDD Ass'y フロッピーディスクドライブ	SY99 3.5"	24
20	VH813000	Angle Bracket, FDD	FDD金具		04
30	ED330066	Bind Head Screw	バインド小ネジ	(4pcs)	01

*New Parts (新規部品)

ランク：Japan only

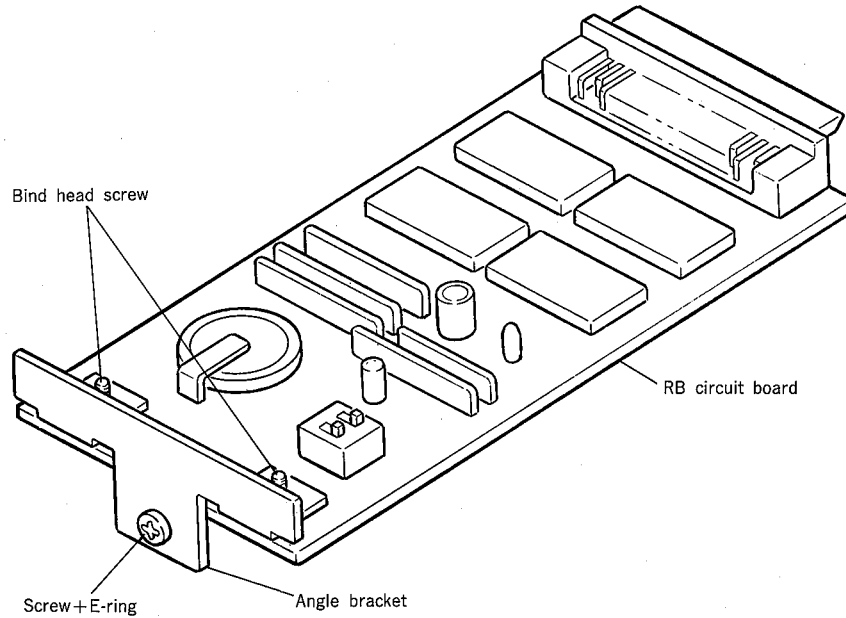
■ DISPLAY ASSEMBLY (ディスプレイ Ass'y)



SY99

Ref. No.	Part No.	Description	部品名	Remarks	ランク
10	VH811400	Display Assembly Escutcheon, LCD	ディスプレイ Ass'y LCD エスカッション	SY99	05
20	VK372800	LCD Assembly	LCD Ass'y		
30	E1330086	Bind Head Tapping Screw 3.0×8 FCM3BL	ハインドヘッドタップネジ	(4pcs)	01
31	EV413036	Toothed Lock Washer A φ 3.0 FCM3BL	歯付座金内歯形	(4pcs)	01

■ EXPANSION MEMORY BOARD (SY拡張メモリーボード) SYEMB05



Ref. No.	Part No.	Description	部品名	Remarks	ランク
		<RAM Board>	SYEMB05	<SY拡張メモリーボード>	
*	--	Circuit Board	RB	RBシート	
*	VL413800	Angle Bracket RB		ボードアングル	09
	ED030066	Bind Head Screw	3.0×6 ZMC2Y	ボインド小ネジ	01
	VL413700	Screw	SWCH12A	ボードネジ	04
	EV501256	E-Ring	φ 2.5 FNM3-3G	E形止め輪	01
	--	Circuit Board	RB	RBシート	
	XI686A00	IC	M62021FP	IC	04
	--	IC	TC74HC08AF-TP1	IC	SYST.RESET (1pc) AND (1pc.)
	--	IC	HM628128LFP-10	IC	SRAM 1M (4pcs)
	VB797600	Diode	RLS-73 0.1A	ダイオード	01
	VE331300	Resistor Array	RGLD8X104J	抵抗アレイ	01
	VL972100	Resistor Array	EXB-F7E104J5	抵抗アレイ	01
	RD256470	Chip Resistor	4.7KΩ 0.1W J	チップ抵抗	01
	RD257100	Chip Resistor	10KΩ 0.1W J	チップ抵抗	01
	RD257470	Chip Resistor	47KΩ 0.1W J	チップ抵抗	01
	--	Chip Monolithic Cera. Cap.	F 0.1μ 25V Z	チップ積層セラコン	01
	--	Slide Switch	KSD02	スライドスイッチ	01
*	VL347500	Connector FX2	FX2-52S-1.27DSL	FX2コネクタ	04
	VE338400	Lithium Battery	SONY/CR2032 3V	リチウム電池	03

*New Parts (新規部品)

ランク : Japan only

66 SY99

■ TEST PROGRAM

VERSION DISPLAY MODE

In order to verify the ROM versions of the SY99, you may want to initiate the Version Display Mode. To initiate this mode press and hold the [Voice], the [INTERNAL], and the [1] switches then the versions of the MAIN ROM and SEQUENCE (SEQ) ROM will be displayed. Press [EXIT] to return to the main program.

A. HOW TO ENTER THE TEST PROGRAM

Turn on the power switch of the SY99 and wait until the LCD has initialized and displays a normal operating mode message. While pressing the [VOICE] switch, press and hold the [BANK D] switch then the [8] switch. The SY99 will run the INITIAL TEST routine (refer to the INITIAL TEST section for details) and indicate that you have entered the Test Program by displaying the following message.

```

*** SY99 TEST Ver #.# *** Please Select

Main ROM : Version #.#   1990-10-??
SEQ. ROM : Version #.#   1990-10-??

[-1] : AUTO           [+1] : MANUAL

[ COPY ] : Fact.set   [ EXIT ] : Exit
  
```

Use the [- 1], [+ 1], [COPY], or [EXIT] panel switches to select the appropriate test mode. If you press [- 1], the auto test mode will be initiated. If you press [+ 1], the MANUAL test mode will be initiated. If you press [COPY], the SY99 will execute Test 49, "49. Factory settings", and then automatically exit the test mode and return to play mode (refer to Test 49 for details).

If you press [EXIT], you will exit the test mode and return to the play mode. The MANUAL mode is the preferred method of running the test program because it allows you to select or jump to any test and execute it. AUTO mode automatically executes each test in a fixed order. Some of the tests in the AUTO mode are automatically executed due to the nature of the test. In the AUTO mode simply press the [+ 1] switch to exit and automatically execute the next test or press [EXIT] to abort the test, then press [+ 1] to automatically execute the next test.

B. PROCEEDING THROUGH THE TESTS

(**MOST OF THESE FUNCTIONS MAINLY PERTAIN TO THE MANUAL TEST MODE**)

When you enter the test program, the following display will appear.

```

*** SY99 TEST Ver #.# *** MODE : MANUAL

* 01 : ROM CHECK
  02 : RAM Read/Write
  03 : SEQENCER ROM
  04 : SEQENCER RAM
  05 : RAM Battery
  
```

Use the [+ 1], [- 1], [ENTER], [COPY], [PAGE+], [PAGE-], [EXIT], or the numeric key pad, or the rotary encoder to move through the various tests of the test program.

Pressing: [+ 1] will execute the test which follows the current test.

[- 1] will execute the test which precedes the current test.

[ENTER] will execute the currently selected test.

[PAGE+] will select the test which follows the current test and displays the test items.

[PAGE-] will select the test which precedes the current test and displays the test items.

[EXIT] will execute Test 50, "50. EXIT" (refer to Test 50 for details).

The numeric keys 0 through 9 of the entry pad can be used to enter a two-digit number to directly select a test. Simply enter the number and then press the [ENTER] switch. For example, if you would like to select TEST 6, press [0], [6] then press the [ENTER] switch.

TEST SELECTION WHEN AN ERROR IS DETECTED

In each of the following tests listed below, if an NG (No Good) error is detected, the following operations of the test will make the SY99 wait for the entry of a test number. You can then retry the test or perform another test. If you press [EXIT], the SY99 will wait for the entry of a test number.

9. Panel switches	10. Pitch bend	11. Modulation wheel 1
12. Modulation wheel 2	13. Data entry	14. Rotary encoder
15. Keyboard	16. Aftertouch	17. MIDI IN/OUT/THRU
18. Card insert	20. Card protect switch	22. Wave card insert
26. Disk eject	27. Breath controller	28. Foot volume
29. Foot controller	30. Sustain switch	31. Foot switch
48. Jacks all off		

INITIAL TEST

The following tests will be performed automatically when the test program is initiated.

- A. Read/write check for the RAM work area of CPU (IC128) of the DM1 circuit board.
- B. Checks the interrupt levels of both M3 ICs (IC256 & IC257) of the DM2 circuit board.

DISPLAY OF TEST RESULTS

If each test checks OK then the Test program proceeds to the Test Program entry display. If Test A is NG the RAM WORK AREA may be at fault and the display will indicate:

```
** IC128(RAM) ERROR, TEST CONTINUE? **
```

If Test B is NG then the error may be related to one of the M3 IC's IRQ levels. The display will indicate the error by showing the following message:

```
* M3 IRQ CHECK ERROR, TEST CONTINUE? *
```

EXITING THE TEST

This test automatically proceeds to the Test Program entry display if the items under test are OK. If an error message occurs press [YES] to exit the test. However, a RAM ERROR may not allow the SY99 to function normally.

TEST PROGRAM TEST 1 - 50 (MANUAL MODE OPERATION)**1. TEST 1: SYSTEM ROM TEST**

```
* 01; ROM CHECK
```

Performs a read test on the ROM for the following addresses.

IC111 : 8000h - 800Fh	IC111 : A000h - A00Fh
IC111 : C000h - C00Fh	IC111 : E000h - E00Fh
IC112 : 6000h - 600Fh	IC113 : 1000h - 100Fh

(This test checks only 16 bytes.)

DISPLAY OF TEST RESULTS

```
OK          * 01: ROM CHECK          OK
```

or

```
NG          * 01: ROM CHECK          n:ICxxx          NG
```

(where n=ROM # and xxx=IC #)

TEST END

Ends after displaying the results.

2. TEST 2: SYSTEM RAM TEST

* 02: RAM Read/Write

Performs a read/write test of RAM on the following addresses.

IC119 : 4000h – 5FFFh

IC115 : 3000h – 37FFFh

DISPLAY OF TEST RESULTS

OK	* 02: RAM Read/Write	OK
----	----------------------	----

or

NG	* 02: RAM Read/Write IC119	NG
----	----------------------------	----

TEST END

Ends after displaying the results. All RAM data is preserved.

3. TEST 3: SEQUENCER ROM TEST

* 03: SEQUENCER ROM

Performs a read test on the ROM (IC101) of DM1 circuit board.

IC101 : 8000h – BFFFh 7 banks (#0 – #6)

C000h – FFFFh 1 bank (#7)

DISPLAY OF TEST RESULTS

OK	* 03: SEQUENCER ROM	OK
----	---------------------	----

NG	* 03: SEQUENCER ROM	NG
----	---------------------	----

TEST END

Ends after displaying the results.

4. TEST 4: SEQUENCER RAM TEST

* 04: SEQUENCER RAM

Performs a RAM read/write test on all addresses of IC103 (RAM 1), IC102 (RAM 2).

1. IC103 : 4000h – 5FFFh

2. IC102 : 6000h (WINDOW 0), 6200h (WINDOW 1), 6400h (WINDOW 2), 6600h (WINDOW 3)

DISPLAY OF TEST RESULTS

OK	* 04: SEQUENCER RAM	OK
----	---------------------	----

NG	* 04: SEQUENCER RAM ICxxx	NG
----	---------------------------	----

(xxx = IC #)

TEST END

Ends after displaying the results. All RAM data is preserved.

5. TEST 5: RAM BACKUP BATTERY TEST

* 05: RAM Battery

This test checks that the voltage of the RAM backup battery is greater than 2.9V and less than 3.3V.

DISPLAY OF TEST RESULTS

OK	* 05: RAM Battery	3.2V	OK
NG	* 05: RAM Battery	#. #V Low	NG
	* 05: RAM Battery	#. #V High	NG

TEST END

Ends after displaying the test results.

6. TEST 6: LCD – ALL DOTS “ON” TEST

* 06: LCD All On

Check that all dots of the LCD change to black (ON) and contrast of the LCD can be controlled by the contrast volume.

DISPLAY OF TEST RESULTS

First, the display indicates “* 06 LCD All On”, then all dots of the LCD change to black (ON).

TEST END

Press [EXIT] to end the test. The display shown below will appear and the SY99 will wait for you to enter a test number.

* 06: LCD All On

7. TEST 7: LCD – ALL DOTS “OFF” TEST

* 07: LCD All Off

Check that all dots change to white (OFF).

DISPLAY OF TEST RESULTS

First, the display indicates “* 07 LCD All OFF”, then all dots of the LCD change to white (OFF).

TEST END

Press [EXIT] to end the test. The display shown below will appear and the SY99 will wait for you to enter a test number.

* 07: LCD All Off

8. TEST 8: LED ON/OFF TEST

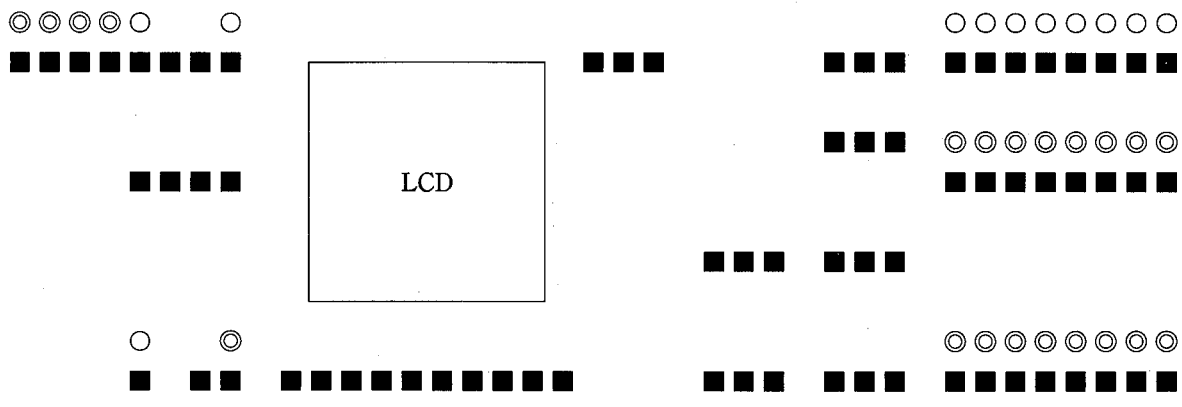
* 08: LED Check

Check that each red LED blinks once in succession from the left end of the unit (refer to the diagram shown below) and then verify that all red LEDs blink together. Next, check that each green LED blinks once, and then all green LEDs blink together. The currently blinking LEDs will be displayed in the LCD as follows.

* 08: LED Check REC RED On

(e.g. The red RECORD LED is blinking)

Check that all LEDs blink. (21 of the 32 LEDs are dual-color red/green LEDs)



Note: (●) indicates a dual-color LED. (○) indicates a single-color LED.

TEST END

Press [EXIT] to end the test. The SY99 will then be waiting for the entry of a test number.

9. TEST 9: PANEL SWITCH TEST

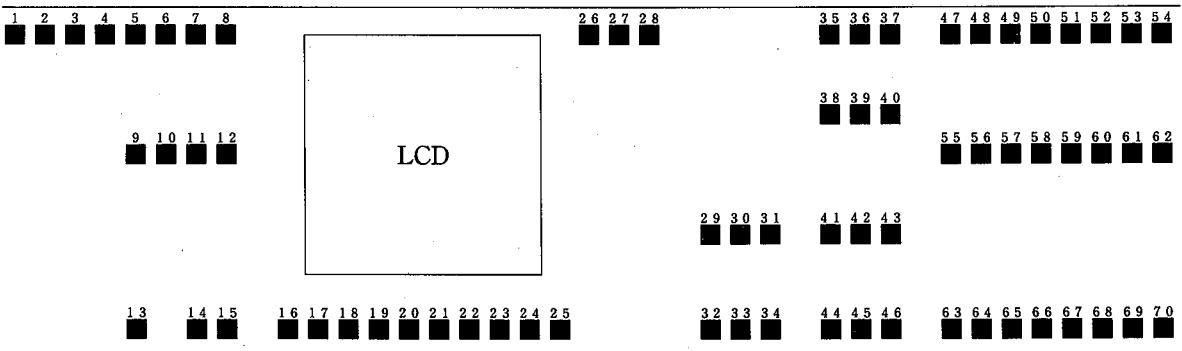
* 09: Panel Switch

Press the panel switches consecutively from the [VOICE] switch to switch [16], according to the order indicated by the LCD display.

* 09: Panel Switch Push REC

(e.g. When checking [RECORD])

The switch pressing order is displayed in the diagram below. If the switch is OK, a beep will sound and you should proceed to test the next switch. If the wrong switch is pressed an unexpected code is sent from the PKS CPU, and the error message NG will be displayed and no sound will be heard. At this time, if the correct switch is pressed then the proper code is received. You will then be able to proceed to test the next switch. The display will indicate OK, if all switches are good.



DISPLAY OF TEST RESULTS

OK * 09: Panel Switch Push 16 OK

NG * 09: Panel Switch Push REC 1? Err

TEST END

When switch [16] is pressed, OK is displayed and the test will end. During the test, if NG is detected, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

10. TEST 10: PITCH BEND WHEEL TEST

* 10: Pitch Bend	50	99
------------------	----	----

According to the target value displayed on the LCD, slowly move the pitch bend wheel. Check that the value changes from 50 to 99 then to 00 and back to 50 (in other words, center to top then to bottom and back to center).

* 10: Pitch Bend	xx	yy
------------------	----	----

(where xx = current pitch bend value and yy = next target value)

DISPLAY OF TEST RESULTS

OK	* 10: Pitch Bend	50	50	OK
NG	* 10: Pitch Bend	xx	Center	NG

(If the pitch bend value at the beginning or end of the test is not center, then xx indicates the pitch bend value when NG was detected)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

11. TEST 11: MODULATION WHEEL 1 TEST

* 11: Modulation Wheel	00	20-80
------------------------	----	-------

According to the target value displayed on the LCD, slowly move modulation wheel 1. Check that the value changes from 00→20→80→99 then back down to 80→20→00 (in other words, from bottom to top the back to the bottom).

* 11: Modulation Wheel	xx	yy
------------------------	----	----

* 11: Modulation Wheel	xx	yy-zz
------------------------	----	-------

(where xx = current value of modulation wheel 1, yy and zz are the next target values)

DISPLAY OF TEST RESULTS

OK	* 11; Modulation Wheel	00	00	OK
NG	(No change in display message)			

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

12. TEST 12: MODULATION WHEEL 2 TEST

* 12: Modulation WH2 50 99

Before beginning this test, move modulation wheel 2 to the center position. According to the target value displayed on the LCD, slowly move modulation wheel 2. Check that the value changes from 50 to 99 then to 00 and back to 50 (in other words, from center to top then to bottom and back to center).

* 12: Modulation WH2 xx yy

* 12: Modulation WH2 xx yy-zz

(where xx = current value of modulation wheel 2, yy and zz are the next target values)

DISPLAY OF TEST RESULTS

OK	* 12: Modulation WH2 50 50 OK
----	-------------------------------

NG (No change in display message)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

13. TEST 13: DATA ENTRY SLIDER TEST

* 13: Data Entry 00 20-80

According to the target value displayed on the LCD, slowly move the data entry slider. Check that the value changes from 00→20→80→99 and the back down to 80→20→00 (in other words, from the bottom to the top and back down to the bottom).

* 13: Data Entry xx yy

* 13: Data Entry xx yy-zz

(where xx = current value of data entry, yy and zz are the next target values)

DISPLAY OF TEST RESULTS

OK	* 13: Data Entry 00 00 OK
----	---------------------------

NG (No change in display message)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

14. TEST 14: ROTARY ENCODER (DATA ENTRY WHEEL) TEST

* 14: R-Encoder	Right	00
-----------------	-------	----

Rotate the rotary encoder (data entry wheel) to the right as indicated by the LCD display. Check that the value on the LCD changes from Right 00→Left 00→Left 01 (in other words, first rotate to the right then to the left).

* 14: R-Encoder	Right	xx
-----------------	-------	----

* 14: R-Encoder	Left	xx
-----------------	------	----

(where xx = current value)

DISPLAY OF TEST RESULTS

OK	* 14: R-Encoder	Left	01	OK
----	-----------------	------	----	----

NG (No change in display message)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

15. TEST 15: KEYBOARD TEST

* 15: Keyboard Check

Play a scale on the keyboard from E0 to G6 with a steady and even touch.

* 15: Keyboard Check	Push	C1
----------------------	------	----

(e.g. in the case of C1)

If the key switch is ok, the note will sound and you should proceed to play the next key. If you play the wrong key this will produce an unexpected code to the PKS CPU and Err will be displayed. As a result the sound of that note will not be heard. However, if the right key is played following the playing of the wrong key, then correct code is received and the note for that key will sound. You can then proceed to play the next key. If all key switches are good then OK will be displayed on the LCD.

DISPLAY OF TEST RESULTS

OK	* 15: Keyboard Check	Push	G6	OK
----	----------------------	------	----	----

NG	* 15: Keyboard Check	Push	xxx 1? Err
----	----------------------	------	------------

(if play the wrong key)

NG	* 15: Keyboard Check	Push	xxx \$nn NG
----	----------------------	------	-------------

(if the initial touch was incorrect)

TEST END

When you play the G6 key and OK is displayed, the test will end.

If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

16. TEST 16: AFTERTOUCH TEST

* 16: After Touch	00	20-80
-------------------	----	-------

According to the target value displayed on the LCD, press a key on the keyboard. Check that the value changes from 00→20→80→99 and back down to 80→20→00 (in other words, apply light pressure and increase pressure to a heavier touch then decrease back to a light touch).

* 16: After Touch	xx	yy
-------------------	----	----

* 16: After Touch	xx	yy-zz
-------------------	----	-------

(where xx = the current aftertouch value, yy and zz are the next target values)

DISPLAY OF TEST RESULTS

OK	* 16: After Touch	00	00	OK
----	-------------------	----	----	----

NG (No change in display message)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

17. TEST 17: MIDI TEST

* 17: MIDI (I/O/T)

After connecting the MIDI IN to the MIDI OUT via a MIDI cable, execute the test. The following message will appear on the LCD.

* 17: MIDI (I/O/T)	Tx:yy	Rx:zz
--------------------	-------	-------

TEST END

When you press [EXIT] the test will end and the SY99 will wait for a test number to be entered. If an NG error occurs, because unexpected data was received, the test will end at that point. If an NG error occurs because no data was received within a certain time, the test will continue until [EXIT] is pressed.

18. TEST 18: DATA CARD INSERT TEST

* 18: D-Card Insert	0
---------------------	---

Insert a RAM card (MCD64) into the DATA card slot and execute the test. Check that when you remove and insert the card back into the slot, the number on the display changes from 0 to 1 and that the OK result is displayed.

DISPLAY OF TEST RESULTS

OK	* 18: D-Card Insert	1	OK
----	---------------------	---	----

NG (No change in display message)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

19. TEST 19: DATA CARDS READ/WRITE TEST

* 19: D-Card R/Write

This performs a read/write test on the following addresses of the RAM card.
 CARD 1 : 20000h–27FFFh CARD 2 : 28000h–2FFFFh
 Insert a RAM card with the memory protect turned off and execute the test.

DISPLAY OF TEST RESULTS

OK	* 19: D-Card R/Write CARD : 12 OK
NG	* 19: D-Card R/Write CARD : x NG

(e.g. if CARD 2 is No Good)

TEST END

After displaying the results, the test will end. All card data is preserved.

20. TEST 20: DATA CARD PROTECT SWITCH TEST

* 20: D-Card Protect 0

Use a RAM card to check that the card protect switch status is being read. Check that when the switch is set from "protect off" to "protect on", the number on the display changes from 0 to 1 and that the OK result is also displayed.

DISPLAY OF TEST RESULTS

OK	* 20: D-Card Protect 1 OK
NG	(No change in display)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

21. TEST 21: RAM BACKUP BATTERY TEST

* 21: D-Card Battery

This test checks that the voltage of the RAM card backup battery.

DISPLAY OF TEST RESULTS

OK	* 21: D-Card Battery #.#V OK
NG	* 21: D-Card Battery #.#V Low NG
	* 21: D-Card Battery #.#V High NG

TEST END

Ends after displaying the test results.

22. TEST 22: WAVEFORM CARD INSERT TEST

* 22: W-Card Insert	0
---------------------	---

Check that when a waveform card is inserted into the slot, the number on the display changes from 0 to 1 and that the OK result is displayed.

DISPLAY OF TEST RESULTS

OK	* 22: W-Card Insert	1	OK
----	---------------------	---	----

NG (No change in display)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

23. TEST 23: WAVE ROM READ TEST

* 23: W-ROM Read

This test is utilized by the factory and it is not intended for field service use.

24. TEST 24: WAVE RAM READ/WRITE TEST

* 24: W-RAM

This performs a read/write test on the following addresses of the wave RAMs.

Internal WAVE RAM :	400000h - 43FFFFh	Expansion WAVE RAM 1 :	440000h - 47FFFFh
Expansion WAVE RAM 2 :	480000h - 4BFFFFh	Expansion WAVE RAM 3 :	4C0000h - 4FFFFFh
Expansion WAVE RAM 4 :	500000h - 53FFFFh	Expansion WAVE RAM 5 :	540000h - 57FFFFh

And then checks the voltage of the backup battery of the internal WAVE RAM and the expansion WAVE RAMs.

DISPLAY OF TEST RESULTS

OK	* 24: W-RAM	000000	OK
----	-------------	--------	----

NG	* 24: W-RAM	XORBOX	NG
----	-------------	--------	----

The LCD will indicate that the error is occurred by displaying the alphabet R, B, O or X.

R : Only the read/write test is NG.	O : The read/write test and the battery check are OK.
B : Only the battery check is NG.	X : The read/write test and the battery check are NG.

TEST END

After displaying the results, the test will end. All RAM data is preserved.

25. TEST 25: DISK READ/WRITE TEST

* 25: Disk Read/Write

Use a blank disk to test the disk format. This test will write and read two types of data. Testing is performed on the following tracks.

SIDE 0 : TRACK 40 (sector 4) – TRACK 00 (sector 1) – TRACK 79 (sector 9)
 SIDE 1 : TRACK 40 (sector 4) – TRACK 00 (sector 1) – TRACK 79 (sector 9)

Insert a blank disk with the write protect off and execute the test.

DISPLAY OF TEST RESULTS

OK * 25: Disk Read/Write C79:H1 Verify OK

NG * 25: Disk Read/Write Cyy:Hx nnnnnnn NG

(where x = side or head number, yy = track or cylinder number, and nnnnnn: condition at time of error)

TEST END

After displaying the results, the test will end.

26. TEST 26: DISK EJECT TEST

* 26: Disk Eject 0

Insert a blank disk and execute the test. Check that when the eject button is pressed and the disk is removed, the number on the display changes from 0 to 1 and that the OK result is displayed.

DISPLAY OF TEST RESULTS

OK * 26: Disk Eject 1 OK

NG (No change in display message)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

27. TEST 27: BREATH CONTROLLER TEST

* 27: Breath Control 99 00

Connect a breath controller and blow into it. Check that the number on the display changes from 00→01→20→80→95→99→80→20→01→00 (in other words, off to strong and back to off).

* 27: Breath Control xx yy-zz

(where xx = current breath control value, yy and zz are the next target values)

DISPLAY OF TEST RESULTS

OK * 27: Breath Control xx 00 OK

(where xx = breath controller value at end of test)

NG (No change in display)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

28. TEST 28: FOOT VOLUME TEST

* 28: Foot Volume	00	20-80
-------------------	----	-------

Connect a foot controller and operate it throughout its range. Check that the number on the display changes from 00→01→20→80→95→99→95→80→20→01→00 (in other words, starting from the raised position then to the lowered position and back to the raised position).

* 28: Foot Volume	xx	yy-zz
-------------------	----	-------

(where xx = current foot volume value, yy and zz are the next target values)

DISPLAY OF TEST RESULTS

OK	* 28: Foot Volume	xx	00	OK
----	-------------------	----	----	----

(where xx = foot volume value at end of test)

NG (No change in display)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

29. TEST 29: FOOT CONTROLLER TEST

* 29: Foot Control	00	20-80
--------------------	----	-------

Connect a foot controller and operate it throughout its range. Check that the number on the display changes from 00→01→20→80→95→99→95→80→20→01→00 (in other words, starting from the raised position then to the lowered position and back to the raised position).

* 29: Foot Control	xx	yy-zz
--------------------	----	-------

(where xx = current foot controller value, yy and zz are the next target values)

DISPLAY OF TEST RESULTS

OK	* 29: Foot Control	xx	00	OK
----	--------------------	----	----	----

(where xx = foot controller value at end of test)

NG (No change in display)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

30. TEST 30: SUSTAIN SWITCH TEST

* 30: Sustain	1
---------------	---

Connect a sustain switch and press it on and off. Check that the number on the display changes from 1 to 0 then back to 1 and verify that the OK result is displayed.

DISPLAY OF TEST RESULTS

OK	* 30: Sustain	1	OK
----	---------------	---	----

NG (No change in display)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

31. TEST 31: FOOT SWITCH TEST

* 31: Foot Switch	1
-------------------	---

Connect a foot switch and press it on and off. Check that the number on the display changes from 1 to 0 then back to 1 and verify that the OK result is displayed.

DISPLAY OF TEST RESULTS

OK	* 31: Foot Switch	1	OK
----	-------------------	---	----

NG (No change in display)

TEST END

After displaying the result, the test will end. If NG is detected during the test, refer to section B, "B. PROCEEDING THROUGH THE TESTS".

32. TEST 32: 1 kHz FM SOUND OUTPUT (OUTPUT L1) TEST

* 32: 1KHz to L1-> L1

Check that the correct signal is output from OUTPUT L1 and PHONES (L) jacks.

The signal route is as follows:

The digital representation of the 1 kHz signal is output from S00 terminal (channel 0) of OPS3 IC (IC220) to INDV1 terminal (channel 13) of the M3 IC (IC256). From the INDV1 terminal of the M3 IC, the signal sent to the IN1 terminal of the PAN(2) IC (IC215). From the PAN(2) IC, the signal is output from the S1 and S2 terminals. The signal is then sent to the MX10 and the MX11 inputs of the MIX5 IC (IC228). Now the signal is sent out of the MIX5 IC via the MX00 and the MX01 terminals which feeds the signal to the SI1 and SI2 inputs of the AFDO (FLOATING POINT CONVERTER) IC (IC206). The AFDO and the DAC work together to produce the analog that is output from the CH1 (Channel 1) terminal. The signal goes to the analog circuits and is output from the OUTPUT L1 jack. It should be noted that the active low FMSEL signal must be at a 0 volt or LOW logic level in order to output this signal.

ITEMS TO CHECK

Insert the appropriate 1/4" phone plugs into each output jack and check OUTPUT L1, OUTPUT L2, OUTPUT R1, OUTPUT R2, and PHONES (L/R) outputs. If necessary, verify the frequency, output waveform, output level, and THD of each output using a frequency counter, oscilloscope, AC voltmeter (with 12.47 kHz filter) and distortion meter. The volume control must be set at maximum for these checks. While sounding, the LCD will display the following message:

* 32: 1KHz to L1-> L1 Output On

Listed below are the specifications and conditions of each output during this test.

OUTPUT L1 : 1kHz \pm 1.5Hz, sine wave, distortion 0.2%, 0.0dB \pm 2dB (10k ohm load)
 OUTPUT L2 : less than -70dB
 OUTPUT R1 : less than -70dB
 OUTPUT R2 : less than -70dB

PHONES (L) : 1kHz, sine wave, distortion 0.2% or less, + 5.5dB \pm 2dB (150 ohm load)
 PHONES (R) : less than - 60dB

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 32: 1KHz to L1-> L1 Output Off

33. TEST 33: 1kHz FM SOUND OUTPUT (OUTPUT R1) TEST

* 33: 1KHz to R1-> R1

ITEMS TO CHECK

Check that the correct signal is output from OUTPUT R1 and the PHONES (R) jacks.

The basic signal route is the same as it was in TEST 32 except the signal is output from the CH2 (Channel 2) of the AFDO IC.

Insert the appropriate 1/4" phone plugs into each output jack and check OUTPUT L1, OUTPUT L2, OUTPUT R1, OUTPUT R2, and PHONES (L/R) outputs. If necessary, verify the frequency, output waveform, output level, and THD of each output using the previously specified test equipment (refer to TEST 32). The volume control must be set at maximum for these checks. While sounding, the LCD will display the following message:

* 33: 1KHz to R1-> R1 Output On

Listed below are the specifications and conditions of each output during this test.

OUTPUT R1 : 1kHz \pm 1.5Hz, sine wave, distortion 0.2%, 0.0dB \pm 2dB (10k ohm load)

OUTPUT R2 : less than - 70dB

OUTPUT L1 : less than - 70dB

OUTPUT L2 : less than - 70dB

PHONES (L) : less than - 60dB

PHONES (R) : 1kHz, sine wave, distortion 0.2% or less, + 5.5dB \pm 2dB (150 ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 33: 1KHz to R1-> R1 Output Off

34. TEST 34: 1kHz FM SOUND OUTPUT (OUTPUT L2) TEST

* 34: 1KHz to L2-> L2

ITEMS TO CHECK

Check that the correct signal is output from OUTPUT L2 and the PHONES (L) jacks.

The basic signal route is the same as it was in TEST 32 except the signal is output from the CH3 (Channel 3) of the AFDO IC.

Insert the appropriate 1/4" phone plugs into each output jack and check OUTPUT L1, OUTPUT L2, OUTPUT R1, OUTPUT R2, and PHONES (L/R) outputs. If necessary, verify the frequency, output waveform, output level, and THD of each output using the previously specified test equipment (refer to TEST 32). The volume control must be set at maximum for these checks. While sounding, the LCD will display the following message:

* 34: 1KHz to L2-> L2 Output On

Listed below are the specifications and conditions of the output during this test.

OUTPUT L2 : 1kHz \pm 1.5Hz, sine wave, distortion 0.2%, 0.0dB \pm 2dB (10k ohm load)

OUTPUT L1 : less than -70dB

OUTPUT R2 : less than -70dB

OUTPUT R1 : less than -70dB

PHONES (L) : 1kHz, sine wave, distortion 0.2% or less, +5.5dB \pm 2dB (150 ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 34: 1KHz to L2-> L2 Output Off

35. TEST 35: 1kHz FM SOUND OUTPUT (OUTPUT R2) TEST

* 35: 1KHz to R2-> R2

ITEMS TO CHECK

Check that the correct signal is output from OUTPUT R2 and the PHONES (R) jacks.

The basic signal route is the same as it was in TEST 32 except the signal is output from the CH4 (Channel 4) of the AFDO IC.

Insert the appropriate 1/4" phone plugs into each output jack and check OUTPUT L1, OUTPUT L2, OUTPUT R1, OUTPUT R2, and PHONES (L/R) outputs. If necessary, verify the frequency, output waveform, output level, and THD of each output using the previously specified test equipment (refer to TEST 32). The volume control must be set at maximum for these checks. While sounding, the LCD will display the following message:

* 35: 1KHz to R2-> R2 Output On

Listed below are the specifications and conditions of each output during this test.

OUTPUT R2 : 1kHz \pm 1.5Hz, sine wave, distortion 0.2%, 0.0dB \pm 2dB (10k ohm load)

OUTPUT R1 : less than -70dB

OUTPUT L1 : less than -70dB

OUTPUT L2 : less than -70dB

PHONES (R) : 1kHz, sine wave, distortion 0.2% or less, +5.5dB \pm 2dB (150 ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 35: 1KHz to R2-> R2 Output Off

36. TEST 36: 1kHz FM SOUND OUTPUT (OUTPUT L2 OUTPUT L1) TEST

* 36: 1KHz to L2-> L1

ITEMS TO CHECK

Check that when the plug connected to OUTPUT L2 is pulled out, the signal being output from OUTPUT L2 is now output from OUTPUT L1. The basic signal route is the same as it was for TEST 34.

Insert the appropriate 1/4" phone plug into OUTPUT L1 and verify, if necessary, the frequency, output waveform, output level, and THD of this output using the previously specified test equipment (refer to TEST 32). The volume control must be set at maximum for this test. While sounding, the LCD will display the following message:

* 36: 1KHz to L2-> L1 Output On

The specifications for this test are as follows:

OUTPUT L1 : 1kHz, sine wave, 0.0dB \pm 2dB (10k ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 36: 1KHz to L2-> L1 Output Off

37. TEST 37: 1kHz FM SOUND OUTPUT (OUTPUT R2 OUTPUT R1) TEST

* 37: 1KHz to R2-> R1

ITEMS TO CHECK

Check that when the plug connected to OUTPUT R2 is pulled out, the signal being output from OUTPUT R2 is now output from OUTPUT R1. The basic signal route is the same as it was for TEST 35.

Insert the appropriate 1/4" phone plug into OUTPUT R1 and verify, if necessary, the frequency, output waveform, output level, and THD of this output using the previously specified test equipment (refer to TEST 32). The volume control must be set at maximum for this test. While sounding, the LCD will display the following message:

* 37: 1KHz to R2-> R1 Output Off

The specifications for this test are as follows:

OUTPUT R1 : 1kHz, sine wave, 0.0dB \pm 2dB (10k ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 37: 1KHz to R2-> R1 Output Off

38. TEST 38: 1kHz FM SOUND OUTPUT (OUTPUT R1-OUTPUT L1) TEST

* 38: 1KHz to R1-> L1

ITEMS TO CHECK

Check that when the plug connected to OUTPUT R1 is pulled out, the signal being output from OUTPUT R1 is now output from OUTPUT L1. The basic signal route is the same as it was for TEST 33.

Insert the appropriate 1/4" phone plug into OUTPUT L1 and verify, if necessary, the frequency, output waveform, output level, and THD of this output using the previously specified test equipment (refer to TEST 32). The volume control must be set at maximum for this test. While sounding, the LCD will display the following message:

* 38: 1KHz to R1-> L1 Output On

The specifications for this test are as follows:

OUTPUT L1 : 1kHz, sine wave, 0.0dB \pm 2dB (10k ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 38: 1KHz to R1-> L1 Output Off

39. TEST 39: 1kHz PCM SOUND OUTPUT (EFFECT → OUTPUT L, R) TEST

* 39: PCM to Eff Para

SIGNAL PATH

The digital representation of 1 kHz signal is output from SO0 terminal (channel 1) of the OPS3 IC (IC218) to DIINO terminal of the M3 IC (IC257). From DIOUT1 and DIOUT2 terminals of the M3 IC, the signal is sent to the SI1 terminal of the ESI ICs (IC222 and IC223). The signal output from the SO1 terminal of ESI IC is sent to the SIO terminal of the DSP2 ICs (IC226 and IC227).

From the DSP2 ICs, the signal is out from SO0 terminal to the ESI IC and the other DSP2 IC. These signals then sent to the MIX inputs of the MIX5 IC (IC228). Now the signal is sent out of the MIX5 IC via the MX0 terminals which feeds the signal to the SI1 and SI2 inputs of the AFD0 IC. The AFD0 and the DAC work together to produce the analog that is output from each output terminals via the analog circuits. It should be noted that the active low FMSEL signal must be at a +5V or HIGH logic level in order to output this signal.

Each effective signal route is as follows:

- OUTPUT L1 : MOD(IC221), DRAM(IC229-IC232), SO0 terminal of DSP2 (IC226), MX00 terminal (channel L) of MIX5 (IC228)
- OUTPUT R1 : MOD(IC221), DRAM(IC232-IC235), SO1 terminal of DSP2 (IC226), MX00 terminal (channel R) of MIX5 (IC228)
- OUTPUT L2 : MOD(IC223), DRAM(IC236-IC239), SO0 terminal of DSP2 (IC227), MX01 terminal (channel L) of MIX5 (IC228)
- OUTPUT R2 : MOD(IC223), DRAM(IC232-IC235), SO1 terminal of DSP2 (IC227), MX01 terminal (channel R) of MIX5 (IC228)

ITEMS TO CHECK

Insert the appropriate 1/4" phone plug into each output jack and verify, if necessary, the frequency, output waveform, output level, and THD of this output using the previously specified test equipment (refer to TEST 32).

The volume control must be set at maximum for this test. While sounding, the LCD will display the following message:

* 39: PCM to Eff Para Output On

The specifications for this test are as follows:

- OUTPUT L1 : 1kHz, sine wave, distortion 0.3% or less, 0.0±2dBm (10k ohm load)
- OUTPUT R1 : 1kHz, sine wave, distortion 0.3% or less, 0.0±2dBm (10k ohm load)
- OUTPUT L2 : 1kHz, sine wave, distortion 0.3% or less, 0.0±2dBm (10k ohm load)
- OUTPUT R2 : 1kHz, sine wave, distortion 0.3% or less, 0.0±2dBm (10k ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 39: PCM to Eff Para Output Off

40. TEST 40: 1kHz FM SOUND OUTPUT (EFFECT → OUTPUT L) TEST

* 40: FM to Eff Para

Check that the correct signal is output from OUTPUT L1 and OUTPUT L2 jacks. The signal route is the same as it was in TEST 39 except for the following: The signal is output from the SO0 terminal (channel 0) of the OPS3 IC (IC220) to the M3 IC (IC256). And the FMSEL signal must be at LOW logic level.

ITEMS TO CHECK

Insert the appropriate 1/4" phone plug into each output jack and verify, if necessary, the frequency, output waveform and output level using the previously specified test equipment (refer to TEST 32). The volume control must be set at maximum for this test. While sounding, the LCD will display the following message:

* 40: FM to Eff Para	Output On
----------------------	-----------

The specifications for this test are as follows:

OUTPUT L1 : 1kHz, sine wave, $0.0 \pm 2\text{dBm}$ (10k ohm load)
 OUTPUT L2 : 1kHz, sine wave, $0.0 \pm 2\text{dBm}$ (10k ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur; (1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 40: FM to Eff Para	Output Off
----------------------	------------

41. TEST 41: 1kHz FM SOUND OUTPUT (EFFECT → OUTPUT L1) TEST

* 41: FM to Eff Seri

Check that the correct signal is output from OUTPUT L1 jacks. The digital representation of 1 kHz signal is output from SO0 terminal (channel 0) of the OPS3 IC (IC220) to DIINO terminal of the M3 IC (IC256). From DIOU1 and DIOU2 terminals of the M3 IC, the signal is sent. Then the signal output from the SO2 terminal of the DPS2 IC (IC226) is sent to the DSP2 IC (IC227). From the DSP2 IC (IC227), the signal is sent to the input terminal of the MIX5 IC (IC228). Now the signal is sent out of the MIX5 IC via the MX00 terminal. It should be noted that the FMSEL signal must be at a 0V or LOW logic level, and the MOD ICs, DRAM ICs are not used during this test.

ITEMS TO CHECK

Insert the appropriate 1/4" phone plug into OUTPUT L1 jack and verify, if necessary, the frequency, output waveform and output level using the previously specified test equipment (refer to TEST 32). The volume control must be set at maximum for this test. While sounding, the LCD will display the following message:

* 41: FM to Eff Seri	Output On
----------------------	-----------

The specifications for this test is as follows:

OUTPUT L1 : 1kHz, sine wave, $0.0 \pm 2\text{dBm}$ (10k ohm load)

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur; (1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 41: FM to Eff Seri	Output Off
----------------------	------------

42. TEST 42: AWM (M3) SOUND OUTPUT TEST

* 42: PCM Check

SIGNAL PATH

This outputs the sound which is stored in addresses 012000h–01FFFFh of WAVE ROM. The data stored at these addresses is retrieved by the M3(A) IC (IC257) and output via pin 1 (INDV0 terminal, channel 0). The signal from pin 1 is then output to pin 11 (INO terminal) of the PAN(1) IC (IC216). The PAN (1) IC outputs the signal from pins 21 and 22 (S1 and S2 terminals, respectively) and sends the signal to pins 12 and 13 (SI2 and SI1 terminals, respectively) of the PAN (2) IC (IC215). The PAN (2) IC outputs the signal from pins 21 and 22 (S1 and S2 terminals, respectively) to pins 8 and 7 (MXI0 and MXI1 terminals) of MIX5 (IC228) IC. The signal is sent out of the MIX5 IC via the MXO0 and the MXO1 terminals. This ultimately produces signal output from OUTPUT L1.

ITEMS TO CHECK

Confirm that a AWM signal is being sent to OUTPUT L1 using an amplifier and speaker to monitor the signal. The AWM signal is not a steady tone. While this signal is sounding, the LCD will display the following message:

* 42: PCM Check	Output On
-----------------	-----------

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur; (1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 42: PCM Check	Output Off
-----------------	------------

43. TEST 43: FM SOUND OUTPUT THROUGH M3 IC (AWM) TEST

* 43: FM Thru M3(PCM)

SIGNAL PATH

A sine wave which is frequency swept by the EGM2 (1) IC will cause signals to be alternately output from OUTPUT L1, OUTPUT R1, OUTPUT L2 and OUTPUT R2 in a two channel pair sequence. The FMSEL signal to the EGM2 (1) and OPS3 (1) must be at a 1 or HIGH logic level for this test. The appropriate data from EGM2 (1) IC (IC217) is sent to the OPS3 (1) IC (IC218) in order to produce the sound. The OPS3 (1) IC outputs the signals from pins 54 and 55 (SO0, channel 1 and SO1, channel 9) via IC225 (pins 8 and 9) to pins 27 and 28 (terminals DIINO and DIIN1) of the M3 (A) IC (IC257). The M3 (A) IC outputs the signals from pins 1 and 2 (INDV0, channel 5 and INDV1, channel 6) to pins 10 and 11 (IN1 and IN0 terminals) of the PAN (1) IC (IC216). The PAN (1) IC sends the signals out from pins 21 and 22 (S1 and S2 terminals) to pins 12 and 13 (ST2 and ST1 terminals) of the PAN (2) IC (IC215). The PAN (2) IC outputs the signals from pins 21 and 22 (S1 and S2 terminals) to pins 8 and 7 (MXI0 and the MXI1 terminals) of MIX5 IC (IC228). The signal is sent out of the MIX5 IC via the MXO0 and the MXO1 terminals. This ultimately produces signal output from OUTPUT L1, OUTPUT R1, OUTPUT L2, OUTPUT R2.

ITEMS TO CHECK

Insert the appropriate 1/4" phone plug into OUTPUT L1 and observe the output waveform with an oscilloscope. Check that the level does not change excessively as the output sweeps through its frequency range. The volume control must be set at comfortable listening level for this test. While sounding, the LCD will display the following message.

* 43: FM Thru M3(PCM)	Output On
-----------------------	-----------

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

```
* 43: FM Thru M3(PCN) Output Off
```

44. TEST 44: FM SOUND OUTPUT THROUGH M3 IC (DIGITAL FILTER) TEST

```
* 44: FM Thru M3(FM)
```

SIGNAL PATH

A sine wave which is frequency swept by the EGM2 (2) IC will cause signals to be alternately output from OUTPUT L1, OUTPUT R1, OUTPUT L2 and OUTPUT R2 in a two channel pair sequence. The FMSEL signal to the EGM2 (2) must be at a 0 or LOW logic level and the OPS3 (2) must be at a 1 or HIGH logic level for this test. The appropriate data from EGM2 (2) IC (IC219) is sent to the OPS3 (2) IC (IC220) in order to produce the sound. The OPS3 (2) IC outputs the signals from pins 54 and 55 (SO0, channel 0 and SO1, channel 8) via IC225 (pins 8 and 9) to pins 27 and 28 (terminals DIIN0 and DIIN1) of the M3 (B) IC (IC256). The M3 (B) IC outputs the signals from pins 1 and 2 (INDV0, channel 14 and INDV1, channel 15) to pins 10 and 11 (IN1 and IN0 terminals) of the PAN (2) IC (IC215). The PAN (2) IC sends the signals out from pins 21 and 22 (S1 and S2 terminals) to pins 8 and 7 (MXI0 and MXI1 terminals) of MIX5 IC. The signal is sent out of the MIX5 IC via the MXO0 and the MXO1 terminals. This ultimately produces signal output from OUTPUT L1, OUTPUT R1, OUTPUT L2, OUTPUT R2.

ITEMS TO CHECK

Insert the appropriate 1/4" phone plug into OUTPUT L1 and observe the output waveform with an oscilloscope. Check that the level does not change excessively as the output sweeps through its frequency range. The volume control must be set at a comfortable listening level for this test. While sounding, the LCD will display the following message.

```
* 44: FM Thru M3(FM) Output On
```

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

```
* 44: FM Thru M3(FM) Output Off
```

45. TEST 45: FM SOUND OUTPUT FEEDBACK THROUGH M3 IC TEST

```
* 45: Feedback FM->M3
```

The basic signal path is the same as it was for TEST 43 except for the following:

The frequency swept sine wave produced by the EGM2 (1) and OPS3 (1) will be fed back from the M3 (A) IC to the OPS3 (1) IC. As in TEST 43, the output signals will occur alternately in a two channel pair sequence. For this test, the signals from pins 1 and 2 (INDV0 and INDV1 terminals) of M3 (A) IC (IC257) will be fed back to pins 69 and 70 (S10 and S11 terminals) of OPS3 (1) IC (IC218).

ITEMS TO CHECK

Insert the appropriate 1/4" phone plug into OUTPUT L1 and observe the output waveform with an oscilloscope. Check that the level does not change excessively as the output sweeps through its frequency range. It should be noted that due to the feedback condition of this test there may be a slight amount of distortion present in the output signal. The volume control must be set at a comfortable listening level for this test. While sounding, the LCD will display the following message:

* 45: Feedback FM->M3 Output On

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 45: Feedback FM Output Off

46. TEST 46: HIGH CLICK SOUND TEST

* 46: Click High

ITEMS TO CHECK

Check that a high click signal is properly output from OUTPUT L1, OUTPUT L2, OUTPUT R1 and OUTPUT R2. Make sure that the click volume control is set to maximum. While sounding, the LCD will display the following message:

* 46: Click High Click On

Verify that the high click signal is sent to each output by using an amplifier and speaker to monitor signal. Insert the appropriate 1/4" phone plugs into OUTPUT L1, OUTPUT L2, OUTPUT R1 and OUTPUT R2 and observe the output waveform with an oscilloscope. Check that the output waveform is a rounded square wave with an approximate peak-to-peak voltage of 500mV.

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 46: Click High Click Off

47. TEST 47: LOW CLICK SOUND TEST

* 47: Click Low

ITEMS TO CHECK

Check that a low click signal is properly output from OUTPUT L1. Make sure that the click volume control is set to maximum. While sounding, the LCD will display the following message:

* 47: Click Low Click On

Verify that the low click signal is sent to OUTPUT L1 by using an amplifier and speaker to monitor signal. Insert the appropriate 1/4" phone plugs into OUTPUT L1, OUTPUT L2, OUTPUT R1 and OUTPUT R2 and observe the output waveform with an oscilloscope. Check that the output waveform is a rounded square wave with an approximate peak-to-peak voltage of 500mV.

TEST END

Press [EXIT] to end the test. After pressing [EXIT] three things occur;

(1) the following display will appear, (2) the sound will stop and (3) the SY99 will wait for the entry of a test number.

* 47: Click Low Click Off

48. TEST 48: JACKS ALL OFF TEST

* 48: Jack All Off

Connect the Sustain and Foot Switch pedals to the appropriate jacks. With nothing connected to the Foot Volume, Foot Controller, and Breath controller jacks, check that the following display appears.

* 48: Jack All Off SU FS

Then while pressing the foot switches connected to the Sustain and Foot Switch jacks, remove the pedal plugs, and check that the display shows "OK".

DISPLAY OF TEST RESULTS

OK * 48: Jack All Off OK

NG * 48: Jack All Off FV NG

(e.g. if the foot volume jack is NG)

TEST END

The result is displayed and the test will end.

49. TEST 49: FACTORY SET TEST

* 49: Factory Set

This test is used to initialize the data listed below to the factory settings:

- Synthesizer system data
- 64-internal voice data
- 16-internal multi data
- Sequencer setup data

When this test is executed, the following display will appear.

* 49: Factory Set [NO] or [YES] ?

If you press [YES], the factory preset data will be restored.

If you press [NO], they will not be restored.

DISPLAY OF TEST RESULTS

If factory settings are restored.

OK * 49: Factory Set OK

If not restored there will be no change in the display as shown below.

* 49: Factory Set [NO] or [YES] ?

TEST END

The LCD displays the results, the factory preset data will be restored, and the test will then end. After the factory preset data has been restored, the system data will be as follows:

SY99

SYNTH

Note Shift.....+0
 Fine Turning+0
 Fixed Velocity.....off
 Velocity Curve0(normal)
 Assignable Foot Switch65
 Assignable Wheel13
 Edit Confirmon
 Kbd Trans Ch1

Voice Recv Ch.....1
 Local on/off.....on
 Note on/offall
 Device Numberall
 Bulk Protecton
 Program Change.....on
 Pattern Protecton
 MDR Memory.....0Kbyte

----- Greeting Message -----

"Create YOUR sound !"
 "...I'm ready"

----- PAN factory set -----

I1~I32 = P1 ~P32

----- MCT factory set -----

I 1 = P62(Far East)
 I 2 = P63(Blue)

----- VOICE -----

I-A01~D16 =P1-A01~D16

----- MULTI -----

I-01~16 =P1-01~16

SEQUENCER

record quantize0(off)
 click sw.....1(rec)
 click beat0(1/4)
 record typeover
 sync0(internal)
 receiveKBD
 filter velocity1(on)
 filter control change1(on)
 filter pitch bend1(on)
 filter program change1(on)
 filter after touch.....0(off)
 filter exclusive1(on)

midi control1(on)
 click/beat1/96
 accent1 value24
 accent2 value56
 accent3 value88
 accent4 value120
 gate type1(normal)

50. TEST 50: EXIT TEST PROGRAM

* 50: Exit

When this is executed, the following display will appear.

* 50: Exit [NO] or [YES] ?

To exit the test program mode, press the [YES] switch. To remain in the test program mode press the [NO] switch. This will cause the SY99 to wait for the entry of a test number.

DISPLAY OF TEST RESULTS

If test mode is not exited.

* 50: Exit [NO] or [YES] ?

■テストプログラム

A. テストエントリー

本体の電源立ち上げ後、数秒待ち、次の操作をする。

[VOICE]を押しながら[BANK D]を押しさらに[8]を押すと、次の画面が表示される。

```

*** SY99 TEST Ver #.## *** Please Select

Main ROM : Version #.# 1990-10-??
SEQ. ROM : Version #.# 1990-10-??

[-1] : AUTO          [+1] : MANUAL

[COPY] : Fact.set   [EXIT] : Exit

```

[-1]、[+1]、[COPY]および[EXIT]を使用してテストモードの選択を行う。

[+1]を押すと、マニュアルモードで、テストにエントリーされる。

[COPY]を押すと、“49. ファクトリーセット”を実行した後、自動的にテストモードから抜け、プレイモードになる。

[EXIT]を押すとテストモードを抜け、プレイモードになる。

B. テストの進め方

テストにエントリーすると、まず、次の画面が表示される。

```

*** SY99 TEST Ver #.## *** MODE : MANUAL

* 01 : ROM CHECK
  02 : RAM Read/Write
  03 : SEQENCER ROM
  04 : SEQENCER RAM
  05 : RAM Battery

```

[+1]、[-1]、[ENTER]、[COPY]、[PAGE+]、[PAGE-]、[EXIT]、[TEN KEY]およびロータリーエンコーダーを使用してテストを進める。

[+1]を押すと、現在選択されているテストの次のテストが実行される。

[-1]を押すと、現在選択されているテストの一つ前のテストが実行される。

[ENTER]を押すと、現在選択されているテストが実行される。

[PAGE+]を押すと、現在選択されているテストの次のテストが選択され、テスト項目が表示される。

[PAGE-]を押すと、現在選択されているテストの一つ前のテストが選択され、テスト項目が表示される。

[EXIT]を押すと、“50. EXIT”が実行される。

[TEN KEY 0]から[TEN KEY 9]を使用して、2桁の数字を入力することにより、テストの選択が行える。

バージョン表示モード；

[Voice]→[INT]→[1]を押す。MAIN ROMとSEQ ROMのバージョンが表示され、[EXIT]によって、本体プログラムへ復帰する。

NGと判断した時のテストの進めかた

次の各テストにおいて、NGと判断した場合、以下に示す操作により、テストナンバー待ち状態にでき、もう一度テストを行うか、他のテストを行う事が出来る。

9. パネルスイッチ
10. ピッチバンド
11. モジュールションホイール1
12. モジュールションホイール2

13. データエントリー
14. ロータリーエンコーダー
15. キーボード
16. アフタータッチ
17. MIDI IN/OUT
18. カードインサート
20. カードプロテクトスイッチ
22. ウェーブカードインサート
26. ディスクイジェクト
27. プレスコントロール
28. フットボリューム
29. フットコントロール
30. サステイーン
31. フットスイッチ
48. ジャック オール オフ

[EXIT]を押すと、テストナンバー待ち状態となる。

ただし、'9. パネルスイッチ'の[EXIT]のテストの場合には、この方法は、対応していない。

0. INITIAL TEST

テストに使用するH8内蔵RAM h'FB80-h'FF7F
DM2シート M3×2 よりのIRQ (DM2 BUS TEST)。
テストプログラムエントリー時、自動的に実施。

判定結果の表示

OK 特に無し
NG *WORK RAM ERROR

** IC128(RAM) ERROR,TEST CONTINUE? **

*M3 IRQ

* M3 IRQ CHECK ERROR,TEST CONTINUE? *

テストの終了方法

[YES]を押す

1. SYSTEM ROM

* 01; ROM CHECK

ROMのリードテストを行う。次の6つのアドレスに対してテストを行う。

IC111 =h'80000-h'8000F
IC111 =h'A0000-h'A000F
IC111 =h'C0000-h'C000F
IC111 =h'E0000-h'E000F
IC112 =h'60000-h'6000F
IC113 =h'10000-h'1000F
(ROMテストは16byteのみCheck)

判定結果の表示

OK * 01; ROM CHECK OK

NG * 01; ROM CHECK n:ICxxx NG

(ROM n がNGの場合、xxx : NGとなったICの番号)

テストの終了方法

判定を表示、出力して終了する。

2. SYSTEM RAM

* 02: RAM Read/Write

RAMのリード/ライトテストを行う。次のICのアドレスに対してテストを行う。

IC119 =h'40000-h'5FFFF

IC115 =h'30000-h'37FFF

判定結果の表示

OK	* 02: RAM Read/Write	OK
----	----------------------	----

NG	* 02: RAM Read/Write IC119	NG
----	----------------------------	----

テストの終了方法

判定を表示、出力して終了する。

すべてのRAMのデータは保存される。

3. SEQUENCER ROM

* 03: SEQUENCER ROM

ROMのリードテストを行う。次のICのアドレスに対してテストを行う。

IC101=h'8000-h'BFFFで#0-#6の7BANK

IC101=h'C000-h'FFFFで#7の1BANK

判定結果の表示

OK	* 03: SEQUENCER ROM	OK
----	---------------------	----

NG	* 03: SEQUENCER ROM	NG
----	---------------------	----

テストの終了方法

判定を表示、出力して終了する。

4. SEQUECER RAM

* 04: SEQUENCER RAM

RAMのリード/ライトテストを行う。次のICの全アドレスに対してテストを行う。

1: IC103=h'4000-h'5FFF

2: IC102=h'6000(WINDOW 0),h'6200(WINDOW 1),h'6400(WINDOW 2),h'6600(WINDOW 3)

判定結果の表示

OK	* 04: SEQUENCER RAM	OK
----	---------------------	----

NG	* 04: SEQUENCER RAM ICxxx	NG
----	---------------------------	----

(ICxxx:NGとなったICの番号)

テストの終了方法

判定を表示、出力して終了する。
すべての RAM のデータは保存される。

5. Battery

* 05: RAM Battery

RAMバックアップバッテリー電圧が、2.9V以上3.3V以下を確認する。

判定結果の表示

OK	* 05: RAM Battery	3.2V	OK
----	-------------------	------	----

NG	* 05: RAM Battery	#. #V	Low	NG
----	-------------------	-------	-----	----

* 05: RAM Battery	#. #V	High	NG
-------------------	-------	------	----

テストの終了方法

判定を表示、出力して終了する。

6. LCDドット黒

* 06: LCD All On

全ドットが黒に変化していることを確認する。コントラストVRで、コントラスト調整が出来る事。

テストの終了方法

[EXIT]を押すと、テストは終了し、次の画面を表示して、テストナンバー待ち状態になる。

* 06: LCD All On

7. LCDドット白

* 07: LCD All Off

全ドットが白に変化していることを確認する。

テストの終了方法

[EXIT]を押すと、テストは終了し、次の画面を表示して、テストナンバー待ち状態になる。

* 07: LCD All Off

8. LED順次点灯・全点灯

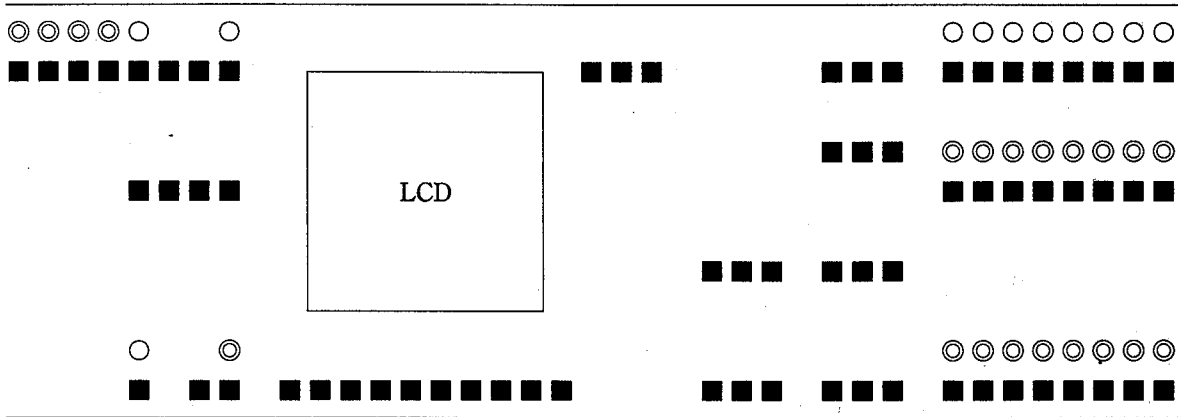
* 08: LED Check

次の図の左から順に赤LEDが1回点滅した後、すべての赤色のLEDが同時に点灯し、次に緑LEDが1回点滅した後、すべての緑色のLEDが同時に点灯することを確認する。現在点滅しているLEDが以下の様にLCDに表示される。

* 08: LED Check	REC	RED	On
-----------------	-----	-----	----

(RECORDの赤色のLEDが点滅している場合)

すべてのLEDが点滅することを確認する。(32個中21個は赤/緑の2色)



注) ◎印は、2色のLEDを示す。○印は、単色のLEDを示す。

テストの終了方法

[EXIT]を押すと、その時点の各LEDの点燈/消燈の状態のままテストは終了し、テストナンバー待ち状態になる。

9. パネルスイッチ

* 09: Panel Switch

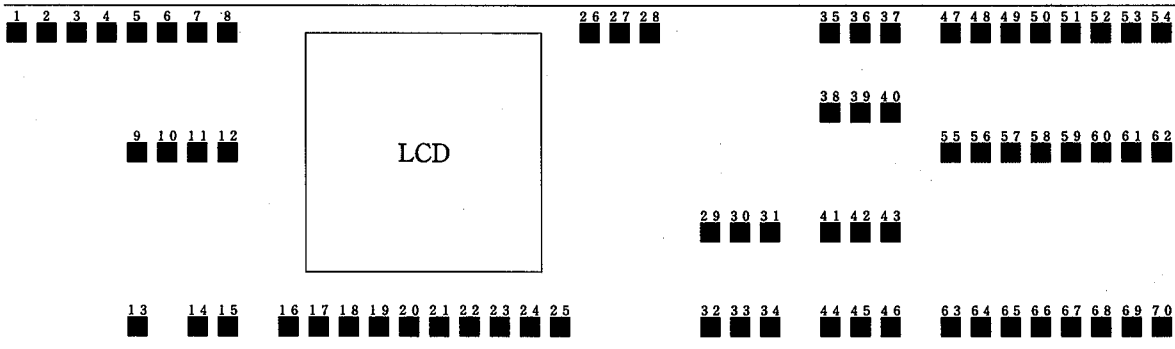
[VOICE]から[16]までのパネルスイッチを、以下の様なLCDの表示に従ってON/OFFする。

* 09: Panel Switch Push REC

([Record]のチェックの場合)

チェックの順序を下図に示す。

正常な場合、「ポーッ」と発音して、次のスイッチのテストに進む。
 期待されないコードがPKSより送られると、NGが表示され発音しない。
 その後、正しいコードが受信されると、次のスイッチのテストに進む。
 すべてのスイッチが正常であれば、OKが表示される。



判定結果の表示

OK * 09: Panel Switch Push 16 OK

NG * 09: Panel Switch Push REC !? Err

テストの終了方法

[16]までチェックすると、OKが表示され、テストは終了する。
 テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

10. ピッチベンド

* 10: Pitch Bend	50	99
------------------	----	----

ピッチベンドを、以下の様なLCD表示に従って50-99-00-50(中央-上-下-中央)と滑らかに動かす。

* 10: Pitch Bend	xx	yy
------------------	----	----

xx;現在のピッチベンドの値
yy;次の目標値

引っ掛りがなく、数字が滑らかに変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 10: Pitch Bend	50	50	OK
----	------------------	----	----	----

NG	* 10: Pitch Bend	xx	Center	NG
----	------------------	----	--------	----

テスト開始時および終了時にピッチベンドの値がCenterでなかった場合、xx:NG判定時のピッチベンドの値

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

11. モジュレーションホイール1

* 11: Modulation WH1	00	20-80
----------------------	----	-------

モジュレーションホイール1を、以下の様なLCD表示に従って00-20-80-99-20-80-00(下-上-下)と滑らかに動かす。

* 11: Modulation WH1	xx	yy
----------------------	----	----

* 11: Modulation WH1	xx	yy-zz
----------------------	----	-------

xx :現在のモジュレーションホイール1の値
yy, yy-zz :次の目標値

引っ掛りがなく、数字が滑らかに変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 11: Modulation WH1	00	00	OK
----	----------------------	----	----	----

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

12. モジュレーションホイール2

* 12: Modulation WH2	50	99
----------------------	----	----

検査を始める前にモジュレーションホイール2の位置を中央にセットしておく。

モジュレーションホイール2を、以下の様なLCD表示に従って50-99-00-50(中央-上-下-中央)と滑らかに動かす。

* 12: Modulation WH2	xx	yy
----------------------	----	----

* 12: Modulation WH2	xx	yy-zz
----------------------	----	-------

xx ;現在のモジュレーションホイール2の値

yy, yy-zz ;次の目標値

引っ掛りがなく、数字が滑らかに変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 12: Modulation WH2	50	50	OK
----	----------------------	----	----	----

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

13. データエントリー

* 13: Data Entry	00	20-80
------------------	----	-------

データエントリーを、以下の様なLCD表示に従って00-20-80-99-20-80-00(下-上-下)と滑らかに動かす。

* 13: Data Entry	xx	yy
------------------	----	----

* 13: Data Entry	xx	yy-zz
------------------	----	-------

xx ;現在のデータエントリーの値

yy, yy-zz ;次の目標値

引っ掛りがなく、数字が滑らかに変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 13: Data Entry	00	00	OK
----	------------------	----	----	----

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

14. ロータリーエンコーダー

* 14: R-Encoder	Right	00
-----------------	-------	----

ロータリーエンコーダーを、以下の様なLCD表示に従って00-01-00-01(右回転、左回転)と動かす。

* 14: R-Encoder	Right	xx
-----------------	-------	----

* 14: R-Encoder	Left	xx
-----------------	------	----

xx;現在の値

引っ掛りがなく、OKの判定が出ることを確認する。

判定結果の表示

OK	* 14: R-Encoder	Left	01	OK
----	-----------------	------	----	----

NG	表示なし
----	------

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

15. キーボード

* 15: Keyboard Check

E0からG6まで76鍵を、以下の様なLCDの表示に従い、イニシャルタッチh'10-h'6Fの強さでスケーリングする。

* 15: Keyboard Check	Push	C1
----------------------	------	----

(C1のチェックの場合)

正常な場合、キーON音を出力し、次のキーのチェックへ進む。

期待されないコードがPKSより送られると、NGが表示されSINE波は出力されないが、その後、正しいコードが受信されると、キーON音を出力し、次のキーのチェックへ進む。

すべてのキーが正常であれば、OKが表示される。

判定結果の表示

OK	* 15: Keyboard Check	Push	G6	OK
----	----------------------	------	----	----

NG	* 15: Keyboard Check	Push	xxx !? Err
----	----------------------	------	------------

(期待されないコードがPKSより送られて来た場合)

* 15: Keyboard Check	Push	xxx \$nn NG
----------------------	------	-------------

(イニシャルタッチが正常でない場合)

テストの終了方法

G6までチェックすると、OKが表示され、テストは終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

16. アフタータッチ

* 16: After Touch	00	20-80
-------------------	----	-------

アフタータッチを、以下の様なLCD表示に従って

00-20-80-99-20-80-00(弱-強-弱)と鍵盤を押す。

* 16: After Touch	xx	yy
-------------------	----	----

* 16: After Touch	xx	yy-zz
-------------------	----	-------

xx ; 現在のアフタータッチの値
yy, yy-zz ; 次の目標値

引っ掛かりがなく、数字が滑らかに変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 16: After Touch	00	00	OK
----	-------------------	----	----	----

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

17. MIDI IN/OUT

* 17: MIDI (I/O/I)

IN、OUTをMIDIケーブルで接続した後、テストを実行する。

LCDには、以下の様に表示が行われる。

* 17: MIDI (I/O/I)	Ix:yy	Rx:zz
--------------------	-------	-------

テストの終了方法

[EXIT]を押すと、テストは終了し、テストナンバー待ち状態になる。

期待されないデータが受信されたため、NGとなった場合、その時点でテストは終了する。

一定時間内に受信が終了しないため、NGとなった場合は、[EXIT]が押されるまでテストは続けられる。

18. カードインサート

* 18: D-Card Insert	0
---------------------	---

テスト用カードをスロットに挿入し、テストを実行する。カードを抜いて再度入れた時、数字が0から1に変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 18: D-Card Insert	1	OK
----	---------------------	---	----

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

19. カードリードライト

* 19: D-Card R/Write

テスト用カードを使用して、次の2つのアドレスに対してカードのリード/ライトテストを行う。

CARD 1 = h'20000 - h'27FFF

CARD 2 = h'28000 - h'2FFFF

メモリープロテクトをオフにしたテスト用カードを差し込み、テストを実行させる。

判定結果の表示

OK	* 19: D-Card R/Write CARD : 12	OK
----	--------------------------------	----

NG	* 19: D-Card R/Write CARD : x	NG
----	-------------------------------	----

(CARD 2が NG の場合)

テストの終了方法

判定を表示、出力して終了する。
すべてのCARD上のデータは保存される。

20. カードプロテクトスイッチ

* 20: D-Card Protect 0

テスト用カードを使用して、カードプロテクトスイッチの状態を読み込めることを確認する。スイッチを操作して、プロテクトオフからプロテクトオンの状態にした時、数字が0から1に変化しOKの判定が出ることを確認する。

判定結果の表示

OK	* 20: D-Card Protect 1	OK
----	------------------------	----

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。
テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

21. カードバッテリー

* 21: D-Card Battery

RAMカードバックアップバッテリー電圧が、測定できることを確認する。

判定結果の表示

OK	* 21: D-Card Battery #.#V	OK
----	---------------------------	----

NG	* 21: D-Card Battery #.#V Low	NG
----	-------------------------------	----

* 21: D-Card Battery #.#V High	NG
--------------------------------	----

テストの終了方法

判定を表示、出力して終了する。

22. ウェーブカードインサート

* 22: W-Card Insert 0

テスト用ウェーブカードをスロットに入れた時、数字が0から1に変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 22: W-Card Insert 1	OK
NG	表示なし	

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

23. ウェーブROMリード

* 23: W-ROM Read

本テストは、工業出荷検査用のため、ここでは実行しません。

24. ウェーブ RAM

* 24: W-RAM

インターナショナルWAVE RAM、拡張WAVE RAM のリード/ライトテストを行う。

テストするアドレスは、

インターナルWAVE RAM :h'400000-h'43FFFF	拡張WAVE RAM1 :h'440000-h'47FFFF
拡張 WAVE RAM2:h'480000-h'4BFFFF	拡張WAVE RAM3 :h'4C0000-h'4FFFFF
拡張 WAVE RAM4:h'500000-h'53FFFF	拡張WAVE RAM5 :h'540000-h'57FFFF

インターナルWAVE RAM、拡張WAVE RAMのバッテリーチェックを行う。

判定結果の表示

OK	* 24: W-RAM 000000	OK
NG	* 24: W-RAM XORBOX	NG

テスト結果は、インターナルWAVE RAM、拡張WAVE RAM 1~5 の順に記号で表示する。記号は、

R: リード/ライト テストのみNG	B: バッテリーチェックのみNG
O: 両方ともOK	X: 両方ともNG

テストの終了方法

判定を表示、出力して終了する。

すべてのRAMのデータは保存される。

25. ディスクリードライト

* 25: Disk Read/Write

生ディスクを使用して、ディスクのフォーマット/リード/ライトを2種類のDataに対してテストを行う。
次のトラックに対してテストを行う。

SIDE 0 = 40(SEC 4)-00(SEC 1)-79(SEC 9)

SIDE 1 = 40(SEC 4)-00(SEC 1)-79(SEC 9)

メモリープロテクトをオフにした生ディスクを差し込み、テストを実行させる。

判定結果の表示

OK * 25: Disk Read/Write C79:H1 Verify OK

NG * 25: Disk Read/Write Cyy:Hx nnnnnnn NG

(x ; side番号、yy ; トラック番号)

(nnnnnn ; エラー時の状態)

テストの終了方式

判定を表示、出力して終了する。

26. ディスクイジェクト

* 26: Disk Eject 0

生ディスクを挿入してテストを実行する。

イジェクトボタンを押しディスクを取り出した時、数字が0から1に変化し、OKの判定が出ることを確認する。

判定結果の表示

OK * 26: Disk Eject 1 OK

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

27. ブレスコントロール

* 27: Breath Control 99 00

ブレスコントローラーを接続し、以下の様なLCD表示に従って00-01-20-80-95-99-20-80-00-01(止-強-止)のシュミレーションをする。

* 27: Breath Control xx yy-zz

xx ; 現在のブレスコントロールの値

yy,yy-zz ; 次の目標値

数字が滑らかに変化し、OKの判定が出ることを確認する。

判定結果の表示

OK * 27: Breath Control xx 00 OK

xx ; テスト終了時のブレスコントロールの値

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

28. フットボリューム

* 28: Foot Volume	00	20-80
-------------------	----	-------

フットボリュームを接続し、以下の様なLCD表示に従って00-01-20-80-95-99-20-80-00-01(上げる-踏み込む-上げる)と滑らかに動かす。

* 28: Foot Volume	xx	yy-zz
-------------------	----	-------

xx ; 現在のフットボリュームの値
yy, yy-zz ; 次の目標値

数字が滑らかに変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 28: Foot Volume	xx	00	OK
----	-------------------	----	----	----

xx ; テスト終了時のフットボリュームの値

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

29. フットコントロール

* 29: Foot Control	00	20-80
--------------------	----	-------

フットコントロールを接続し、以下の様なLCD表示に従って00-01-20-80-95-99-20-80-00-01(上げる-踏み込む-上げる)と滑らかに動かす。

* 29: Foot Control	xx	yy-zz
--------------------	----	-------

xx ; 現在のフットコントロールの値
yy, yy-zz ; 次の目標値

数字が滑らかに変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 29: Foot Control	xx	00	OK
----	--------------------	----	----	----

xx ; テスト終了時のフットコントロールの値

NG 表示なし

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

30. サステイーン

* 30: Sustain	1
---------------	---

サステイーンペダルを接続し、ON/OFFする。この時、数字が1-0-1と変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 30: Sustain	1	OK
NG	表示なし		

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

31. フットスイッチ

* 31: Foot Switch	1
-------------------	---

フットペダルを接続し、ON/OFFする。この時、数字が1-0-1と変化し、OKの判定が出ることを確認する。

判定結果の表示

OK	* 31: Foot Switch	1	OK
NG	表示なし		

テストの終了方法

判定を表示、出力して終了する。

テスト途中で、NGと判断した場合の処理方法は、「B. テストの進めかた」を参照のこと。

32. 1kHz OUTPUT L1発音(FM)

* 32: 1KHz to L1-> L1

OUTPUT L1およびPHONES(L)より正常な信号が出力されていることを確認する。

なお信号源は、FMSEL=L-OPS3(IC220)のS00(CH0)-M3(IC256)のINDV1(CH13)-PAN(IC215)のS1, S2-MIX5(IC228)のMXO1,2のルートを使っている。

OUTPUT L1、OUTPUT L2、OUTPUT R1、OUTPUT R2、PHONES(L)、PHONES(R)共にジャックを差し込み、各出力の周波数、出力波形、出力レベルを周波数カウンタ、オシロスコープ、レベル計(12.47kHzフィルター付き)、歪率計で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 32: 1KHz to L1-> L1 Output On

チェック項目

OUTPUT L1 ; 1kHz±1.5Hz、sine波、歪率 0.2%、0.0±2dbm(負荷10kohm)

OUTPUT L2 ; -70dbm以下

OUTPUT R1 ; -70dbm以下

OUTPUT R2 ; -70dbm以下

PHONES(L) ; 1kHz、sine波、歪率 0.2%、+5.5±2dbm(負荷150ohm)

PHONES(R) ; -60dbm以下

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 32: 1KHz to L1-> L1 Output Off

33. 1kHz OUTPUT R1発音(FM)

```
* 33: 1KHz to R1-> R1
```

OUTPUT R1および PHONES(R) より正常な信号が出力されていることを確認する。

なお信号源は、TEST 32と同じ。

OUTPUT L1、OUTPUT L2、OUTPUT R1、OUTPUT R2、PHONES(L)、PHONES(R)共にジャックを差し込み、各出力の出力波形、出力レベルを、オシロスコープ、レベル計(12.47kHzフィルター付き)、歪率計で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

```
* 33: 1KHz to R1-> R1 Output On
```

チェック項目

OUTPUT R1 ; 1kHz、sine波、歪率 0.2%、 $0.0 \pm 2\text{dbm}$ (負荷10kohm)

OUTPUT L1 ; -70dbm 以下

OUTPUT R2 ; -70dbm 以下

OUTPUT L2 ; -70dbm 以下

PHONES(R) ; 1kHz、sine波、歪率0.2%、 $+5.5 \pm 2\text{dbm}$ (負荷150ohm)

PHONES(L) ; -60dbm 以下

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

```
* 33: 1KHz to R1-> R1 Output Off
```

34. 1kHz OUTPUT L2 発音(FM)

```
* 34: 1KHz to L2-> L2
```

OUTPUT L2およびPHONES(L)より正常な信号が出力されていることを確認する。

なお信号源は、TEST 32と同じ。

OUTPUT L1、OUTPUT L2、OUTPUT R1、OUTPUT R2、PHONES(L)、PHONES(R)共にジャックを差し込み、各出力の出力波形、出力レベルをオシロスコープ、レベル計(12.47kHzフィルター付き)、歪率計で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

```
* 34: 1KHz to L2-> L2 Output On
```

チェック項目

OUTPUT L2 ; 1kHz、sine波、歪率 0.2%、 $0.0 \pm 2\text{dbm}$ (負荷10kohm)

OUTPUT L1 ; -70dbm 以下

OUTPUT R1 ; -70dbm 以下

OUTPUT R2 ; -70dbm 以下

PHONES(L) ; 1kHz、sine波、歪率0.2%、 $+5.5 \pm 2\text{dbm}$ (負荷150ohm)

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

```
* 34: 1KHz to L2-> L2 Output Off
```

35. 1kHz OUTPUT R2 発音(FM)

* 35: 1kHz to R2-> R2

OUTPUT R2および PHONES(R)より正常な信号が出力されていることを確認する。

なお信号源は、TEST 32と同じ。

OUTPUT L1、OUTPUT L2、OUTPUT R1、OUTPUT R2、PHONES(L)、PHONES(R)共にジャックを差し込み、各出力の出力波形、出力レベルをオシロスコープ、レベル計(12.47kHzフィルター付き)、歪率計で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 35: 1kHz to R2-> R2 Output On

チェック項目

OUTPUT R2 ; 1kHz波、sine波、歪率0.2%、 $0.0 \pm 2\text{dbm}$ (負荷10kohm)

OUTPUT L1 ; -70dbm 以下

OUTPUT L2 ; -70dbm 以下

OUTPUT R1 ; -70dbm 以下

PHONES(R) ; 1kHz、sine波、歪率0.2%、 $+5.5 \pm 2\text{dbm}$ (負荷150ohm)

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 35: 1kHz to R2-> R2 Output Off

36. L MONO

* 36: 1kHz to L2-> L1

OUTPUT L2より出力されている信号がOUTPUT L2のプラグを抜いた時、OUTPUT L1より出力されることを確認する。なお信号源は、TEST 32と同じ。

OUTPUT L1にジャックを差し込み、出力波形、出力レベルを、オシロスコープ、レベル計(12.47kHzフィルター付き)で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 36: 1kHz to L2-> L1 Output On

チェック項目

OUTPUT L1 ; 1kHz、sine波、 $0.0 \pm 2\text{dbm}$ (負荷10kohm)

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 36: 1kHz to L2-> L1 Output Off

37. R MONO

* 37: 1kHz to R2-> R1

OUTPUT R2より出力されている信号がOUTPUT R2のプラグを抜いた時、OUTPUT R1より出力されることを確認する。なお信号源は、TEST 32と同じ。

OUTPUT R1にジャックを差し込み、出力波形、出力レベルを、オシロスコープ、レベル計(12.47kHzフィルター付き)で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 37: 1kHz to R2-> R1 Output Off

チェック項目

OUTPUT R1 ; 1kHz、sine波、0.0±2dbm (負荷10kohm)

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 37: 1KHz to R2-> R1 Output Off

38. L R MONO

* 38: 1KHz to R1-> L1

OUTPUT R1より出力されている信号がOUTPUT R1のプラグを抜いた時、OUTPUT L1より出力されることを確認する。なお信号源は、TEST 32と同じ。

OUTPUT L1にジャックを差し込み、出力波形、出力レベルを、オシロスコープ、レベル計 (12.47kHzフィルター付き) で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 38: 1KHz to R1-> L1 Output On

チェック項目

OUTPUT L1 ; 1kHz、sine波、0.0±2dbm (負荷10kohm)

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 38: 1KHz to R1-> L1 Output Off

39. PCM to Effect Parallel

* 39: PCM to Eff Para

Effectを通ったPCM側の信号が、OUTPUT L1,OUTPUT R1,OUTPUT L2,OUTPUT R2より正常に出力されていることを確認する。

なお信号源は、FMSEL=H-OPS3 (IC218)のSO0 (CH1)-M3 (IC257)のDIOUT1,2-DSP2 (IC226,IC227)のSO0,1-MIX5 (IC228)のMXO0,1のルートを使用。

DSP2は、それぞれのMOD,DRAMを通して出力されます。

2基のDSP2の接続はパラレルです。Effect部の信号経路は下記のとおり。

OUTPUT L1=MOD (IC221),DRAM (IC229-232),DSP2 (IC226)のSO0-MIX5のMXO0-L

OUTPUT R1=MOD (IC221),DRAM (IC232-235),DSP2 (IC226)のSO1-MIX5のMXO0-R

OUTPUT L2=MOD (IC223),DRAM (IC236-239),DSP2 (IC227)のSO0-MIX5のMXO0-L

OUTPUT R2=MOD (IC223),DRAM (IC239-242),DSP2 (IC227)のSO1-MIX5のMXO0-R

OUTPUT L1、OUTPUT R1、OUTPUT L2、OUTPUT R2 にジャックを差し込み (他はオープン)、出力波形、出力レベルを、オシロスコープ、レベル計 (12.47kHz フィルター付き)、歪率計で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 39: PCM to Eff Para Output On

チェック項目

OUTPUT L1 ; 1kHz、sine波、歪率0.3%、0.0±2dbm (負荷10kohm)

OUTPUT R1 ; 1kHz、sine波、歪率0.3%、0.0±2dbm (負荷10kohm)

OUTPUT L2 ; 1kHz、sine波、歪率0.3%、0.0±2dbm (負荷10kohm)

OUTPUT R2 ; 1kHz、sine波、歪率0.3%、0.0±2dbm (負荷10kohm)

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 39: PCM to Eff Para Output Off

40. FM to Effect Parallel

* 40: FM to Eff Para

Effectを通ったFM側の信号が、OUTPUT L1、OUTPUT L2より正常に出力されていることを確認する。
信号源は、FMSEL=L-OPS3(IC220)のSO0(CHO)-M3(IC256)のDIOU0,1-DSP2(IC226,227)のSO0,1のルートを使っています。

Effect部はTEST39と同じです。

OUTPUT L1、OUTPUT R1、OUTPUT L2、OUTPUT R2 にジャックを差し込み(他はオープン)、出力波形、出力レベルを、オシロスコープ、レベル計(12.47kHz フィルター付き)で観測する。

マスターボリュームは max とする。発音中は、LCD 表示が以下の様になる。

* 40: FM to Eff Para Output On

チェック項目

OUTPUT L1 ; 1kHz、sine波、0.0±2dbm(負荷10kohm)

OUTPUT L2 ; 1kHz、sine波、0.0±2dbm(負荷10kohm)

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 40: FM to Eff Para Output Off

41. FM to Effect Seri

* 41: FM to Eff Seri

シリアル接続されたエフェクトを通った信号がOUTPUT L1に正常に出力されている事を確認する。

尚信号源は、FMSEL=L-OPS3(IC220)のSO0(CHO) - M3(IC256)のDIOU0,1-DSP2(IC226)のSO2 - DSP2(IC227)のSO0 - MIX5(IC228)のMXO0のルートを使用。MOD、DRAMは使用していない。2基のDSP2の接続はシリアルです。

OUTPUT L1にジャックを差し込み(他はオープン)、出力波形、出力レベルをオシロスコープ、レベル計(12.47kHz フィルター付き)で観測する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下のようになる。

* 41: FM to Eff Seri Output On

チェック項目

OUTPUT L1 ; 1kHz、sine波、0.0±2dbm (負荷10kohm)

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 41: FM to Eff Seri Output Off

42. PCM Check

* 42: PCM Check

WAVE ROM-012000番地~01FFFF番地に記録されている音を、M3(IC257), INDV0端子(CH0)-PAN(IC216) S1, S2-PAN(IC215)S1, S2-MIX5(IC228)のMXO0,1を通してOUTPUT L1に出力する。
OUTPUT L1 にジャックを差し込み、OUTPUT L1の出力波形を観測する。
マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 42: PCM Check	Output On
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チェック項目
聴感

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 42: PCM Check	Output Off
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43. FM Thru M3(PCM)

* 43: FM Thru M3(PCM)

EGM2で周波数 SWEEPされた正弦波をL1, R1, L2, R2各端子より2ch分交互に出力する。音源は、FMSEL=H-EGM2(IC217)-FMSEL=H-OPS3(IC218)のSO0(CH1), SO1(CH9)-M3(IC257)のINDV0(CH5),INDV1(CH6)-PAN(IC216)のS1, S2-PAN(IC215)のS1, S2-MIX5(IC228)のMXO0,1のルートを使用している。
OUTPUT L1にジャックを差し込み、出力波形を観測する。又、レベルが周波数SWEEPに対し著しく変化しない事を確認する。
マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 43: FM Thru M3(PCM)	Output On
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チェック項目
聴感

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 43: FM Thru M3(PCM)	Output Off
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44. FM Thru M3(FM)

* 44: FM Thru M3(FM)

EGM2で周波数 SWEEPされた正弦波をL1, R1, L2, R2各端子より2ch分交互に出力する。音源は、FMSEL=L-EGM2(IC219)-FMSEL=H-OPS3(IC220)のSO0(CH0), SO1(CH8)-M3(IC256)のINDV0(CH14),INDV1(CH15)-PAN(IC215)のS1, S2-MIX5(IC228)のMXO0,1のルートを使用している。
OUTPUT L1にジャックを差し込み、出力波形を観測する。又、レベルが周波数SWEEPに対し著しく変化しない事を確認する。
マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 44: FM Thru M3(FM)	Output On
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チェック項目
聴感

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 44: FM Thru M3(FM) Output Off

45. Feedback FM- M3

* 45: Feedback FM->M3

EGM2で周波数 SWEEPされた正弦波が、M3を通してフィードバックされる。2ch分交互に出力される。

音源は、TEST 43と同じ、但しM3(IC257)のINDV0, INDV1の信号がOPS3(IC218)のSI0,SI1端子にフィードバックされる。

OUTPUT L1にジャックを差し込み、出力波形を観測する。

特にフィードバックされて歪みっぽい音である事を確認する。

マスターボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 45: Feedback FM->M3 Output On

チェック項目

聴感

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 45: Feedback FM Output Off

46. ハイクリック発音

* 46: Click High

OUTPUT L1, L2, R1, R2 より正常なハイクリック信号が出力されていることを確認する。

OUTPUT L1, OUTPUT R1, OUTPUT L2, OUTPUT R2共にジャックを差し込み、OUTPUT L1, R1, L2, R2 の出力波形をオシロスコープで観測する。

クリックボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 46: Click High Click On

チェック項目

聴感により、ハイクリックと判別がつくことを確認する。

出力波形が、なまった方形波であることを確認する。

500mVp-p

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 46: Click High Click Off

47. ロークリック発音

* 47: Click Low

OUTPUT L1より正常なロークリック信号が出力されていることを確認する。

OUTPUT L1, OUTPUT R1, OUTPUT L2, OUTPUT R2 共にジャックを差し込み、OUTPUT L1 の出力波形をオシロスコープで観測する。

クリックボリュームはmaxとする。発音中は、LCD表示が以下の様になる。

* 47: Click Low Click On

チェック項目

聴感により、ロックリッパと判別がつくことを確認する。

出力波形が、なまった方形波であることを確認する。

500mVp-p

テストの終了方法

[EXIT]を押すと、次の画面を表示し、発音は終了し、テストナンバー待ち状態になる。

* 47: Click Low Click Off

48. ジャック オールオフ

* 48: Jack All Off

サスティーン、フットスイッチはプラグを差したままで、フットボリューム、フットコントロール、プレスコントロールのジャックにプラグを差さない状態で、次の状態が表示される事。

* 48: Jack All Off SU FS

その後、サスティーン、フットスイッチのペダルを踏みながらプラグを抜いた時、“OK”が表示される事を確認する。

判定結果の表示

OK

* 48: Jack All Off

OK

NG

* 48: Jack All Off FV

NG

(フットボリュームのジャックがNGの場合)

テストの終了方法

判定を表示し、出力して終了する。

49. ファクトリーセット

* 49: Factory Set

次のデータを、イニシャルデータにセットする。

シンセサイザーシステム

インターナル64ボイス

インターナル16マルチ

シーケンサーセットアップ

テストを実行すると、次の画面が表示される。

* 49: Factory Set [NO] or [YES] ?

[YES]を押すと、ファクトリーセットされる。

[NO]を押すと、セットされない。

判定結果の表示

セットされた場合

* 49: Factory Set

OK

セットされなかった場合

* 49: Factory Set [NO] or [YES] ?

テストの終了方法

判定を表示、出力して終了する。

ファクトリーセット終了後、次に示すシステムデータがセットされる。

SYNTH

Note Shift.....+0
 Fine Turning+0
 Fixed Velocity.....off
 Velocity Curve0(normal)
 Assignable Foot Switch65
 Assignable Wheel13
 Edit Confirmon
 Kbd Trans Ch.....1
 Voice Recv Ch.....1
 Local on/off.....on
 Note on/offall
 Device Numberall
 Bulk Protecton
 Program Change.....on
 Pattern Protecton
 MDR Memory.....0Kbyte

----- Greeting Message -----

"Create YOUR sound !"
 "...I'm ready"

----- PAN factory set -----

I1~I32 = P1 ~P32

----- MCT factory set -----

I 1 = P62(Far East)
 I 2 = P63(Blue)

----- VOICE -----

I-A01~D16 =P1-A01~D16

----- MULTI -----

I-01~16 =P1-01~16

SEQUENCER

record quantize0(off)
 click sw1(rec)
 click beat0(1/4)

 record typeover
 sync0(internal)
 receiveKBD

 filter velocity1(on)
 filter control change1(on)
 filter pitch bend1(on)
 filter program change1(on)
 filter after touch0(off)
 filter exclusive1(on)

 midi control1(on)
 click/beat1/96

 accent1 value24
 accent2 value56
 accent3 value88
 accent4 value120
 gate type1(normal)

50. EXIT

* 50: Exit

テストを実行すると、次の画面が表示される。

* 50: Exit	[NO] or [YES] ?
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[YES]を押すと、テストモードから抜ける。
 [NO]を押すと、テストナンバー待ち状態になる。

判定結果の表示

テストモードから抜けなかった場合

* 50: Exit	[NO] or [YES] ?
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■ ERROR MESSAGES

MIDI

MIDI buffer full !

When the SY99 attempted to receive or transmit a large amount of MIDI data, its handling capacity was exceeded.

MIDI data error !

An error occurred when receiving MIDI data.

MIDI checksum error !

An error occurred when receiving bulk data.

Data empty !

Sequence data (bulk) was received, but the message contained no data.

Bulk rejected; sample exists !

An unoccupied sample number could not be found, so the bulk sample data was not received.

Song memory full !

When receiving sequence data (bulk), the internal memory capacity was exceeded, and not all the data was received.

Device number is off !

Since the device number is off, bulk data cannot be transmitted or received.

Device number mismatch !

Since the device numbers did not match, the bulk data was not received.

Bulk canceled !

While receiving or transmitting bulk data, EXIT was pressed to abort the operation.

Data card

Data card not ready !

The data card is not correctly inserted into the slot.

Card protected !

Since the memory protect switch of the card is on, data cannot be saved to the card.

Illegal format !

The card is the wrong format.

Verify error !

The data was not correctly saved.

Illegal size !

The data card is not of the 64 Kbyte variety accepted by the SY99.

Wave card

Wave card not ready !

The wave card is not correctly inserted into the slot.

Different wave card (ID=) !

The wave card which is inserted is not the one used by the voice or multi.

ID Number mismatch !

A multi includes voices which use two or more wave cards.

Disk

Disk not ready !

The disk is not correctly inserted into the disk drive.

Illegal change !

During the backup operation, the original and back up disks were inserted in the wrong order.

Illegal disk !

The data in the disk is faulty.

Bad disk !

The disk is faulty.

File not found !

The file was not found.

Write protected !

The disk is write protected.

Disk full !

There is no more memory available on the disk.

Directory full !

The directory area on the disk is full, and new files cannot be created.

Media type error !

The disk is the wrong type.

Illegal file !

The file is not for the SY99.

Song memory full !

The sequencer memory is full.

Sample memory full !

The sample memory area is full.

MDR memory full !

The MDR memory area is full.

Sequencer and display

Please stop sequencer !

The sequencer cannot play during disk or card loading or saving, during bulk data transmission, or during master control setting.

Illegal time !

You attempted to execute the Get Pattern operation, but the time signature was incorrect.

Illegal input !

You attempted to enter an invalid data value in Edit Insert mode.

Range is exceeded !

The parameter you specified in an edit job is beyond the valid range.

Data not found !

When you executed the Search Part operation in Chain Pattern, the specified data was not found.

Internal buffer full !

More sequence data was played back than could be sounded.

Battery

Change internal battery !

The internal backup battery needs to be replaced.

Change wave BAT !

The backup battery for the internal MDR/sample RAM area or an expansion memory board (as specified in the message) needs to be replaced.

Change card battery !

The card backup battery needs to be replaced.

Other

Use bank D !

The voice must be stored in bank D.

Only C1–C6 data valid !

You are attempting to save a drum voice to bank A, B, or C. Only the data for notes C1 through C6 will be saved as a result of this operation. Save the voice to bank D if you wish to save the data for all notes E0 through G6.

Illegal mark !

You attempted to mark a display page while using the compare function.

Use bank A–C !

The voice must be stored in bank A, B, or C.

Sample

Please allocate sample memory !

You attempted to enter Sample utility mode, but no memory has been allocated for sample use. Please use the System utility memory allocate function to allocate memory for sample use.

Not enough memory for sample !

You attempted to reduce the memory area allocated for sample use beyond the minimum required by currently existing samples. Please initialize the sample memory or delete samples before attempting to reduce the amount of MDR/sample RAM allocated for sample use.

Over internal waveform number !

You attempted to load a number of waveforms exceeding the internal memory maximum of 64.

Sample data not exists !

You attempted copy or save sample data from a sample number which does not contain any data.

Sample data protected !

Copy-protected sample data may not be saved to disk or transmitted via MIDI dump.

Over sample number !

You attempted to load a number of samples exceeding the internal memory maximum of 99.

MDR

Please allocate MDR memory !

You attempted to enter Sample utility mode, but no memory has been allocated for MDR use. Please use the System utility memory allocate function to allocate memory for MDR use.

Not enough memory for MDR !

You attempted to reduce the memory area allocated for MDR use beyond the minimum required by currently existing MDR data. Please initialize the MDR memory or delete MDR data before attempting to reduce the amount of MDR/sample RAM allocated for MDR use.

MDR data already exists !

You attempted to input data to an MDR number already occupied by data. Please choose a different MDR number as the destination for incoming data.

MDR data not found !

You attempted to output data from an empty MDR number.

■ エラーメッセージ

MIDI関係	
ディスプレイ表示	メッセージの内容
MIDI buffer full !	一度に多量のMIDIデータが送受信されたため、送受信ができません。データ量を減らしてください。
MIDI data error !	MIDIデータを受信した際、異常がありました。
MIDI checksum err !	バルクデータの受信の際、異常がありました。
Bulk canceled !	シーケンスバルクデータ受信中や、サンプルダンプスタンダード送信中に[EXIT]が押されたので、データの送受信を中止しました。
Bulk protected !	バルクプロテクトがオンになっているため、バルクデータの受信ができません。
Device number is off !	デバイスナンバーがオフになっているため、バルクデータの送受信ができません。
Device number mismatch !	デバイスナンバーのチャンネルが一致していないため、バルクデータの受信ができません。

データカード関係	
ディスプレイ表示	メッセージの内容
Data Card not ready !	カードが本体に正しくセットされていません。
Card protected !	カード自体のプロテクトスイッチがオンになっているため、カードへのデータの書き込みができません。
Illegal format !	カードのフォーマットが違います。
Verify error !	カードのセーブが正しく行われていません。
Illegal size !	カードの容量が64KByte ではありません。
Verity error !	カードの不良で、書き込みができません。

ウェイブカード関係	
ディスプレイ表示	メッセージの内容
Wave card not ready !	ウェイブカードが本体に正しくセットされていません。
Different wave card (ID=) !	プレイしようとしているボイスで使用されるべきウェイブフォームは、現在カードスロットにセットされているものと異なるウェイブフォームカードのものです。
ID Number mismatch !	同時には1つのウェイブフォームカードしか使用できないにもかかわらず、マルチを構成する各々のボイスの必要なウェイブカードが異なっているため正常に発音できません。

ディスク関係	
ディスプレイ表示	メッセージの内容
Disk not ready !	ディスクが本体に正しくセットされていません。
Illegal change !	バックアップ作業中に、新旧のディスクの順番を間違えて挿入しました。
Illegal disk !	ディスク内のデータ不良です。
Bad disk !	ディスク不良です。

File not found !	ファイルが見つかりません。
Write protected !	ディスクがプロテクトされています。
Disk full !	ディスクのメモリーが一杯です。
Directory full !	ディレクトリのエリアが一杯で、ファイルが作れません。
Media type error !	ディスクの種類が違います。
Illegal file !	本機用のファイルではありません。

シーケンサー関係	
ディスプレイ表示	メッセージの内容
Please stop sequencer !	ディスクやカードのロード、セーブまたは、バルク送信などは、シーケンサーのプレイ中には実行することはできません。
Illegal Time !	ゲットパターンを実行しようとしたが、設定されている拍子が異なっています。
Range is exceeded !	エディットジョブで指定したパラメータは設定できる範囲を超えています。
Data not Found !	チェインパターンでサーチパートを実行したが、目的のデータはありませんでした。
Illegal input !	エディットのインサートモードで入力しようとしたデータの値が正しくありません。
Internal buffer full !	シーケンサーを再生している時、シーケンスデータが多くて、すべてを発音することができません。(エラー処理の間、しばらくシーケンサーの発音が止まります)
Data Empty !	エディットジョブを実行しようとした小節には、データがありません。
Song Memory full !	シーケンサー用の内部メモリーが一杯です。エディットジョブの実行、MIDIの受信、ディスクのロードができません。

電池関係	
ディスプレイ表示	メッセージの内容
Change internal battery !	本体内のバックアップバッテリーが寿命です。
Change card battery !	カードのバックアップバッテリーが寿命です。
Change wave BAT ! ---	インターナル RAMまたは、増設 RAMボードのバックアップバッテリー (---の部分に表示) が寿命です。

サンプル関係	
ディスプレイ表示	メッセージの内容
Sample memory full !	サンプル領域が不足するため、データのロードができません。
Please allocate Sample memory !	サンプル領域が設定されていません。
Not enough memory for Sample !	サンプル領域が不足するため、メモリアロケートの設定を変更できません。
Sample data not exists !	指定されたサンプルナンバーにサンプルが存在しないため、バルクの送信、ディスクへのセーブ、サンプルのコピーができません。
Sample data protected !	サンプルにプロテクトがかかっているため、データのセーブ、バルク送信ができません。
Over internal waveform number !	ウェーブフォームの数が多すぎてロードできません。
Over Sample number !	サンプルの数が多すぎてロードできません。
Bulk rejected; Sample exists !	空きサンプルナンバーがないので、サンプルデータスタンダードを受信することができません。

MDR 関係	
ディスプレイ表示	メッセージの内容
MDR memory full !	MDR 領域が不足するため、データのロードができません。
Please allocate MDR memory !	MDR 領域が設定されていません。
Not enough memory for MDR !	MDR 領域が不足するため、メモリアロケートの設定を変更できません。
MDR data already exists !	既にデータが存在しているので、インプットできません。
MDR data not found !	指定された MDRナンバーにデータが存在しません。

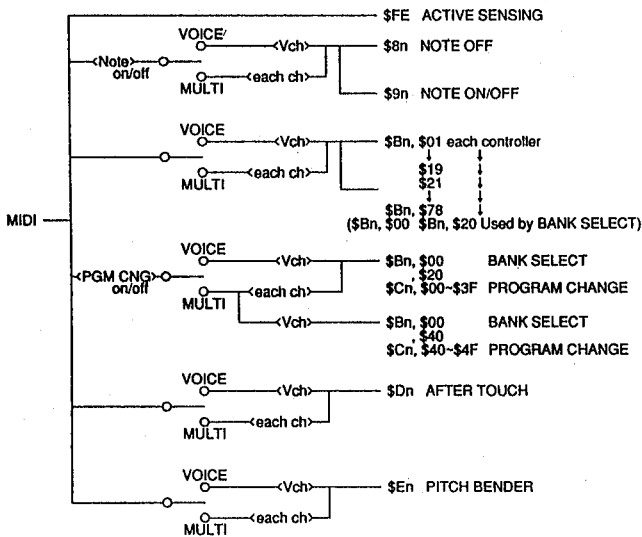
その他	
ディスプレイ表示	メッセージの内容
Use bank D !	<ul style="list-style-type: none"> ・ 4 エlementタイプのボイスは、バンクDにしかストアできません。 ・ Disk 1 Voice load の時、セーブ時にバンクDにあったボイスはバンクA~Cにしかロードできません。
Use bank A-C !	Disk 1 Voice load の時、セーブ時にバンクA-CにあったボイスはバンクA-Cにしかロードできません。
Illegal mark !	コンペア中のため、現在の画面には、マークすることはできません。
Only C1-C6 data valid !	ドラムセットボイスをバンクA~Cにストアすると、C1~C6のデータ以外は無効となります。

MIDI DATA FORMAT

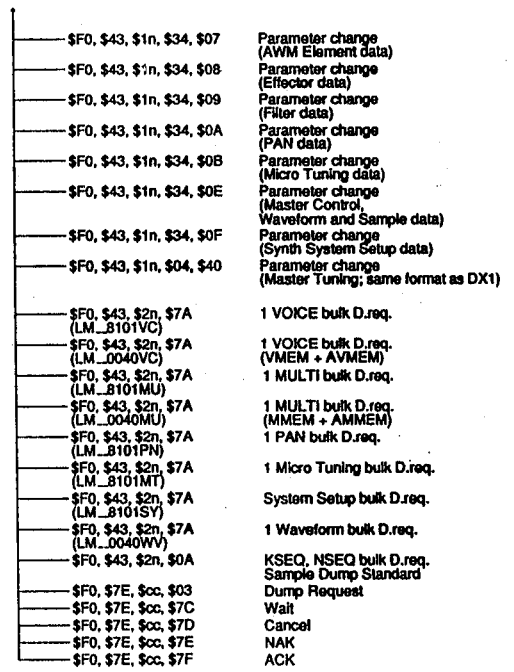
1. Synthesizer mode

1.1 MIDI reception/transmission block diagram

< MIDI reception conditions > 1/2
 Vch ... Voice Receive ch.



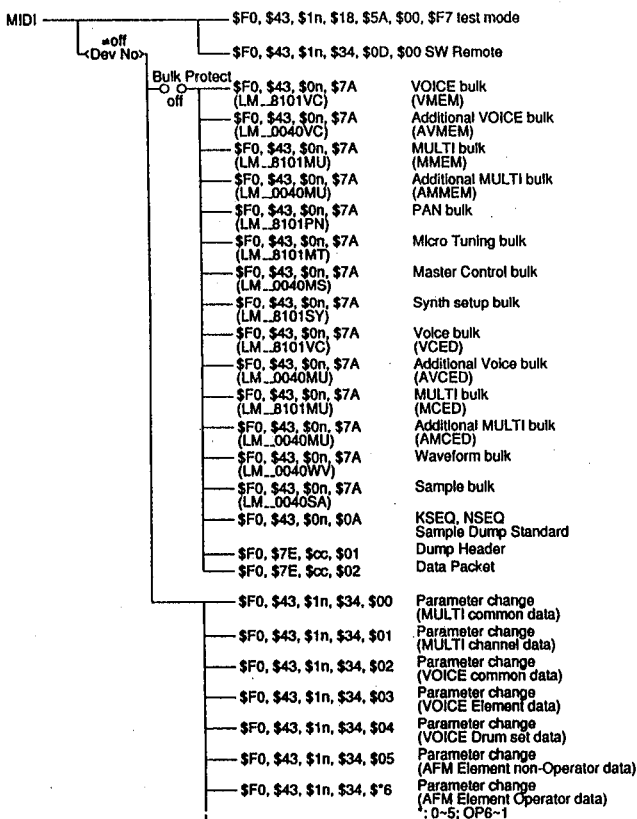
(continued on previous page)



Dev No = Device Number

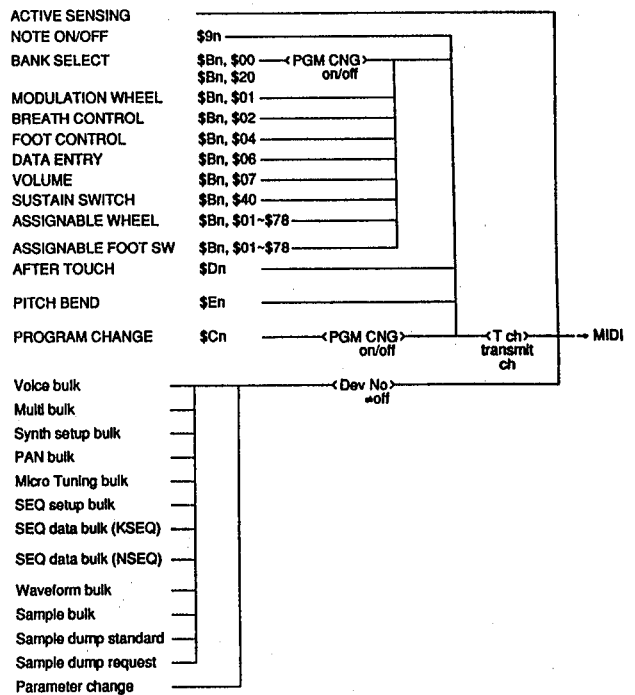
- VCED = Voice edit buffer
- AVCED = Additional Voice edit buffer
- MCED = Multi edit buffer
- AMCED = Additional Multi edit buffer
- VMEM = Voice memory
- AVMEM = Additional Voice memory
- MMEM = Multi memory
- AMMEM = Additional Multi memory

< MIDI reception conditions > 2/2



(continued on next page)

< MIDI transmission conditions >



1.2 Channel messages

1.2.1 Transmission

1.2.1.1 Note on/off

Transmission note range = C1 (36) ... C6 (96)
 Velocity range = 0 ... 127 (0: note off)

1.2.1.2 Control change

When the following controllers are moved MIDI data will be transmitted.

ctrl#	parameter	data rng
1	Modulation wheel	1-127
2	Breath control	1-127
4	Foot control	1-127
6	Data entry slider	1-127
7	Volume pedal	1-127
64	Sustain switch	1, 127
96	data increment Sw.	127
97	data decrement Sw.	127
0-120	Assignable wheel	1-127
0-120	Assignable foot sw	1, 127

← Transmitted in all modes other than Voice
 ← Play mode or Multi Play mode.

1.2.1.3 Program change

When a voice is selected in voice mode, or when a multi is selected in multi mode, a program change is transmitted. The program change number is assigned according to the mode, as follows.

		Bank No. (MSB), (LSB)	PGM CNG No.
Voice	Internal	1 (\$00), (\$00)	1-64 (\$00-\$3F)
	Card	2 (\$00), (\$01)	↓
	Preset 1	3 (\$00), (\$02)	↓
	Preset 2	6 (\$00), (\$05)	↓
Multi	Internal	17 (\$00), (\$10)	65-80 (\$40-\$4F)
	Card	18 (\$00), (\$11)	↓
	Preset	19 (\$00), (\$12)	↓
Voice in Multi	Internal	34 (\$00), (\$20)	1-64 (\$00-\$3F)
	Card	34 (\$00), (\$21)	↓
	Preset 1	35 (\$00), (\$22)	↓
	Preset 2	38 (\$00), (\$25)	↓

BANK SELECT \$Bn, \$00, \$00-\$7F (MSB)
 \$Bn, \$20, \$00-\$7F (LSB)
 PROGRAM CHANGE \$Cn, \$00-\$7F

No bank select or program change messages are transmitted if Program Change mode is set to OFF.

1.2.1.4 Pitch bend

Pitch bend is transmitted with 7 bit precision.

1.2.1.5 Aftertouch

Transmitted from MIDI when aftertouch is operated.

1.2.1.6 Channel mode messages

Channel mode messages are not transmitted.

1.2.2 Reception

1.2.2.1 Note on/off

Velocity note range = C-2 ... G8
 Velocity range = 1 ... 127 (note on only)

* A system setup parameter allows selection of
 normal = receive all note numbers
 odd = receive odd note numbers
 even = receive even note numbers

1.2.2.2 Control change

The following parameters can be controlled via MIDI.

cntrl#	parameter	data rng
1-120	Pitch Modulation	0...127
1-120	Amplitude Modulation	0...127
1-120	Filter Modulation	0...127
1-120	Pan LFO Modulation	0...127
1-120	Cutoff Bias	0...127
1-120	Pan Bias	0...127
1-120	EG Bias	0...127
1-120	Volume (assignable)	0...127
5	Portamento Time	0...127
7	Volume	0...127
64	Sustain Switch	0...127
65	Portamento Switch	0...127

Control #s 0(\$00) and 32(\$20) are used for bank select.

1.2.2.3 Program change

The SY99 responds as follows when a program change message is received:

- 1) If Program Change mode is OFF:
: Bank select and program change messages are not recognized.
- 2) If Program Change mode is ON:
: Response as described by the chart in 2.2.1.3. Voice selection is carried out upon reception of the program change message.

1.2.2.4 Pitch bend

Pitch bend is recognized by the MSB only.

1.2.2.5 Aftertouch

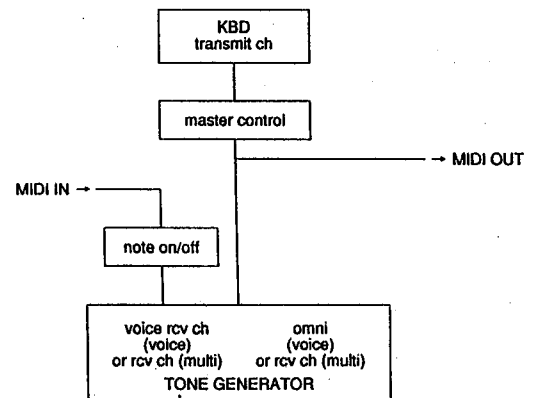
Channel aftertouch is recognized. Polyphonic aftertouch is not recognized.

1.2.2.6 Channel mode messages

Not received

1.2.3 Diagram of the keyboard section and tone generator section

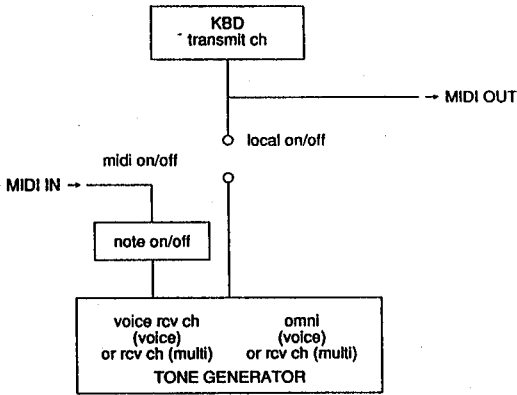
* Normal mode



Note 1) In voice mode, sound will be produced even if the Voice Receive Channel does not match the Keyboard Trans. Channel.

Note 2) Notes from the keyboard and notes from MIDI will be distinguished in polyphonic mode, and not distinguished in monophonic mode. Controller data from the keyboard and controller data from the MIDI (control change, aftertouch, pitch bend) will not be distinguished except for the sustain switch.

* Master control mode



2. System exclusive messages

2.1 Parameter changes

The SY99 is capable of transmitting and recognizing the following 17 types of parameter change message (with the exception of 14. Switch Remote, which is recognized but cannot be transmitted). Recognition of 14. Switch Remote has the same effect on the display as pressing the switch.

- 1). Multi Common Data parameter change
- 2). Multi Channel Data parameter change
- 3). Voice Common Data parameter change
- 4). Voice Element Data parameter change
- 5). Voice Drum Set Data parameter change
- 6). AFM Element Common Data parameter change
- 7). AFM Element Operator Data parameter change
- 8). AWM Element Data parameter change
- 9). Waveform Data parameter change
- 10). Effect Data parameter change
- 11). Filter Data parameter change
- 12). PAN Data parameter change
- 13). Micro Tuning Data parameter change
- 14). Switch Remote parameter change
- 15). Master Control Data parameter change
- 16). System Setup Data parameter change
- 17). Sample Data parameter change

Parameter message recognition can be turned off by turning the Device Number switch off. It cannot be turned off using any other MIDI switch.

2.1.1 Multi Common Data parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000000 00
00000000 00
00000000 00
00000000 00
000ppppp ppppp - Table 1-1, N2
00000000 00
0vvvvvvv vvvvvv - ascii
11110111 F7
    
```

This message modifies Multi Common data (multi name) one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : ignore.
- Voice Edit mode : ignore.
- Multi Play mode : move to Multi Edit mode and receive. (Display changes.)
- Multi Edit mode : receive. (Display changes.)
- Voice Edit mode in Multi : receive.
- Utility mode (Voice mode) : ignore.
- Utility mode (Multi mode) : move to Voice Edit mode and receive. (Display changes.)

2.1.2 Multi Channel Data parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000001 01
0000cccc cccc - Voice Channel Number
00000000 00
000ppppp ppppp - Table 1-2, N2
00000000 00
0vvvvvvv vvvvvv - Data Value
11110111 F7
    
```

This message modifies the channel data of a multi one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : ignore.
- Voice Edit mode : ignore.
- Multi Play mode : move to Multi Edit mode and receive. (Display changes.)
- Multi Edit mode : receive. (Display changes.)
- Voice Edit mode in Multi : receive.
- Utility mode (Voice mode) : ignore.
- Utility mode (Multi mode) : move to Voice Edit mode and receive. (Display changes.)

2.1.3 Voice Common Data parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000010 02
00000000 00
00000000 00
0ppppppp pppppp - Table 1-3, N2
00000000 00
0vvvvvvv vvvvvv - Data Value
11110111 F7
    
```

This message modifies voice common data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : move to Voice Edit mode and receive. (Display changes.)
- Voice Edit mode : receive. (Display changes.)
- Multi Play mode : ignore.
- Multi Edit mode : ignore.
- Voice Edit mode in Multi : receive. (Display changes.)
- Utility mode (Voice mode) : move to Voice Edit mode and receive. (Display changes.)
- Utility mode (Multi mode) : ignore.

2.1.4 Voice Element Data parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000011 03
0ee00000 ee - Element Number
00000000 00
0000pppp pppp - Table 1-4, N2
00000000 00
0vvvvvvv vvvvvv - Data Value
11110111 F7
    
```

This message modifies voice element data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : if the specified element exists, move to Voice Edit mode and receive. If not, ignore.
- Voice Edit mode : if the specified element exists, receive. if not, ignore. (The screen goes into Edit Mode.)
- Multi Play mode : ignore.
- Multi Edit mode : ignore.
- Voice Edit mode in Multi : same as for Voice Edit mode.
- Utility mode (Voice mode) : same as for Voice Play mode.
- Utility mode (Multi mode) : ignore.

SY99

2.1.5 Voice Drum Set Data parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000100 04
0mmmmmmm mmmmmmm - MIDI Note Number
00000000 00
0000pppp pppp - Table 1-5, N2
0000000v v - Data Value (MSB)
0vvvvvvv vvvvvvv - Data Value (LS7bits)
11110111 F7
    
```

This message modifies voice drum set data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : if originally a drum set, move to Voice Edit mode and receive. (Display changes.) If not, ignore. (The screen goes into Edit Mode.)
- Voice Edit mode : if originally a drum set, receive. (Display changes.) If not, ignore.
- Multi Play mode : ignore.
- Multi Edit mode : ignore.
- Voice Edit mode in Multi : same as for Voice Edit mode.
- Utility mode (Voice mode) : same as for Voice Play mode.
- Utility mode (Multi mode) : ignore.

2.1.6 AFM Element Common Data parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000101 05
0ee00000 ee - Element Number
00000000 00
0000pppp ppppp - Table 1-6, N2
00000000 00
0vvvvvvv vvvvvvv - Data Value
11110111 F7
    
```

This message modifies AFM element common data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : if the specified element exists and is AFM, move to Voice Edit mode and receive. (Display changes.) If not, ignore. (The screen goes into Edit Mode.)
- Voice Edit mode : if the specified element exists and is AFM, receive. If not, ignore. (Display changes.)
- Multi Play mode : ignore.
- Multi Edit mode : ignore.
- Voice Edit mode in Multi : same as Voice Edit mode.
- Utility mode (Voice mode) : same as Voice Play mode.
- Utility mode (Multi mode) : ignore.

2.1.7 AFM Element Operator Enable Parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000101 05
0ee00000 ee - Element Number
01111111 7F
01111111 7F
00000000 00
00vvvvvv vvvvvvv - Data Value
11110111 F7
    
```

This message modifies AFM operator enable data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : if the specified element exists and is AFM, move to Voice Edit mode and receive. (Display changes.) If not, ignore. (The screen goes into Edit Mode.)
- Voice Edit mode : if the specified element exists and is AFM receive. If not, ignore. (Display changes.)
- Multi Play mode : ignore.
- Multi Edit mode : ignore.
- Voice Edit mode in Multi : same as Voice Edit mode.
- Utility mode (Voice mode) : same as Voice Play mode.
- Utility mode (Multi mode) : ignore.

2.1.8 AFM Element Operator Data Parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000110 000 - 6 (Operator Number)
0ee00000 ee - Element Number
00000000 00
00pppppp pppppp - Table 1-7, N2
0000000v v - Data Value (MSB)
0vvvvvvv vvvvvvv - Data Value (LS7bits)
11110111 F7
    
```

This message modifies AFM operator parameter data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : if the specified element exists and is AFM, move to Voice Edit mode and receive. (Display changes.) If not, ignore. (The screen goes into Edit Mode.)
- Voice Edit mode : if the specified element exists and is AFM, receive. (Display changes.) If not, ignore.
- Multi Play mode : ignore.
- Multi Edit mode : ignore.
- Voice Edit mode in Multi : same as in Voice Edit mode.
- Utility mode (Voice mode) : same as in Voice Play mode.
- Utility mode (Multi mode) : ignore.

2.1.9 AWM Element Data parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000111 07
0ee00000 ee - Element Number
00000000 00
0pppppppp ppppppp - Table 1-8, N2
0000000v v - Data Value (MSB)
0vvvvvvv vvvvvvv - Data Value (LS7bits)
11110111 F7
    
```

This message modifies AWM element data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : if the specified element exists and is AWM, move to Voice Edit mode and receive. (Display changes.) If not, ignore. (The screen goes into Edit Mode.)
- Voice Edit mode : if the specified element exists and is AWM, receive. If not, ignore. (Display changes.)
- Multi Play mode : ignore.
- Multi Edit mode : ignore.
- Voice Edit mode in Multi : same as in Voice Edit mode.
- Utility mode (Voice mode) : same as in Voice Play mode.
- Utility mode (Multi mode) : ignore.

2.1.10 Waveform and Sample parameter change

Waveform parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000110 0E
00wwwwww wwwwww - Waveform Number
00000101 05
0pppppppp ppppppp - Table 1-9, N2
00000000 00
0vvvvvvv vvvvvvv - Data Value
11110111 F7
    
```

Sample parameter change

```

11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00000110 0E
0sssssss ssssss - Sample Number
00000101 06
0pppppppp ppppppp - Table 1-17, N2
00000000 00
0vvvvvvv vvvvvvv - Data Value
11110111 F7
    
```

These messages change a single parameter of waveform or sample data. These messages are recognized, and the data for the specified waveform or sample are changed, regardless of the SY99's current mode setting.

2.1.11 Effect Data parameter change

```
11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00001000 08
00000000 00
00000000 00
000ppppp ppppp - Table 1-10, N2
00000000 00
0vvvvvvv vvvvvv - Data Value
11110111 F7
```

This message modifies effect data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : move to Voice Edit mode and receive.
- Voice Edit mode : receive. (Display changes.)
- Multi Play mode : move to Multi Edit mode and receive.
- Multi Edit mode : receive. (Display changes.)
- Voice Edit mode in Multi : receive. Modify the Multi effect. (Display changes.)

- Utility mode (Voice mode) : same as in Voice Play mode.
- Utility mode (Multi mode) : same as in Multi Play mode.

2.1.12 Filter Data parameter change

```
11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00001001 09
0ee0ffff ee - Element Number, fff - filter Number
00000000 00
00pppppp pppppp - Table 1-11, N2
0000000v v - Data Value (MSB)
0vvvvvvv vvvvvv - Data Value (LS7bits)
11110111 F7
```

This message modifies filter data one parameter at a time. When this message is received, the following will occur.

- Voice Play mode : if the specified element exists, move to Voice Edit mode and receive. If not, ignore.
- Voice Edit mode : if the specified element exist, receive. If not, ignore. (Display changes.)
- Multi Play mode : ignore.
- Multi Edit mode : ignore.
- Voice Edit mode in Multi : same as in Voice Edit mode.
- Utility mode (Voice mode) : same as in Voice Play mode.
- Utility mode (Multi mode) : ignore.

2.1.13 PAN Data parameter change

```
11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00001010 0A
000mmmmm mmmmm - Memory Number
00000000 00
000ppppp ppppp - Table 1-12, N2
00000000 00
0vvvvvvv vvvvvv - Data Value
11110111 F7
```

This message modifies dynamic pan data one parameter at a time. When this message is received, the mode will not change, and the pan data of the specified memory will be modified.

2.1.14 Micro Tuning Data parameter change

```
11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00001010 08
000mmmmm mmmmm - Memory Number
0000000p p - Table 1-13, N1
0ppppppp pppppp - Table 1-13, N2
0vvvvvvv vvvvvv - Data Value (MS7bits)
0vvvvvvv vvvvvv - Data Value (LS7bits)
11110111 F7
```

This message modifies micro tuning data one parameter at a time. When this message is received, the mode will not change, and the micro tuning data of the specified memory will be modified.

2.1.15 Switch Remote parameter change

```
11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00001010 00
00000000 00
00000000 00
0ppppppp pppppp - Table 1-14, N2
00000000 00
0vvvvvvv vvvvvv - Data Value
11110111 F7
```

This message is only received, and allows remote control of all panel switches. The message will have the same effect as if that switch had been pressed.

2.1.16 Master Control Data parameter change

```
11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00001110 0E
00nn0ttt nttt - Table 1-15, T2
0ppppppp pppppp - Table 1-15, N1
0ppppppp pppppp - Table 1-15, N2
0vvvvvvv vvvvvv - Data Value
0vvvvvvv vvvvvv - Data Value
11110111 F7
```

This message modifies master control data one parameter at a time. When this message is received, the mode will not change.

2.1.17 System Setup Data parameter change

```
11110000 F0
01000011 43
0001nnnn nnnn - Device Number
00110100 34
00001111 0F
00000000 00
00000000 00
0ppppppp pppppp - Table 1-16, N2
00000000 00
0vvvvvvv vvvvvv - Data Value
11110111 F7
```

This message modifies system setup data one parameter at a time. When this message is received, the mode will not change.

3. Bulk dump

Reception is possible any time when not playing back or recording. The data is transmitted when the MIDI UTILITY "bulk dump" is executed or when a dump request is received.

3.1 Voice data bulk dump

There are nine types of voice data bulk dump as follows.

- 1). 1AFM Voice bulk dump
- 2). 2AFM Voice bulk dump
- 3). 4AFM Voice bulk dump
- 4). 1AWM Voice bulk dump
- 5). 2AWM Voice bulk dump
- 6). 4AWM Voice bulk dump
- 7). 1AFM_1AWM Voice bulk dump
- 8). 2AFM_2AWM Voice bulk dump
- 9). Drum_set Voice bulk dump

```
0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 01110100 7A
4 0bbbbbbb byte count
5 0bbbbbbb
6 01001100 4C(ascii"L")
7 01001101 4D(ascii"M")
8 00100000 20(ascii" ")
9 00100000 20(ascii" ")
10 00110000 38(ascii"8")
11 00110001 31(ascii"1")
12 00110000 30(ascii"0")
13 00110001 31(ascii"1")
14 01010110 56(ascii"V")
15 01000011 43(ascii"C")
16 00000000 00
↓ ↓
29 00000000 00
30 0ttttttt tttttt - Memory_type
31 00mmmmmm mmmmmm - Memory Number
32 0ddddd dddddd - Data
↓ ↓
0sssssss ssssss - check_sum
11110111 F7
```

MIDI Utility 1 Voice Bulk transmission

Memory_type = 7F
Memory Number = 00

MIDI Utility Vc & Mlt Bulk or 64 Voices Bulk transmission

Memory_type = 00 (INT)
Memory Number = start from 0 and transmit consecutively to 63 (During Vc & Mlt, start from Multi 0 and transmit consecutively to 15.)

1 Voice Bulk transmission by Dump Request

Memory_type = 00 (INT)
02 (PRESET1)
03 (PRESET2)
7F (Edit_Buffer)
Memory Number = 0-63 (the number specified by the Dump Request)

For reception, data will be processed as Edit_buffer when the Memory_type is 7F, and as INTERNAL for other cases.

* For details of the bulk dump data and dump request format, refer to table 2-1.

For transmission at the main unit panel operation, additional voice data is also transmitted. When a dump request is received, only voice data is transmitted.

3.2 Additional voice data bulk dump

- 1). 1AFM & 1AWM Voice bulk dump
- 2). 2AFM & 2AWM & 1AFM_1AWM Voice bulk dump
- 3). 4AFM & 4AWM & 2AFM_2AWM Voice bulk dump
- 4). Drum_set Voice bulk dump

0	11110000	F0
1	01000011	43
2	0000nnnn	nnnn - Device Number
3	01111010	7A
4	0bbbbbbb	byte count
5	0bbbbbbb	
6	01001100	4C(ascii"L")
7	01001101	4D(ascii"M")
8	00100000	20(ascii" ")
9	00100000	20(ascii" ")
10	00110000	30(ascii"0")
11	00110000	30(ascii"0")
12	00110100	34(ascii"4")
13	00110000	30(ascii"0")
14	01010100	56(ascii"V")
15	01000011	43(ascii"C")
16	00000000	00
↓	↓	↓
29	00000000	00
30	0ttttttt	tttttt - Memory_type
31	0mmmmmm	mmmmmm - Memory Number
32	0ddddd	ddddd - data
↓	↓	↓
	0sssssss	sssssss - check_sum
	11110111	F7

MIDI Utility 1 Voice Bulk transmission

Memory_Type = 7F
Memory Number = 00

MIDI Utility Vc & Mlt Bulk or 64 Voice Bulk transmission

Memory_type = 00 (INT)
Memory Number = start from 0 and transmit consecutively to 63 (During Vc & Mlt, start from Multi 0 and transmit consecutively to 15.)

1 Voice Bulk transmission by Dump Request

Memory_type = 00 (INT)
02 (PRESET1)
03 (PRESET2)
Memory Number = 0-63 (the number specified by the Dump Request)

For reception, data will be processed as Edit_buffer when the Memory_type is 7F, and as INTERNAL for other cases.

* For details of the bulk dump data and dump request format, refer to table 2-2.

For transmission at the main unit panel operation or at the reception of a dump request, voice data is also transmitted.

3.3 Multi data bulk dump

0	11110000	F0
1	01000011	43
2	0000nnnn	nnnn - Device Number
3	01111010	7A
4	0bbbbbbb	byte count
5	0bbbbbbb	
6	01001100	4C(ascii"L")
7	01001101	4D(ascii"M")
8	00100000	20(ascii" ")
9	00100000	20(ascii" ")
10	00110000	30(ascii"0")
11	00110000	30(ascii"0")
12	00110100	34(ascii"4")
13	00110000	30(ascii"0")
14	01001101	4D(ascii"M")
15	01010101	55(ascii"U")
16	00000000	00
↓	↓	↓
29	00000000	00
30	0ttttttt	tttttt - Memory_type
31	0mmmmmm	mmmmmm - Memory Number
32	0ddddd	ddddd - data
↓	↓	↓
	0sssssss	sssssss - check_sum
	11110111	F7

MIDI Utility 1 Multi Bulk transmission

Memory_type = 7F
Memory Number = 00

MIDI Utility Vc & Mlt Bulk or 16 Multi Bulk transmission

Memory_type = 00 (INT)
Memory Number = start from 0 and transmit consecutively to 15 (During Vc & Mlt, start from Voice 0 and transmit consecutively to 63.)

1 Multi Bulk transmission by Dump Request

Memory_type = 00 (INT)
02 (PRESET)
Memory Number = 0-15 (the number specified by the Dump Request)

For reception, data will be processed as Edit_buffer when the Memory_type is 7F, and as INTERNAL for other cases.

* For details of the bulk dump data and dump request format, refer to table 3-1.

For transmission at the main unit panel operation, additional voice data is also transmitted. When a dump request is received, only multi data is transmitted.

3.4 Additional multi data bulk dump

0	11110000	F0
1	01000011	43
2	0000nnnn	nnnn - Device Number
3	01111010	7A
4	0bbbbbbb	byte count
5	0bbbbbbb	
6	01001100	4C(ascii"L")
7	01001101	4D(ascii"M")
8	00100000	20(ascii" ")
9	00100000	20(ascii" ")
10	00110000	30(ascii"0")
11	00110000	30(ascii"0")
12	00110100	34(ascii"4")
13	00110000	30(ascii"0")
14	01001101	4D(ascii"M")
15	01010101	55(ascii"U")
16	00000000	00
↓	↓	↓
29	00000000	00
30	0ttttttt	tttttt - Memory_type
31	0mmmmmm	mmmmmm - Memory Number
32	0ddddd	ddddd - data
↓	↓	↓
	0sssssss	sssssss - check_sum
	11110111	F7

MIDI Utility 1 Multi Bulk transmission

Memory_type = 7F
Memory Number = 00

MIDI Utility Vc & Mlt Bulk or 16 Multi Bulk transmission

Memory_type = 00 (INT)
 Memory Number = start from 0 and transmit consecutively to 15. (During Vc & Mlt, start from Voice 0 and transmit consecutively to 63.)

1 Multi Bulk transmission by Dump Request

Memory_type = 00 (INT) 02 (PRESET)
 Memory Number = 0~15 (the number specified by the Dump Request)

For reception, data will be processed as Edit_buffer when the Memory_type is 7F, and as INTERNAL for other cases.

* For details of the bulk dump data and dump request format, refer to table 3-2.

For transmission at the main unit panel operation or at the reception of a dump request, voice data is also transmitted.

3.5 Pan data bulk dump

0	11110000	F0
1	01000011	43
2	0000nnnn	nnnn - Device Number
3	01111010	7A
4	0bbbbbbb	byte count
5	0bbbbbbb	
6	01001100	4C(ascii"L")
7	01001101	4D(ascii"M")
8	00100000	20(ascii" ")
9	00100000	20(ascii" ")
10	00111000	38(ascii"8")
11	00111001	31(ascii"1")
12	00110000	30(ascii"0")
13	00110001	31(ascii"1")
14	01010000	50(ascii"P")
15	01001110	4E(ascii"N")
16	00000000	00
↓	↓	↓
29	00000000	00
30	0ttttttt	tttttt - Memory_type
31	00mmmmmm	mmmmmm - Memory Number
32	0ddddd	ddddd - data
↓	↓	↓
	0sssssss	sssssss - check_sum
	11110111	F7

MIDI Utility Vc & Mlt Bulk or PAN Bulk transmission

Memory_type = 00 (INT)
 Memory Number = start from 0 and transmit consecutively to 31

PAN Bulk transmission by Dump Request

Memory_type = 00 (INT) 02 (PRESET)
 Memory Number = 0~63 (the number specified by the Dump Request)

For reception, bits 5 and 6 of Memory_type and memory Number will be ignored, and the data will be processed as INTERNAL.

* For details of the bulk dump data and dump request format, refer to table 4.

3.6 Micro tuning data bulk dump

0	11110000	F0
1	01000011	43
2	0000nnnn	nnnn - Device Number
3	01111010	7A
4	0bbbbbbb	byte count
5	0bbbbbbb	
6	01001100	4C(ascii"L")
7	01001101	4D(ascii"M")
8	00100000	20(ascii" ")
9	00100000	20(ascii" ")
10	00111000	38(ascii"8")
11	00111001	31(ascii"1")
12	00110000	30(ascii"0")
13	00110001	31(ascii"1")
14	01001101	4D(ascii"M")
15	01010100	54(ascii"Y")
16	00000000	00
↓	↓	↓
29	00000000	00
30	00000000	00 - Memory_type
31	00mmmmmm	mmmmmm - Memory Number
32	0ddddd	ddddd - data
↓	↓	↓
	0sssssss	sssssss - check_sum
	11110111	F7

MIDI Utility Vc & Mlt Bulk or Micro Tuning Bulk transmission

Memory_type = 00 (INT)
 Memory Number = start from 0 and transmit consecutively to 1

MCT Bulk transmission by Dump Request

Memory_type = 00 (INT) 02 (PRESET)
 Memory Number = 0~63 (the number specified by the Dump Request)

For reception, bits 1~6 of Memory_type and Memory Number will be ignored, and the data will be processed as INTERNAL.

* For details of the bulk dump data and dump request format, refer to table 5.

3.7 Master control data bulk dump

0	11110000	F0
1	01000011	43
2	0000nnnn	nnnn - Device Number
3	01111010	7A
4	0bbbbbbb	byte count
5	0bbbbbbb	
6	01001100	4C(ascii"L")
7	01001101	4D(ascii"M")
8	00100000	20(ascii" ")
9	00100000	20(ascii" ")
10	00111000	38(ascii"8")
11	00111001	31(ascii"1")
12	00110100	34(ascii"4")
13	00110000	30(ascii"0")
14	01001101	4D(ascii"M")
15	01001011	53(ascii"S")
16	00000000	00
↓	↓	↓
31	00000000	00
32	0ddddd	ddddd - data
↓	↓	↓
	0sssssss	sssssss - check_sum
	11110111	F7

* For details of the bulk dump data and dump request format, refer to table 6.

Data transmitted at the main unit panel operation is followed by SYNTH system setup data when it is transmitted.

For transmission at the reception of a dump request, only master control data is transmitted.

3.8 SYNTH system setup data bulk dump

0	11110000	F0
1	01000011	43
2	0000nnnn	nnnn - Device Number
3	01111010	7A
4	0bbbbbbb	byte count
5	0bbbbbbb	
6	01001100	4C(ascii"L")
7	01001101	4D(ascii"M")
8	00100000	20(ascii" ")
9	00100000	20(ascii" ")
10	00111000	38(ascii"8")
11	00111001	31(ascii"1")
12	00110000	30(ascii"0")
13	00110001	31(ascii"1")
14	01010011	53(ascii"S")
15	01010011	59(ascii"Y")
16	00000000	00
↓	↓	↓
31	00000000	00
32	0ddddd	ddddd - data
↓	↓	↓
	0sssssss	sssssss - check_sum
	11110111	F7

* For details of the bulk dump data and dump request format, refer to table 7.

For transmission at the main unit panel operation, master control data is transmitted before this data.

For transmission at the reception of a dump request, only this data is transmitted.

3.9 SEQ setup data bulk dump

```

0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 01111110 7E
4 0bbb0000 ] byte count
5 0bbb0000 ]
6 01001100 4C(ascii"L")
7 01001101 4D(ascii"M")
8 00100000 20(ascii" ")
9 00100000 20(ascii" ")
10 00110000 38(ascii"8")
11 00110001 31(ascii"1")
12 00110000 30(ascii"0")
13 00110001 31(ascii"1")
14 01010011 53(ascii"S")
15 01010011 53(ascii"S")
16 0ddddddd ddddddd - data
↓
0sssssss sssssss - check_sum
11110111 F7
    
```

* For details of the bulk dump data, refer to table 8-1.

Dump request

```

0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 01111110 7E
4 01001100 4C(ascii"L")
5 01001101 4D(ascii"M")
6 00100000 20(ascii" ")
7 00100000 20(ascii" ")
8 00110000 38(ascii"8")
9 00110001 31(ascii"1")
10 00110000 30(ascii"0")
11 00110001 31(ascii"1")
12 01010011 53(ascii"S")
13 01010011 53(ascii"S")
14 11110111 F7
    
```

3.10 SEQ song and pattern data bulk dump (KSEQ)

Reception is possible at any time when not playing or recording. This data is transmitted when the MIDI UTILITY "bulk dump" is executed, or when a dump request is received.

```

0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 00001010 0A
4 0bbb0000 ] byte count
5 0bbb0000 ] max. 4096
6 01001100 4C(ascii"L")
7 01001101 4D(ascii"M")
8 00100000 20(ascii" ")
9 00100000 20(ascii" ")
10 01001011 48(ascii"K")
11 01010011 53(ascii"S")
12 01000101 45(ascii"E")
13 01010010 51(ascii"O")
14 00100000 20(ascii" ")
15 00100000 20(ascii" ")
16 0ddddddd ddddddd - data
↓
0sssssss sssssss - check_sum
11110111 F7
    
```

When the byte count is below 4096, the byte count is that count. When the byte count exceeds 4096, a byte count and check_sum operation is repeated for every 4096 bytes marked off from the top.

Dump request

```

0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 00001010 0A
4 01001100 4C(ascii"L")
5 01001101 4D(ascii"M")
6 00100000 20(ascii" ")
7 00100000 20(ascii" ")
8 01001011 48(ascii"K")
9 01010011 53(ascii"S")
10 01000101 45(ascii"E")
11 01010010 51(ascii"O")
12 00100000 20(ascii" ")
13 00100000 20(ascii" ")
14 11110111 F7
    
```

3.11 SEQ song and pattern data bulk dump (NSEQ)

Reception is possible at any time when not playing or recording. This data is transmitted when the MIDI UTILITY "bulk dump" is executed, or when a dump request is received.

```

0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 00001010 0A
4 0bbb0000 ] byte count
5 0bbb0000 ] max. 4096
6 01001100 4C(ascii"L")
7 01001101 4D(ascii"M")
8 00100000 20(ascii" ")
9 00100000 20(ascii" ")
10 01001110 4E(ascii"N")
11 01010011 53(ascii"S")
12 01000101 45(ascii"E")
13 01010010 51(ascii"O")
14 00100000 20(ascii" ")
15 00100000 20(ascii" ")
16 0ddddddd ddddddd - data
↓
0sssssss sssssss - check_sum
11110111 F7
    
```

When the byte count is below 4096, the byte count is that count. When the byte count exceeds 4096, a byte count and check_sum operation is repeated for every 4096 bytes marked off from the top.

Dump request

```

0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 00001010 0A
4 01001100 4C(ascii"L")
5 01001101 4D(ascii"M")
6 00100000 20(ascii" ")
7 00100000 20(ascii" ")
8 01001110 4E(ascii"N")
9 01010011 53(ascii"S")
10 01000101 45(ascii"E")
11 01010010 51(ascii"O")
12 00100000 20(ascii" ")
13 00100000 20(ascii" ")
14 11110111 F7
    
```

3.12 Waveform data bulk dump

```

0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 01111010 7A
4 0bbb0000 ] byte count
5 0bbb0000 ]
6 01001100 4C(ascii"L")
7 01001101 4D(ascii"M")
8 00100000 20(ascii" ")
9 00100000 20(ascii" ")
10 00110000 30(ascii"0")
11 00110000 30(ascii"0")
12 00110100 34(ascii"4")
13 00110000 30(ascii"0")
14 01010111 57(ascii"W")
15 01010110 56(ascii"V")
16 00000000 00
↓
29 00000000 00
30 00000ttt ttt - Memory_type
31 0mmmmmmm mmmmmm - Memory Number
32 0ddddddd ddddddd - data
↓
0sssssss sssssss - check_sum
11110111 F7
    
```

Dump request

```

0 11110000 F0
1 01000011 43
2 0010nnnn nnnn - Device Number
3 01111010 7A
4 01001100 4C(ascii"L")
5 01001101 4D(ascii"M")
6 00100000 20(ascii" ")
7 00100000 20(ascii" ")
8 00110000 30(ascii"0")
9 00110000 30(ascii"0")
10 00110100 34(ascii"4")
11 00110000 30(ascii"0")
12 01010111 57(ascii"W")
13 01010110 56(ascii"V")
14 00000000 00
↓
27 00000000 00
28 00000ttt ttt - Memory_type
29 0mmmmmmm mmmmmm - Memory Number
30 11110111 F7
    
```

The Sample utility Waveform Bulk Dump operation transmits the following information:

Memory_type = \$00 (INT)
 Memory number = \$00 to \$3F, in sequence

A bulk dump in response to a dump request message transmits the following:

Memory_type = \$00 (INT), \$01 (CARD), \$02, \$03 (PRE1), \$04 (PRE2)
 Memory number = a number from \$00 to \$3F, as specified by the dump request

	Preset 1, numbers 1 to 128	Preset 1, numbers 129 to 155
Memory_type	\$02	\$03
Memory Number	\$00-\$7F	\$00-\$1A

Requests for nonexistent numbers are ignored.

Incoming bulk dumps are handled as INTERNAL memory; bit 6 of Memory_type and Memory Number are ignored.

* Refer to attached Table 9-1 for details regarding bulk dump data format.

4. Sample dumps

The SY99 is capable of handling two sample dumps in two formats: the Sample Dump Standard, and the SY99 Sample Bulk Dump. Sample dumps in both of these formats are recognized. Sample dumps in both are transmitted when the Sample utility Sample Dump operation is executed, and in response to a Sample Dump Standard Dump Request. When an SY99 Sample Bulk Dump Request is received, the SY99 Sample Bulk Dump only is transmitted. The upper limit of sample numbers in either format is set at \$62; sample numbers higher than \$62 are treated as \$62.

Sample Dump Standard

```
DUMP REQ  F0. 7E. cc. 03. ss. $$. F7
ACK       F0. 7E. cc. 7E. dd. F7
MARK     F0. 7E. cc. 7E. dd. F7
CANCEL   F0. 7E. cc. 7D. dd. F7
WAIT     F0. 7E. cc. 7C. dd. F7
DATA PACKET F0. 7E. cc. 02. kk. <120 byte>. 11. F7
DUMP HEADER F0. 7E. cc. 01. ss. $$. ee. ff. ff. gg. gg. gg. gg.
          hh. hh. hh. 11. 11. 11. jj. F7
```

pp : packet number
 cc : channel number
 ss ss : sample number
 ee : sample format (8 to 28 bits; SY99 handles samples of 16 bits or less)
 ff ff ff : sample period
 gg gg gg : sample length
 hh hh hh : loop start
 ii ii ii : loop end
 jj : loop type
 kk : running packet count (0-127) (number of current packet)
 11 : checksum (XOR of 7E cc 02 kk <120 bytes>)

SY99 Sample Bulk Dump

```
0 11110000 F0
1 01000011 43
2 0000nnnn nnnn - Device Number
3 01111010 7A
4 0bbbbbbb ] byte count
5 0bbbbbbb
6 01001100 4C(ascii"L")
7 01001101 4D(ascii"M")
8 00100000 20(ascii" ")
9 00100000 20(ascii" ")
10 00110000 30(ascii"0")
11 00110000 30(ascii"0")
12 00110100 34(ascii"4")
13 00110000 30(ascii"0")
14 01010011 53(ascii"S")
15 01000001 41(ascii"A")
16 00000000 00
↓ ↓
30 00000000 00
31 0mmmmmmm - Memory number
32 0ddddddd - data
↓ ↓
0s:ss:sss - check_sum
11110111 F7
```

Dump request

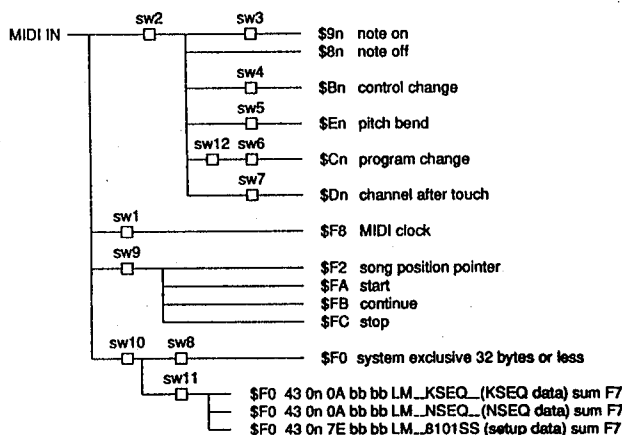
```
0 11110000 F0
1 01000011 43
2 0010nnnn nnnn - Device Number
3 01111010 7A
4 01001100 4C(ascii"L")
5 01001101 4D(ascii"M")
6 00100000 20(ascii" ")
7 00100000 20(ascii" ")
8 00110000 30(ascii"0")
9 00110000 30(ascii"0")
10 00110100 34(ascii"4")
11 00110000 30(ascii"0")
12 01010011 53(ascii"S")
13 01000001 41(ascii"A")
14 00000000 00
↓ ↓
28 00000000 00
29 0mmmmmmm - Memory number
30 11110111 F7
```

* Refer to attached Table 9-2 for details regarding bulk dump data format.

5. Sequencer mode

5.1 MIDI reception/transmission block diagram

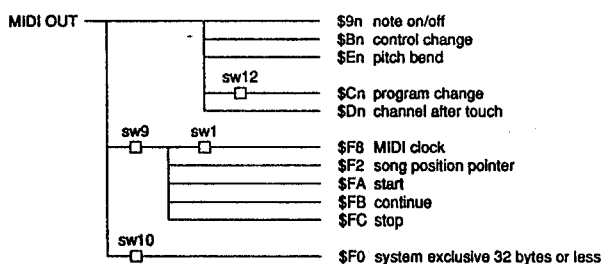
(Receive flow chart)



(Note)

- sw1 on when SEQ SETUP sync is set to MIDI
- sw2 during RECORD, the SEQ SETUP reception channel
- sw3 during RECORD, the SEQ SETUP velocity on/off
- sw4 during RECORD, the SEQ SETUP control change on/off
- sw5 during RECORD, the SEQ SETUP pitch bend on/off
- sw6 during RECORD, the SEQ SETUP program change on/off
- sw7 during RECORD, the SEQ SETUP channel pressure on/off
- sw8 during RECORD, the SEQ SETUP system exclusive (of 32 bytes or less) on/off
- sw9 on/off in SEQ SETUP for song position pointer, start, continue, stop
- sw10 set by the utility Device Number
- sw11 received only when utility Bulk Memory Protect = off
- sw12 received only when utility Program Change Mode is not off

(Transmit flow chart)



(Note)

- sw1 on when SEQ SETUP sync is set to INTERNAL
- sw9 on/off in SEQ SETUP for MIDI clock song position pointer, start, continue, stop
- sw10 set by the utility Device Number
- sw12 received only when utility Program Change Mode is not off

5.2 Channel messages

Received only during RECORD. Transmitted only during PLAY and overdubbing.
For transmission and reception conditions, refer to the Receive flow chart and Transmit flow chart.

5.3 Mode messages

Neither received nor transmitted.

5.4 System common messages

Receive only \$F2 and \$F7. All others are neither received nor transmitted.

5.5 System realtime messages

5.5.1 Status F8, FA, FB, FC

Receive.

5.5.2 Status F9, FD, FF

After decoding, do nothing.

6. Status FE (active sensing)

a) Transmission

Transmit FE at intervals of approximately 170 msec.

b) Reception

Once FE has been received, if no MIDI data arrives for an interval longer than approximately 300 msec, the MIDI receive buffer is cleared, and if any keys remain on, they will be turned off.

< Table 1-1 >

MIDI Parameter Change table (Multi mode Common Data)

\$F0, \$43, \$1n, \$34, \$00, \$00, \$00, N2, \$00, V2, \$F7

Note) n ; Device Number
V2 ; parameter value

[header section]

	N2	data name	data range	note
0	00	MNAM 0	ascii	Multi Voice Set Name
1	01	MNAM 1	ascii	.
2	02	MNAM 2	ascii	.
3	03	MNAM 3	ascii	.
4	04	MNAM 4	ascii	.
5	05	MNAM 5	ascii	.
6	06	MNAM 6	ascii	.
7	07	MNAM 7	ascii	.
8	08	MNAM 8	ascii	.
9	09	MNAM 9	ascii	.
10	0A	MNAM10	ascii	.
11	0B	MNAM11	ascii	.
12	0C	MNAM12	ascii	.
13	0D	MNAM13	ascii	.
14	0E	MNAM14	ascii	.
15	0F	MNAM15	ascii	.
16	10	MNAM16	ascii	.
17	11	MNAM17	ascii	.
18	12	MNAM18	ascii	.
19	13	MNAM19	ascii	.

< Table 1-2 >

MIDI Parameter Change table (Multi mode Channel Data)

\$F0, \$43, \$1n, \$34, \$01, T2, \$00, N2, \$00, V2, \$F7

Note) n ; Device Number
T2 ; voice channel number
V2 ; parameter value

[channel data]

	N2	data name	data range	note
0	00	OFVCSW	b6 ; off/on	Off_voice_switch
		INDV	b5~2 ; off, 1~8	Individual Output select
		OUT0CH	b0 ; off/on	Output 1 select
		OUT1CH	b1 ; off/on	Output 2 select
				Note: INDV0 #8104 only valid.
1	01	VMEM	int, p1, p2, crd	Voice Memory Select
2	02	VNUM	0~63	Voice number
3	03	VOL	0~127	Volume
4	04	MTUN	-64~+63 (o/b)	Tuning
5	05	MNSF	-64~+63 (o/b)	Note shift
6	06	STPAN	voice, -31~+31 (o/b)	Performance static PAN Note: Voice PAN setting not valid if a setting other than "voice" is selected.
7	07	EFSDMD	voice/multi	effect send mode
8	08	EFLN1CH	b0 ; off/on	effect send line 1 select
		EFLN2CH	b1 ; off/on	effect send line 2 select
		EFLN3CH	b2 ; off/on	effect send line 3 select
		EFLN4CH	b3 ; off/on	effect send line 4 select
9	09	EFSDLV	0~127	effect send level

Note) (o/b) ; offset binary

< Table 1-3 >

MIDI Parameter Change table (Voice data common data)

\$F0, \$43, \$1n, \$34, \$02, \$00, \$00, N2, \$00, V2, \$F7

Note) n ; channel number
V2 ; parameter value

[header data]

	N2	data name	data range	note
0	00	ELMODE	0: 1AFM_mono 1: 2AFM_mono 2: 4AFM_mono 3: 1AFM_poly 4: 2AFM_poly 5: 1AWM_poly 6: 2AWM_poly 7: 4AWM_poly 8: 1AFM_1AWM_poly 9: 2FM_2PGM_poly 10: DRUM_SET	—Element Select Mode—
1	01	VNAM0	ascii	—Voice Name— * * * * * * * * * *
2	02	VNAM1	ascii	
3	03	VNAM2	ascii	
4	04	VNAM3	ascii	
5	05	VNAM4	ascii	
6	06	VNAM5	ascii	
7	07	VNAM6	ascii	
8	08	VNAM7	ascii	
9	09	VNAM8	ascii	
10	0A	VNAM9	ascii	

[Controllers]

	N2	data name	data range	note
11	28	WPBR	0~12	—Pitch Bend— Wheel Pitch Bend Range
12	29	ATPBR	-12~+12 (s/m)	
13	2A	PMASN	0~121	—Pitch Modulation— Device Assign (MIDI control #)
14	2B	PMRNG	0~127	
15	2C	AMASN	0~121	—Amplitude Modulation— Device Assign (MIDI control #)
16	2D	AMRNG	0~127	
17	2E	FMASN	0~121	—Filter Modulation— Device Assign (MIDI control #)
18	2F	FMRNG	0~127	
19	30	PNLASN	0~121	—PAN Modulation— Note) Valid only when Multi is selected Device Assign (MIDI control #)
20	31	PNLRNG	0~127	
21	32	COASN	0~121	—Filter Cut_off Bias— Device Assign (MIDI control #)
22	33	CORNG	0~127	
23	34	PNBASN	0~121	—PAN Bias— Note) Valid only when Multi is selected Device Assign (MIDI control #)
24	35	PNBRNG	0~127	
25	36	EGBASN	0~121	—EG Bias— Device Assign (MIDI control #)
26	37	EGBRNG	0~127	
27	38	VVLASN	0~121	—Voice Volume— Device Assign (MIDI control #)
28	39	VVLLML	0~127	

Note) For the above Device Assigns, 121 will select After Touch.

[Only for Normal]

	N2	data name	data range	note
29	3A	MCTUN	0~65	Micro Tuning table select
30	3B	RNDP	0~7	Random Pitch fluctuation
31	3C	FORM	0, 1	—Portamento— Note: Only valid in FM_Element only Voice modes.
32	3D	POS	0~127	
33	3E	reserve		
34	3F	VVOL	0~127	Voice Volume
35	40	reserve		
36	41	reserve		
37	42	AFTMD	all, top, btm, hi, low	zoned after touch mode zoned after touch split point
38	43	SPTPNT	0~127	

Note) (s/m) ; sign magnitude

< Table 1-4 >

MIDI Parameter Change table (Normal Voice Element Data)

\$F0, \$43, \$1n, \$34, \$03, T2, \$00, N2, \$00, V2, \$F7

Note) n ; Device Number
V2 ; parameter value
T2 ; 0, e1, e0, 0, 0, 0, 0

e1	e0	
0	0	Element 1
0	1	Element 2
1	0	Element 3
1	1	Element 4

	N2	data name	data range	note
0	00	ELVL	0~127	Element Level Element Detune Element Note Shift
1	01	ELDT	-7~+7 (s/m)	
2	02	ELNS	-64~+63 (o/b)	
3	03	ENLL	0~127 (note #)	—Element Note Limit— Low Limit High Limit
4	04	ENLH	0~127 (note #)	
5	05	EVLL	0~127 (note #)	—Element Velocity Limit— Low Limit High Limit
6	06	EVLH	0~127 (note #)	
7	07	PANNM	0~31	PAN data set table select Note: Only valid when selected for Multi
8	08	MCTEN OUT0SEL OUT1SEL	b0; off/on b1; off/on b2; off/on	Micro Tuning switch Output 1 select Output 2 select
9	09	EFLN1EL EFLN2EL EFLN3EL EFLN4EL	b0; off/on b1; off/on b2; off/on b3; off/on	effect send line 1 select effect send line 2 select effect send line 3 select effect send line 4 select effect send level effect send velocity sense effect send level scale
10	0A	EFSDLV	0~127	
11	0B	EFSDVC	-7~+7 (s/m)	
12	0C	EFSDSC	-7~+7 (s/m)	

Note) (s/m) ; sign magnitude
(o/b) ; offset binary

< Table 1-5 >

MIDI Parameter Change table (Drum_Set)

\$F0, \$43, \$1n, \$34, \$04, T2, \$00, N2, V1, V2, \$F7

Note) n ; channel number
T2 ; MIDI note number
N2 ; parameter number
V1 ; MSB of parameter value (for parameters other than WAV*, V1 will be \$00)
V2 ; LSB of parameter value

	N2	data name	data range	note
0	00	ALTGRP OUT1 OUT0	b6; 0~1 b1; 0~1 b0; 0~1	Alternate group Output 2 select Output 1 select
1	01	WSRC	0;Pre1 1;Card 2;— 3;Int 4;Pre2	Wave Source
2	02	WAV	0~max. 255	Wave Number (V1;MSB V2;LS7bits)
3	03	WWL	0~127	Wave Volume
4	04	WTN	-64~+63 (o/b)	Wave Tuning
5	05	WNS	-48~+36 (o/b)	Wave Note Shift
6	06	WPN	-31~+31 (o/b)	Static PAN
7	07	EFLN1C_1 EFLN2C_1 EFLN3C_1 EFLN4C_1	b0; off/on b1; off/on b2; off/on b3; off/on	effect send line 1 select effect send line 2 select effect send line 3 select effect send line 4 select
8	08	EFSDLVC_1	0~127	effect send level
9	09	EFSDVLC_1	-7~+7 (s/m)	effect send velocity sense

Note) (o/b) ; offset binary

< Table 1-6 >

MIDI Parameter Change table (AFM Element Common)
 \$F0, \$43, \$1n, \$34, \$05, T2, \$00, N2, \$00, V2, \$F7

Note) n ; Device Number
 N2 ; parameter number
 V2 ; parameter value

T2	
\$00	Element 1
\$20	Element 2
\$40	Element 3
\$60	Element 4

	N2	data name	data range	note
0	00	ALGNUM	0~44	(127 is free algorithm number. Algorithm only via MIDI)
1	01	FPR1	0~63	—Pitch EG— key_on Rate 1
2	02	FPR2	0~63	key_on Rate 2
3	03	FPR3	0~63	key_on Rate 3
4	04	FPRR1	0~63	key_off Rate 1
5	05	FPL0	-64~+63 (o/b)	key_on Level 0
6	06	FPL1	-64~+63 (o/b)	key_on Level 1
7	07	FPL2	-64~+63 (o/b)	key_on Level 2
8	08	FPL3	-64~+63 (o/b)	key_on Level 3
9	09	FPR1	-64~+63 (o/b)	key_off Level 1
10	0A	FPEGR	0~3	Range (0: 8oct, 1: 2oct, 2: 1oct, 3: 1/2oct)
11	0B	FPRS	0~7	Rate Scaling
12	0C	FVPSW	off/on	Velocity Switch
13	0D	FLFSPD	0~99	—Main LFO— Speed
14	0E	FLFDLY	0~99	Delay time
15	0F	FLFPMD	0~127	Pitch Modulation Depth
16	10	FLFFMD	0~127	Amplitude Modulation Depth
17	11	FLFFWD	0~127	Filter Modulation Depth
18	12	FLFWAV	0~5	Wave
19	13	FLINTP	0~99	Initial Phase
21	15	SLFWD	0~3	—Sub LFO— Wave
22	16	SLFS	0~127	Speed
23	17	SLFDM	delay/decay	delay mode/decay mode
24	18	SLFDT	0~99	Delay time/decay time
25	19	SLPMD	0~127.	Pitch Modulation Depth

< Table 1-7 >

MIDI Parameter Change table (AFM Element)

\$F0, \$43, \$1n, \$34, T1, T2, \$00, N2, V1, V2, \$F7

Note) n ; Device Number
 N2 ; parameter number
 V1 ; MSB of parameter value
 V2 ; LSB of parameter value

T1		T2	
\$06	OP6	\$00	Element 1
\$16	OP5	\$20	Element 2
\$26	OP4	\$40	Element 3
\$36	OP3	\$60	Element 4
\$46	OP2		
\$56	OP1		

	N2	data name	data range	note
0	00	R1	0~63	EG key_on Rate 1
1	01	R2	0~63	EG key_on Rate 2
2	02	R3	0~63	EG key_on Rate 3
3	03	R4	0~63	EG key_on Rate 4
4	04	RR1	0~63	EG key_off Rate 1
5	05	RR2	0~63	EG key_off Rate 2
6	06	L1	0~63	EG key_on Level 1
7	07	L2	0~63	EG key_on Level 2
8	08	L3	0~63	EG key_on Level 3
9	09	L4	0~63	EG key_on Level 4
10	0A	RL1	0~63	EG key_off Level 1
11	0B	RL2	0~63	EG key_off Level 2
12	0C	SLP	0~3	EG Sustain Loop Point
13	0D	HT	0~63 (disp: 63~0)	EG key_on/Hold Time
14	0E	L0	0~63	EG key_on Level 0
15	0F	RS	-7~+7 (s/m)	EG Rate Scaling
16	10	FAMS	0~7	Amplitude Modulation Sens.
17	11	VSON	-7~+7 (s/m)	Velocity Sensitivity
18	12	reserve		
19	13	ALGSR0	V2 b3~0 ; 0~10	oscillator input 0 Source
20	14	ALGSR1	V1 b0, V2 b6~4 ; 0~10	oscillator input 1 Source
		ALGDST	V2 b1, 0 ; 0~3	oscillator output Destination
		OASCR0	V2 b3, 2 ; 0~2	Out_Accumulator input 0 Source
		OASCR1	V2 b4 ; 0~1	Out_Accumulator input 1 Source
21	15	SHIFT0	V2 b5~3 ; 0~7	oscillator input 0 Shift value
		SHIFT1	V2 b2~0 ; 0~7	oscillator input 1 Shift value
22	16	OCR	0~7	output level Correction
23	17	FWAVE	0~15	Waveform of oscillator
24	18	FMLPMS	V2 b4~2 ; 0~7	M_LFO Pitch Modulation Sens.
		FPM	V2 b1 ; 0~1	Pitch EG Switch
		FPV	V2 b0 ; 0~1	frequency Mode
25	19	KOE	V1 b0 ; 0~1	initial phase set Enable
		PHASE	V2 b6~0 ; 0~127	initial Phase of oscillator
26	1A	FPD	-15~+15 (s/m)	Pitch Detune

	N2	data name	data range	note
27	1B	TL	0~127	out_Level
28	1C	BP1	0~127 (note #)	out_Level scaling Break Point
29	1D	BP2	0~127 (note #)	out_Level scaling Break Point
30	1E	BP3	0~127 (note #)	out_Level scaling Break Point
31	1F	BP4	0~127 (note #)	out_Level scaling Break Point
32	20	EGOS1	-128~+127 (2bytes)	out_Level offset (BP1)
33	21	EGOS2	-128~+127 (2bytes)	out_Level offset (BP2)
34	22	EGOS3	-128~+127 (2bytes)	out_Level offset (BP3)
35	23	EGOS4	-128~+127 (2bytes)	out_Level offset (BP4)
36	24	RVSW	off/on	Rate Velocity Switch
37	25	FPC		frequency Course
38	26	FPF		frequency Fine

< Table 1-8 >

MIDI Parameter Change table (AFM Element)

\$F0, \$43, \$1n, #34, \$07, T2, \$00, N2, V1, V2, \$F7

Note) n ; Device Number
 N2 ; parameter number
 V1 ; MSB of parameter value
 V2 ; LSB of parameter value
 T2 ; table at right

T2	
\$00	Element 1
\$20	Element 2
\$40	Element 3
\$60	Element 4

[AWM generator unit]

	N2	data name	data range	note
0	00	WSOURCE	0: Pre1, 1; Card, 2; AFM, 3; Int, 4; Pre2	AWM Wave Source
1	01	AWMWAVE	V1; MSB1bit V2; LS7bits	AWM Wave number
2	02	PPM	normal/fixd	frequency Mode
3	03	PNOTE	0~127	fixed mode note #
4	04	PPF	-64~+63	frequency Fine
5	05	PMLPMS	0~7	pitch modulation sensitivity
6	06	PPR1	0~63	—Pitch EG— key_on Rate 1
7	07	PPR2	0~63	key_on Rate 2
8	08	PPR3	0~63	key_on Rate 3
9	09	PPR1	0~63	key_off Rate 1
10	0A	PPL0	-64 ~+63 (o/b)	key_on Level 0
11	0B	PPL1	-64 ~+63 (o/b)	key_on Level 1
12	0C	PPL2	-64 ~+63 (o/b)	key_on Level 2
13	0D	PPL3	-64 ~+63 (o/b)	key_on Level 3
14	0E	PPRL1	-64 ~+63 (o/b)	key_off Level 1
15	0F	PPEGR	1~3	Range (1: 2oct, 2: 1oct, 3: 1/2oct)
16	10	PPRS	-7~+7	Rate Scaling
17	11	PVPSW	off/on	Velocity Switch
18	12	PLFSPD	0~99	—Pitch LFO— Speed
19	13	PLFDLY	0~99	Delay time
20	14	PLFPMD	0~127	Pitch Modulation Depth
21	15	PLFFMD	0~127	Amplitude Modulation Depth
22	16	PLFFWD	0~127	Filter Modulation Depth
23	17	PLFWAV	0~5	Wave
24	18	PLINTP	0~99	Initial Phase
25	19	reserve		

[Amplitude EG data]

	N2	data name	data range	note
25	4F	PAEGMD	normal/hold	EG mode
26	50	PAR1	0~63	key_on Rate 1 (attack/hold)
27	51	PAR2	0~63	key_on Rate 2 (decay)
28	52	PAR3	0~63	key_on Rate 3
29	53	PAR4	0~63	key_on Rate 4 (decay)
30	54	PARR1	0~63	key_off Rate 1 (release)
31	55	PAL2	0~63	key_on Level 2 (decay)
32	56	PAL3	0~63	key_on Level 3 (decay)
33	57	PARS	-7~+7	rate scaling
34	58	PABP1	0~127 (note #)	out_Level scaling Break Point
35	59	PABP2	0~127 (note #)	out_Level scaling Break Point
36	5A	PABP3	0~127 (note #)	out_Level scaling Break Point
37	5B	PABP4	0~127 (note #)	out_Level scaling Break Point
38	5C	PAOS21	-128~+127 (2bytes)	out_Level scaling offset
39	5D	PAOS22	-128~+127 (2bytes)	out_Level scaling offset
40	5E	PAOS23	-128~+127 (2bytes)	out_Level scaling offset
41	5F	PAOS24	-128~+127 (2bytes)	out_Level scaling offset
42	60	PAVSON	-7~+7 (s/m)	Velocity Sensitivity
43	61	PARVSW	off/on	Attack Rate Velocity Switch
44	62	PAMS	-7~+7 (s/m)	amplitude modulation sens.

< Table 1-9 >

MIDI Parameter Change table (Waveform)

\$F0, \$43, \$1n, \$34, \$0E, \$T2, Q5, N2, 00, V2, \$F7

Note) n ; Device Number
 T2 ; Waveform number
 N2 ; parameter number
 V2 ; parameter value

	N2	data name	data range	note
0	00	WVNAME0	ascii	---Waveform Name--- * * * * * * *
1	01	WVNAME1	ascii	
2	02	WVNAME2	ascii	
3	03	WVNAME3	ascii	
4	04	WVNAME4	ascii	
5	05	WVNAME5	ascii	
6	06	WVNAME6	ascii	
7	07	WVNAME7	ascii	
8	08	WVFROM	---, 1-99	Sample From
9	09	WVTO	---, 1-99	

< Table 1-10 >

MIDI Parameter Change table (Effect Data)

\$F0, \$43, \$1n, \$34, \$08, \$00, \$00, N2, \$00, V2, \$F7

Note) n ; Device Number
 V2 ; parameter value

	N2	data name	data range	note
32	20	EFMODE	0-2	---Effect--- mode select (off, seri, palla) effect 1 type effect 1 parameter 1 effect 1 parameter 2 effect 1 parameter 3 effect 1 parameter 4 effect 1 parameter 5 effect 1 parameter 6 effect 1 parameter 7 effect 1 parameter 8 effect 1 parameter 9 effect 1 parameter 10 effect 1 output level 1 effect 1 output level 2 effect 2 type effect 2 parameter 1 effect 2 parameter 2 effect 2 parameter 3 effect 2 parameter 4 effect 2 parameter 5 effect 2 parameter 6 effect 2 parameter 7 effect 2 parameter 8 effect 2 parameter 9 effect 2 parameter 10 effect 2 mix level effect 2 output level 1 effect 2 output level 2 output 1 effect balance wet:dry output 2 effect balance wet:dry controller 1 parameter select controller 1 device assign controller 1 MIN controller 1 MAX controller 2 parameter select controller 2 device assign controller 2 MIN controller 2 MAX effect LFO wave select effect LFO speed effect LFO delay time effect LFO initial phase
33	21	EF1TYPE	0-60	
34	22	EF1PRM1		
36	24	EF1PRM2		
38	26	EF1PRM3		
40	28	EF1PRM4		
42	2A	EF1PRM5		
44	2C	EF1PRM6		
46	2E	EF1PRM7		
48	30	EF1PRM8		
50	32	EF1PRM9		
52	34	EF1PRM10		
54	36	EF1OUTLV1	0-100	
55	37	EF1OUTLV2	0-100	
56	38	EF2TYPE	0-60	
57	39	EF2PRM1		
59	3B	EF2PRM2		
61	3D	EF2PRM3		
63	3F	EF2PRM4		
65	41	EF2PRM5		
67	43	EF2PRM6		
69	45	EF2PRM7		
71	47	EF2PRM8		
73	49	EF2PRM9		
75	4B	EF2PRM10		
77	4D	EF2EFBAL1	0-100	
78	4E	EF2OUTLV1	0-100	
79	4F	EF2OUTLV2	0-100	
80	50	OUT1EFBAL	0-100	
81	51	OUT2EFBAL	0-100	
82	52	CTRL1PRM	0-32	
83	53	CTRL1ASN	0-120, AT, VL, SC, LFO	
84	54	CTRL1MIN	0-99	
85	55	CTRL1MAX	0-99	
86	56	CTRL2PRM	0-32	
87	57	CTRL2ASN	0-120, AT, VL, SC, LFO	
88	58	CTRL2MIN	0-99	
89	59	CTRL2MAX	0-99	
90	5A	EFLFOWV	tri, dwn, up, squ, sin, S/H	
91	5B	EFLFOSP	0-99	
92	5C	EFLFODL	0-99	
93	5D	EFLFOPH	0-99, free	

< Table 1-11 >

MIDI Parameter Change table (Filter Data)

\$F0, \$43, \$1n, \$34, \$09, T2, \$00, N2, V1, V2, \$F7

Note) n ; Device Number
 N2 ; parameter number
 V1 ; MSB of parameter value
 V2 ; LSB of parameter value
 T2 ; O, E, E, O, O, N, N, N
 (E, N table at right)

E		N	
00	Element 1	000	AFM filt. 1
01	Element 2	001	AFM filt. 2
10	Element 3	011	AMF common
11	Element 4	100	AWM filt. 1
		101	AWM filt. 2
		101	AWM common

[filter 1 & 2]

	N2	data name	data range	note
0	00	FTYPE	thru/LPF/(HPF)	filter type
1	01	FCTOF	0-127	cut_off frequency
2	02	FMODE	EG, LFO, EG-VA	filter mode
3	03	FR1	0-63	key_on Rate 1
4	04	FR2	0-63	key_on Rate 2
5	05	FR3	0-63	key_on Rate 3
6	06	FR4	0-63	key_on Rate 4
7	07	FRR1	0-63	key_off Rate 1
8	08	FRR2	0-63	key_off Rate 2
9	09	FLO	-64~+63 (o/b)	key_on cut_off Level 0
10	0A	FL1	-64~+63 (o/b)	key_on cut_off Level 1
11	0B	FL2	-64~+63 (o/b)	key_on cut_off Level 2
12	0C	FL3	-64~+63 (o/b)	key_off cut_off Level 3
13	0D	FL4	-64~+63 (o/b)	key_off cut_off Level 4
14	0E	FRL1	-64~+63 (o/b)	key_on cut_off Level 1
15	0F	FRL2	-64~+63 (o/b)	key_on cut_off Level 2
16	10	FRS	-7~+7	rate scaling
17	11	FBP1	0-127 (note #)	c_off_lv1 scaling Break Point
18	12	FBP2	0-127 (note #)	c_off_lv1 scaling Break Point
19	13	FBP3	0-127 (note #)	c_off_lv1 scaling Break Point
20	14	FBP4	0-127 (note #)	c_off_lv1 scaling Break Point
21	15	FOS1	-128~+127 (2bytes)	c_off_lv1 scaling offset
22	16	FOS2	-128~+127 (2bytes)	c_off_lv1 scaling offset
23	17	FOS3	-128~+127 (2bytes)	c_off_lv1 scaling offset
24	18	FOS4	-128~+127 (2bytes)	c_off_lv1 scaling offset

[filter common]

	N2	data name	data range	note
25	32	FRES	0-99	resonance
26	33	FVSON	-7~+7 (s/m)	Velocity Sensitivity
27	34	FCMS	-7~+7 (s/m)	Cut_off modulation sensitivity

< Table 1-12 >

MIDI Parameter Change table (Pan Data)

\$F0, \$43, \$1n, \$34, \$0A, T2, \$00, N2, \$00, V2, \$F7

Note) n ; Device Number
 T2 ; Memory number
 N2 ; parameter number
 V2 ; parameter value

	N2	data name	data range	note
0	00	PNSCSEL	velocity, note #, LFO	PAN source select
1	01	PNSCDPT	0-99	
2	02	PNDT	0-63	---EG--- key_on/Hold Time key_on Rate 1 key_on Rate 2 key_on Rate 3 key_on Rate 4 key_on Rate 4 key_off Rate 1 key_off Rate 2 key_off Rate 2 key_on Level 0 key_on Level 1 key_on Level 2 key_on Level 3 key_on Level 4 key_off Level 1 key_off Level 2 repeat segment
3	03	PNR1	0-63	
4	04	PNR2	0-63	
5	05	PNR3	0-63	
6	06	PNR4	0-63	
7	07	PNRR1	0-63	
8	08	PNRR2	0-63	
9	09	PNL0	-32~+31 (o/b)	
10	0A	PNL1	-32~+31 (o/b)	
11	0B	PNL2	-32~+31 (o/b)	
12	0C	PNL3	-32~+31 (o/b)	
13	0D	PNL4	-32~+31 (o/b)	
14	0E	PNRL1	-32~+31 (o/b)	
15	0F	PNRL2	-32~+31 (o/b)	
16	10	PNSLP	0-3	
17	11	PNNAM0	ascii	
18	12	PNNAM1	ascii	
19	13	PNNAM2	ascii	
20	14	PNNAM3	ascii	
21	15	PNNAM4	ascii	
22	16	PNNAM5	ascii	
23	17	PNNAM6	ascii	
24	18	PNNAM7	ascii	
25	19	PNNAM8	ascii	
26	1A	PNNAM9	ascii	

Note) (o/b) ; offset binary (invert the sign_bit of the 2's complement)

< Table 1-13 >

MIDI Parameter Change table (Micro Tuning Data)
\$F0, \$43, \$1n, \$34, \$0B, T2, N1, N2, V1, V2, \$F7

Note) n ; Device Number
V1 ; MS7bits of parameter value
T2 ; memory number
V2 ; LS7bits of parameter value

Table with columns: N1, N2, data name, data range, note. Lists parameters from MCTC_-2 to MCTC_6 and MCTA# through MCTB# through MCTG# through MCTD# through MCTE# through MCTF# through MCTG#.

Table with columns: N1, N2, data name, data range, note. Lists parameters from MCTC#6 to MCTG_8 and MTNAM0 to MTNAM9. Note for MTNAM0-9 includes a 'Micro Tuning Name' diagram.

< Table 1-14 >

MIDI Parameter Change table (Switch Remote)

\$F0, \$43, \$1n, \$34, \$0D, \$00, \$00, N2, \$00, V2, \$F7

Note) n ; Device Number
N2 ; parameter number
V2 ; parameter value data range : off (\$00-\$3F), on (\$40-\$7F)

Table with columns: N2, sw.#, note. Lists parameters from PSW 1 to PSW 47 with corresponding voice and function notes.

< Table 1-15 >

MIDI Parameter Change table (Master control)

\$F0, \$43, \$1n, \$34, \$0E, T2, N1, N2, V1, V2, \$F7

Note) n ; channel number
 T2 ; control number
 N1 ; parameter type
 N2 ; parameter number
 V1 ; MS7bits of parameter value
 V2 ; LS7bits of parameter value

[Filter section] T2=00

	N1	N2	data name	data range	note
0	00	00	FILPGMMS	0-b'11111111	Program Change Filter ch 16-9
1	00	01	FILPGMLS	0-b'11111111	0;off 1;on ch 8-1
2	00	02	FILCONMS	0-b'11111111	Control Change Filter ch 16-9
3	00	03	FILCONLS	0-b'11111111	0;off 1;onch 8-1
4	00	04	FILPBNMS	0-b'11111111	Pitch Bend Filter ch 16-9
5	00	05	FILPBNLS	0-b'11111111	0;off 1;onch 8-1
6	00	06	FILSUSMS	0-b'11111111	Sustain Pedal Filter ch 16-9
7	00	07	FILSUSLS	0-b'11111111	0;off 1;onch 8-1
8	00	08	FILATCHMS	0-b'11111111	After Touch Filter ch 16-9
9	00	09	FILATCHLS	0-b'11111111	0;off 1;onch 8-1
10	00	0A	FILVOLMS	0-b'11111111	Main Volume Filter ch 16-9
11	00	0B	FILMVOLLS	0-b'11111111	0;off 1;onch 8-1

[controller element section]

T2=0, 0, n, n, 0, t, t, t

n	
00	MIDI-1
01	MIDI-2
10	MIDI-3
11	MIDI-4

t	
000	Control 1
001	Control 2
010	Control 3
011	Control 4
100	Control 5
101	Control 6
110	Control 7
111	Control 8

	N1	N2	data name	data range	note
0	02	00	CONENABL	0-B'00011111	Control Enable 0;off 1;on Bit 0 = midi off/on 1 = pc off/on 2 = Ms/Ls off/on 3 = volume off/on 4 = MDR off/on
1	02	01	MIDITCH	0-15	MIDI Transmit Channel
2	02	02	NLIML	0-127	Note Limit Low
3	02	03	NLIMH	0-127	Note Limit High
4	02	04	VLIML	1-127	Velocity Limit Low
5	02	05	VLIMH	1-127	Velocity Limit High
6	02	06	XPOSE	0-127	Transpose [center 64 (40h)]
7	02	07	VELCURV	0-3	Velocity Curve
8	02	08	AFTCURV	0-3	After Touch Curve
9	02	09	MAINVOL	0-127	MAIN Volume
10	02	0A	BANK	0-16383	Bank Select
11	02	0C	PCNUM	0-127	Program Change Number
12	02	0D	MDRNUM	0-99	MDR Number

[Control name section]

T2=0, 0, 0, 0, 0, t, t, t

t	
000	Control 1
001	Control 2
010	Control 3
011	Control 4
100	Control 5
101	Control 6
110	Control 7
111	Control 8

	N1	N2	data name	data range	note
0	03	00	CNAM0	ascii	---Control Name---
1	03	01	CNAM1	ascii	..
2	03	02	CNAM2	ascii	..
3	03	03	CNAM3	ascii	..
4	03	04	CNAM4	ascii	..
5	03	05	CNAM5	ascii	..
6	03	06	CNAM6	ascii	..
7	03	07	CNAM7	ascii	..
8	03	08	CNAM8	ascii	..
9	03	09	CNAM9	ascii	..

[Control voice section]

T2=0, 0, 0, 0, 0, t, t, t

t	
000	Control 1
001	Control 2
010	Control 3
011	Control 4
100	Control 5
101	Control 6
110	Control 7
111	Control 8

	N1	N2	data name	data range	note
0	04	00	PTM	off/on	Program
		01	VM	voice/multi	Program Mode
		02	VMEM	I/C/P1/P2	Voice Memory
		03	VNUM	0-63	Voice Number
		04	MNUM	I/C/P	Multi Memory
		05	MNUM	0-15	Multi Number

< Table 1-16 >

MIDI Parameter Change table (System Setup)

\$F0, \$43, \$1n, \$34, \$0F, \$00, \$00, N2, \$00, V2, \$F7

Note) n ; channel number
 N2 ; parameter number
 V2 ; parameter value

Note) Exception ; Master_Tuning (at transmit) \$F0 \$43, \$1N, \$04, DT, \$F7 (DX1 Master Tuning is used)

	N2	data name	data range	note
0	00	GRTMSU 0	ascii	---Greeting Message---
1	01	GRTMSU 1	ascii	Upper
2	02	GRTMSU 2	ascii	Upper
3	03	GRTMSU 3	ascii	Upper
4	04	GRTMSU 4	ascii	Upper
5	05	GRTMSU 5	ascii	Upper
6	06	GRTMSU 6	ascii	Upper
7	07	GRTMSU 7	ascii	Upper
8	08	GRTMSU 8	ascii	Upper
9	09	GRTMSU 9	ascii	Upper
10	0A	GRTMSU10	ascii	Upper
11	0B	GRTMSU11	ascii	Upper
12	0C	GRTMSU12	ascii	Upper
13	0D	GRTMSU13	ascii	Upper
14	0E	GRTMSU14	ascii	Upper
15	0F	GRTMSU15	ascii	Upper
16	10	GRTMSU16	ascii	Upper
17	11	GRTMSU17	ascii	Upper
18	12	GRTMSU18	ascii	Upper
19	13	GRTMSU19	ascii	Upper
20	14	GRTMSL 0	ascii	Lower
21	15	GRTMSL 1	ascii	Lower
22	16	GRTMSL 2	ascii	Lower
23	17	GRTMSL 3	ascii	Lower
24	18	GRTMSL 4	ascii	Lower
25	19	GRTMSL 5	ascii	Lower
26	1A	GRTMSL 6	ascii	Lower
27	1B	GRTMSL 7	ascii	Lower
28	1C	GRTMSL 8	ascii	Lower
29	1D	GRTMSL 9	ascii	Lower
30	1E	GRTMSL10	ascii	Lower
31	1F	GRTMSL11	ascii	Lower
32	20	GRTMSL12	ascii	Lower
33	21	GRTMSL13	ascii	Lower
34	22	GRTMSL14	ascii	Lower
35	23	GRTMSL15	ascii	Lower
36	24	GRTMSL16	ascii	Lower
37	25	GRTMSL17	ascii	Lower
38	26	GRTMSL18	ascii	Lower
39	27	GRTMSL19	ascii	Lower
40	28	MNSFT	-64-63 (o/b)	Master Note Shift
41	29	MTUNE	-64-63 (o/b)	Master Fine Tuning (receive only)
42	2A	FIXVEL	off, 1-127	---Velocity---
43	2B	VELCRV	0-7	Fixed velocity Velocity Curve select
44	2C	MW2MCN	0-120	---MIDI Control Number Assign---
45	2D	FSASN	0-120	Modulation Wheel 2 Foot Switch assign
46	2E	EDCONFWS	off/on	Edit Confirm switch
47	2F	TXCH	0-15	---MIDI---
48	30	VRCH	0-15, omni	keyboard transmit channel
49	31	LOCAL	off/on	Voice Receive channel Local switch
50	32	DVCNUM	off, 0-15, all	Device number
51	33	NTSW	all/odd/even	Note_Even_Odd switch
52	34	BLKMPRT	off/on	Bulk data Memory protect switch
53	35	PGCMOD	off/on	Program Change mode
54	36	reserve		
55	37	PATPRT	off/on	Bulk Data Pattern Protect Switch

	N2	data name	data range	note
56	38	CONTHOLD	off/on	Controller Hold
57	39	MEMALOC	0-127	memory allocate for MDR (x4Kbyte)
58	3A	reserve		
59	3B	reserve		
60	3C	reserve		
61	3D	reserve		
62	3E	reserve		
63	3F	reserve		
	40	EBYP	0-2	Effect Bypass Sw (param cng only) 0:off 1:on Normal 2:on OUT-1 Direct OUT-2 Effect

Note) (o/b) ; offset binary (invert the sign_bit of the 2's complement)

< Table 1-17 >

MIDI Parameter Change table (Sample)

\$F0, \$43, \$1n, \$34, \$0E, \$T2, 06, N2, V1, V2, \$F7

Note) n ; Device Number
T2 ; Sample number
N2 ; parameter number
V1 ; MS7bits of parameter value
V2 ; LS7bits of parameter value

	N2	data name	data range	note
0	00	HIKEY	1-127 (note #)	High key
1	02	ORIKEY	0-127 (note #)	Original key
2	04	PITCH	-5376--+5376 (2bytes, o/b)	Pitch
3	08	LOOPMODE	b2 : 0 forward 1 backward b1 : 0 loop off 1 loop on b0 : 0 normal 1 alternate	Loop type, Mode
4	12	VOL	0-127	Volume
5	1C	LOWKEY	1-127 (note #)	Low key
6	1E	SANAM0	ascii	---Sample Name--- *
7	1F	SANAM1	ascii	
8	20	SANAM2	ascii	
9	21	SANAM3	ascii	
10	22	SANAM4	ascii	
11	23	SANAM5	ascii	
12	24	SANAM6	ascii	
13	25	SANAM7	ascii	

< Table 2-1 >

Voice Bulk Dump

Note) Memory_type internal ; \$00
preset 1 ; \$02
preset 2 ; \$03
Edit Buffer ; \$7F (Used only when transmitting from SY99. Memory# is transmitted as \$00, ignored when receiving.)

When receiving Bulk dump, if Memory_type is other than \$7F, this is processed as Internal.

Note) Memory# \$00-\$0F ; Bank A, 1-16
\$10-\$1F ; Bank B, 1-16
\$20-\$2F ; Bank C, 1-16
\$30-\$3F ; Bank D, 1-16

Note) When receiving Bulk dump, bit 6 of Memory# is ignored.

(1) IAFM

	data	Element1 data	Element1 data
0	\$F0	Refer to 00-26 of Table 1-7.	Refer to 00-19 of Table 1-6.
1	\$43	OP6_R1	ALGNUM
2	\$0N	reserve	
3	\$7A	OP6_ALGSRC (MSB)	
4] byte count	OP6_ALGSRC (low 7bits)	Refer to 00-18 of Table 1-11.
5		OP6_ALGDST	FL1_FTYPE
6	M	OP6_FMPMS	FL1_FBP4
7] \$00	OP6_KOE	FL1_FOS1 (MSB)
8		OP6_PHASE	FL1_FOS1 (low 7bits)
9] Memory#	OP6_FPD	FL1_FOS2 (MSB)
10		OP6_BP4	FL1_FOS2 (low 7bits)
11] \$00	OP6_EGOS1 (MSB)	FL1_FOS3 (MSB)
12		OP6_EGOS1 (low 7bits)	FL1_FOS3 (low 7bits)
13] \$00	OP6_EGOS2 (MSB)	FL1_FOS4 (MSB)
14		OP6_EGOS2 (low 7bits)	FL1_FOS4 (low 7bits)
15] \$00	OP6_EGOS3 (MSB)	Refer to 00-18 of Table 1-11.
16		OP6_EGOS3 (low 7bits)	FL2_FTYPE
17] \$00	OP6_EGOS4 (MSB)	
18		OP6_EGOS4 (low 7bits)	FL2_FOS4 (MSB)
19] \$00	OP6_RVSW	FL2_FOS4 (low 7bits)
20		OP6_FPC	
21] \$00	OP6_PPF	Refer to 32-34 of Table 1-11.
22			FFRES
23] \$00		FFVSON
24			FFCMS
25] \$00		check_sum
26			\$F7
27] \$00		
28			
29] \$00		
30			
31] \$00		
32			
33] \$00		
34			
35] \$00		
36			
37] \$00		
38			
39] \$00		
40			
41] \$00		
42			
43] \$00		
44			
45] \$00		
46			
47] \$00		
48			
49] \$00		
50			
51] \$00		
52			
53] \$00		
54			
55] \$00		
56			
57] \$00		
58			
59] \$00		
60			
61] \$00		
62			
63] \$00		
64			
65] \$00		
66			
67] \$00		
68			
69] \$00		
70			
71] \$00		
72			
73] \$00		
74			
75] \$00		
76			
77] \$00		
78			
79] \$00		
80			
81] \$00		
82			
83] \$00		
84			
85] \$00		
86			
87] \$00		
88			
89] \$00		
90			
91] \$00		
92			
93] \$00		
94			
95] \$00		
96			
97] \$00		
98			
99] \$00		
100			

(2) 2AFM

data		Element1 data	
0	\$F0	116	Same as (1) AFM 107-463
1	\$43	↓	
2	\$0N	↓	
3	\$7A	472	
4] byte count	Element2 data	
5		L	
6	M	473	Same as (1) AFM 107-463
7	-	↓	
8	-	↓	
9	8	829	
10	1		
11	0		
12	1	830	check_sum
13	1	831	\$F7
14	V		
15	C		
16] \$00		
17		↓	
29	Memory_type		
30	Memory#		
31			
32	\$01 or \$04 (ELMODE)		
33] Same as (1) AFM 33-97		
↓			
97			
98	Refer to 00-08 of Table 1-4. ELVLO		
↓	↓		
106	MCTENO		
107	Refer to 00-08 of Table 1-4. ELVL1		
↓	↓		
115	MCTEN1		

(4) 1AWM

data		Element1 data	
0	\$F0		Refer to 00-19 of Table 1-8.
1	\$43	107	
2	\$0N	108	
3	\$7A	109	WSOURCE
4] byte count	110	PCMWAVE (MSB)
5		L	↓
6	M	↓	PPM
7	-		
8	-	134	Same as (1) AFM 403-463
9	8	↓	
10	8	↓	
11	1	194	
12	0		
13	1		
14	V		
15	C		
16] \$00		
17		↓	
29	Memory_type		
30	Memory#		
31			
32	\$05 (ELMODE)		
33] Same as (1) AFM 33-97		
↓			
97			
98	Refer to 00-08 of Table 1-4. ELVLO		
↓	↓		
106	MCTENO		
		195	Refer to 4F-62 of Table 1-8. PAEGMD
		↓	↓
		207	PABP4
		208	PAOS21 (MSB)
		209	PAOS21 (low 7bits)
		210	PAOS22 (MSB)
		211	PAOS22 (low 7bits)
		212	PAOS23 (MSB)
		213	PAOS23 (low 7bits)
		214	PAOS24 (MSB)
		215	PAOS24 (low 7bits)
		216	PAVSON
		217	PARVSW
		218	PAMS
		219	check_sum
		220	\$F7

(3) 4AFM

Note) Memory# ; \$30-\$3F
When receiving, cancel if Memory# is other than the above.

data		Element1 data	
0	\$F0	134	Same as (1) AFM 107-463
1	\$43	↓	
2	\$0N	↓	
3	\$7A	490	
4] byte count	Element2 data	
5		L	
6	M	491	Same as (1) AFM 107-463
7	-	↓	
8	-	↓	
9	8	847	
10	1		
11	0		
12	1		
13	V		
14	C		
15] \$00		
16		↓	
29	Memory_type	848	Same as (1) AFM 107-463
30	Memory#	↓	
31		↓	
		1204	
32	\$02 (ELMODE)		
33] Same as (1) AFM 33-97		
↓			
97			
98	Refer to 00-08 of Table 1-4. ELVLO	1205	Same as (1) AFM 107-463
↓	↓	↓	
106	MCTENO	1561	
107	Refer to 00-08 of Table 1-4. ELVL1		
↓	↓		
115	MCTEN1	1562	check_sum
		1563	\$F7
116	Refer to 00-08 of Table 1-4. ELVL2		
↓	↓		
124	MCTEN2		
125	Refer to 00-08 of Table 1-4. ELVL3		
↓	↓		
133	MCTEN3		

(5) 2AWM

data		Element1 data	
0	\$F0	116	Same as (1) 1AWM 107-218
1	\$43	↓	
2	\$0N	↓	
3	\$7A	227	
4] byte count	Element2 data	
5		L	
6	M	228	Same as (1) 1AWM 107-218
7	-	↓	
8	-	↓	
9	8	339	
10	1		
11	0		
12	1		
13	V		
14	C		
15] \$00		
16		↓	
29	Memory_type		
30	Memory#		
31			
32	\$06 (ELMODE)		
33] Same as (1) AFM 33-97		
↓			
97			
98	Refer to 00-08 of Table 1-4. ELVLO		
↓	↓		
106	MCTENO		
107	Refer to 00-08 of Table 1-4. ELVL1		
↓	↓		
115	MCTEN1		

(6) 4AWM

data		Element1 data	
0	\$F0	134	Same as (1) 1AWM 107-218
1	\$43	↓	
2	\$0N	245	
3	\$7A		Element2 data
4] byte count		
5		L	
6	M	246	Same as (1) 1AWM 107-218
7	-	↓	
8	-	357	
9	0		Element3 data
10	1		
11	0		
12	1		
13	V	358	Same as (1) 1AWM 107-218
14	C	↓	
15	-	469	
16] \$00		Element4 data
17		Memory_type	
18	Memory#		
32	\$07 (ELMODE)	470	Same as (1) 1AWM 107-218
33] Same as (1) AFM 33-97	↓	
97		581	
	Refer to 00-08 of Table 1-4.	582	check_sum \$F7
98	ELVLD	583	
106	MCTEN0		
	Refer to 00-08 of Table 1-4.		
107	ELVL1		
115	MCTEN1		
	Refer to 00-08 of Table 1-4.		
116	ELVL2		
124	MCTEN2		
	Refer to 00-08 of Table 1-4.		
125	ELVL3		
133	MCTEN3		

(8) 2AFM_2AWM

Note) Memory# ; \$30-\$3F
When receiving, cancel if Memory# is other than the above.

data		Element1 data	
0	\$F0	134	Same as (1) AFM 107-463
1	\$43	↓	
2	\$0N	490	
3	\$7A		Element2 data
4] byte count		
5		L	
6	M	491	Same as (1) AFM 107-463
7	-	↓	
8	-	847	
9	0		Element3 data
10	1		
11	0		
12	1		
13	V	848	Same as (1) 1AWM 107-218
14	C	↓	
15	-	959	
16] \$00		Element4 data
17		Memory_type	
18	Memory#		
32	\$09 (ELMODE)	960	Same as (1) 1AWM 107-218
33] Same as (1) AFM 33-97	↓	
97		1071	
	Refer to 00-08 of Table 1-4.	1072	check_sum \$F7
98	ELVLD	1073	
106	MCTEN0		
	Refer to 00-08 of Table 1-4.		
107	ELVL1		
115	MCTEN1		
	Refer to 00-08 of Table 1-4.		
116	ELVL2		
124	MCTEN2		
	Refer to 00-08 of Table 1-4.		
125	ELVL3		
133	MCTEN3		

(7) 1AFM_1AWM

data		Element1 data	
0	\$F0	116	Same as (1) AFM 107-463
1	\$43	↓	
2	\$0N	472	
3	\$7A		Element2 data
4] byte count		
5		L	
6	M	473	Same as (4) 1AWM 107-218
7	-	↓	
8	-	584	
9	0		Element3 data
10	1		
11	0		
12	1		
13	V	585	check_sum \$F7
14	C	586	
15] \$00		
16		Memory_type	
17	Memory#		
32	\$08 (ELMODE)		
33] Same as (1) AFM 33-97		
97			
	Refer to 00-08 of Table 1-4.		
98	ELVLD		
106	MCTEN0		
	Refer to 00-08 of Table 1-4.		
107	ELVL1		
115	MCTEN1		

(9) Drum_set

data		Drum_set data		Drum_set data	
0	\$F0	98	ALTC_1, OUT*C_1	194	ALTC_2, OUT*C_2
1	\$43	99	WSRCC_1	↓	↓
2	\$0N	100	WAVC_1 (MSB)	↓	↓
3	\$7A	101	WAVC_1 (low 7bits)	↓	↓
4] byte count	102	WVLC_1	290	ALTC_3, OUT*C_3
5		L	103	WTNC_1	↓
6	M	104	WNSC_1	↓	↓
7	-	105	WPNC_1	↓	↓
8	-	106	ALTC#1, OUT*C#1	386	ALTC_4, OUT*C_4
9	0	↓	↓	↓	↓
10	1	↓	↓	↓	↓
11	0	114	ALTD_1, OUT*D_1	↓	↓
12	1	↓	↓	482	ALTC_5, OUT*C_5
13	V	↓	↓	↓	↓
14	C	122	ALTD#1, OUT*D1	↓	↓
15	-	↓	↓	578	ALTC_6, OUT*C_6
16] \$00	↓	↓	↓	↓
17		Memory_type	130	ALTE_1, OUT*E_1	585
18	Memory#	↓	↓	586	check_sum \$F7
32	\$0A (ELMODE)	138	ALTF_1, OUT*F_1	587	
33] Same as (1) AFM 33-71	↓	↓		
71			146	ALTF#1, OUT*F#1	
] \$00 (ctrl)	↓	↓		
72			154	ALTG_1, OUT*G_1	
77	WVLSN	↓	↓		
78	WVLLML	162	ALTG#1, OUT*G#1		
88] \$00 (norm)	↓	↓		
89			170	ALTA_1, OUT*A_1	
90] \$00 (norm)	↓	↓		
95			178	ALTA#1, OUT*A#1	
96	AWMID high 7	↓	↓		
97	AWMID low 7	186	ALTB_1, OUT*B_1		
		↓	↓		

(10) dump request

	data
0	\$F0
1	\$43
2	\$2N
3	\$7A
4	byte count
5	L
6	M
7	-
8	-
9	0
10	0
11	0
12	4
13	0
14	V
15	C
16	↓
29	\$00
30	Memory_type
31	Memory#
32	\$F7

* Memory type = not \$7F (edit buffer)

< Table 2-2 >

Additional Voice Bulk Dump

Note) Memory_type internal ; \$00
 preset 1 ; \$02
 preset 2 ; \$03
 Edit Buffer ; \$7F (Used only when transmitting from SY99. Memory# is transmitted as \$00, ignored when receiving.)

For WSOURCE, AWMWAV, data which has the same name exists in the Bulk Dump which is the AWM Element data shown in table 1-8, but when Voice Bulk and Additional Voice Bulk are transmitted together, Additional bulk WSOURCE, AWMWAV becomes effective.

When receiving Bulk dump, if Memory_type is other than \$7F, this is processed as Internal.

Note) Memory# \$00-\$0F ; Bank A, 1~16
 \$10-\$1F ; Bank B, 1~16
 \$20-\$2F ; Bank C, 1~16
 \$30-\$3F ; Bank D, 1~16

Note) When receiving Bulk dump, bit 6 of Memory# is ignored.

(1) 1AFM & 1AWM

	data
0	\$F0
1	\$43
2	\$2N
3	\$7A
4	byte count
5	L
6	M
7	-
8	-
9	0
10	0
11	0
12	4
13	0
14	V
15	C
16	↓
29	\$00
30	Memory_type
31	Memory#
32	Refer to 00 of Table 1-3. \$00, \$03, \$05 (ELMODE)
33	Refer to 20~5D of Table 1-10. EFMODE ↓
95	AWMID high 7bit
96	AWMID low 7bit
97	AFTMD
98	SPTPNT
99	Refer to 09~12 of Table 1-4. EFLN1EL0
100	EFSDLV0
101	EFSDVL0
102	EFSDSC0
103	WSOURCE1
104	AWMWAV (MSB)
105	AWMWAV (low 7bit)
106	check_sum
107	\$F7

(2) 2AFM & 2AFM & 1AFM_1AWM

	data
0	\$F0
1	\$43
2	\$2N
3	\$7A
4	byte count
5	L
6	M
7	-
8	-
9	0
10	0
11	0
12	4
13	0
14	V
15	C
16	↓
29	\$00
30	Memory_type
31	Memory#
32	Refer to 00 of Table 1-3. \$01, \$04, \$06, \$08 (ELMODE)
33	Refer to 20~5D of Table 1-10. EFMODE ↓
95	AWMID high 7bit
96	AWMID low 7bit
97	AFTMD
98	SPTPNT
99	Refer to 09~12 of Table 1-4. EFLN1EL0
100	EFSDLV0
101	EFSDVL0
102	EFSDSC0
103	Refer to 09~12 of Table 1-4. EFLN1EL1
104	EFSDLV1
105	EFSDVL1
106	EFSDSC1

107	WSOURCE1
108	AWMWAV (MSB)
109	AWMWAV (low 7bit)
110	WSOURCE2
111	AWMWAV (MSB)
112	AWMWAV (low 7bit)
113	check_sum
114	\$F7

(3) 4AFM & 4AWM & 2AFM_2AWM

Note) Memory# ; \$30-\$3F
 When receiving, cancel if Memory# is other than the above.

	data
0	\$F0
1	\$43
2	\$2N
3	\$7A
4	byte count
5	L
6	M
7	-
8	-
9	0
10	0
11	0
12	4
13	0
14	V
15	C
16	↓
29	\$00
30	Memory_type
31	Memory#
32	Refer to 00 of Table 1-3. \$02, \$07, \$09 (ELMODE)
33	Refer to 20~5D of Table 1-10. EFMODE ↓
95	AWMID high 7bit
96	AWMID low 7bit
97	AFTMD
98	SPTPNT
99	Refer to 09~12 of Table 1-4. EFLN1EL0
100	EFSDLV0
101	EFSDVL0
102	EFSDSC0
103	Refer to 09~12 of Table 1-4. EFLN1EL1
104	EFSDLV1
105	EFSDVL1
106	EFSDSC1

107	Refer to 09~12 of Table 1-4. EFLN1EL2
108	EFSDLV2
109	EFSDVL2
110	EFSDSC2
111	Refer to 09~12 of Table 1-4. EFLN1EL3
112	EFSDLV3
113	EFSDVL3
114	EFSDSC3
115	WSOURCE1
116	AWMWAV (MSB)
117	AWMWAV (low 7bit)
118	WSOURCE2
119	AWMWAV (MSB)
120	AWMWAV (low 7bit)
121	WSOURCE3
122	AWMWAV (MSB)
123	AWMWAV (low 7bit)
124	WSOURCE4
125	AWMWAV (MSB)
126	AWMWAV (low 7bit)
127	check_sum
128	\$F7

(4) Drum_set

data	
0	\$F0
1	\$43
2	\$0N
3	\$7A
4	byte count
5	L
6	M
7	-
8	0
9	0
10	4
11	0
12	V
13	C
14	↓
15	\$00
16	↓
29	Memory_type
30	Memory#
31	↓
32	Refer to 00 of Table 1-3. \$0A (ELMODE)
33	↓
33	Refer to 20-5D of Table 1-10. EFMODE
85	AWMID high 7bit
96	AWMID low 7bit
97	AFTMD
98	SPTPNT
99	Refer to 07-09 of Table 1-5. WSOURCEC_1
100	AWMWAV (MSB)
101	AWMWAV (low 7bit)
102	EFLN1C_1
103	EFSDVLC_1
104	↓
459	WSOURCEC_6
460	AWMWAV (MSB)
461	AWMWAV (low 7bit)
462	EFLN1C_6
463	EFSDVLC_6
464	↓

465	Refer to 07-09 of Table 1-5. ALTE_0
531	↓
531	ALTB_0
541	↓
541	EFSDVLB_0
542	ALTC#_6
619	↓
619	ALTG_6
629	↓
629	ALTG_6
630	WSOURCE_0
631	AWMWAV (MSB)
632	AWMWAV (low 7bit)
663	WSOURCEG_6
664	AWMWAV (MSB)
665	AWMWAV (low 7bit)
666	check_sum
667	\$F7

< Table 3-1 >

Multi Bulk Dump

Note) Memory_type internal ; \$00
 preset ; \$02
 Edit Buffer ; \$7F (Used only when transmitting
 from SY99. Memory# is transmitted
 as \$00, ignored when receiving.)

When receiving Bulk dump, if Memory_type is other than \$7F, this is
 processed as Internal.

Note) Memory# \$00-\$0F ; INT1~16

Note) When receiving Bulk dump, bits 6~4 of Memory# are ignored.

data		data		dump request	
0	\$F0	90	Refer to 00-06 of Table 1-2. OFVCSW_0, OUT*CH_0	0	\$F0
1	\$43	97	↓	1	\$43
2	\$0N	104	OFVCSW_1, OUT*CH_1	2	\$2N
3	\$7A	111	↓	3	\$7A
4	byte count	118	OFVCSW_2, OUT*CH_2	4	L
5	L	125	↓	5	M
6	M	132	OFVCSW_3, OUT*CH_3	6	-
7	-	139	↓	7	-
8	0	146	OFVCSW_4, OUT*CH_4	8	0
9	0	153	↓	9	0
10	4	160	OFVCSW_5, OUT*CH_5	10	4
11	0	167	↓	11	0
12	V	174	OFVCSW_6, OUT*CH_6	12	M
13	C	181	↓	13	U
14	↓	188	OFVCSW_7, OUT*CH_7	14	↓
15	\$00	195	↓	15	\$00
16	↓	201	OFVCSW_8, OUT*CH_8	27	↓
29	Memory_type	202	OFVCSW_9, OUT*CH_9	28	Memory type
30	Memory#	203	OFVCSW_10, OUT*CH_10	29	Memory#
31	↓	203	OFVCSW_11, OUT*CH_11	30	\$F7
32	Refer to 00-13 of Table 1-1. MNAM0	167	OFVCSW_12, OUT*CH_12		
51	↓	174	OFVCSW_13, OUT*CH_13		
51	MNAM19	181	OFVCSW_14, OUT*CH_14		
52	Refer to 00-13 of Table 1-10. EFMODE	188	OFVCSW_15, OUT*CH_15		
80	↓	195	OFVCSW_15, OUT*CH_15		
80	ST_MIX2	201	STPAN15		
		202	check_sum		
		203	\$F7		

* Memory type =
 not \$7F
 (Edit buffer)

(5) dump request

data	
0	\$F0
1	\$43
2	\$2N
3	\$7A
4	L
5	M
6	-
7	-
8	0
9	0
10	4
11	0
12	V
13	C
14	↓
14	\$00
27	↓
28	Memory type
29	Memory#
30	\$F7

* Memory type = not \$7F (edit buffer)

< Table 3-2 >

Additional Multi Bulk Dump

Note) Memory_type internal ; \$00
 preset ; \$02
 Edit Buffer ; \$7F (Used only when transmitting
 from SY99. Memory# is transmitted
 as \$00, ignored when receiving.)

When receiving Bulk dump, if Memory_type is other than \$7F, this is
 processed as Internal.

Note) Memory # \$00-\$0F ; INT1~16

Note) When receiving Bulk dump, bits 6~4 of Memory# are ignored.

data		data		dump request	
0	\$F0	95	Refer to 07-09 of Table 1-2. EFSDMD0	0	\$F0
1	\$43	98	↓	1	\$43
2	\$0N	101	EFSDMD1	2	\$2N
3	\$7A	104	↓	3	\$7A
4	byte count	107	EFSDMD2	4	L
5	L	110	↓	5	M
6	M	113	EFSDMD3	6	-
7	-	116	↓	7	-
8	0	119	EFSDMD4	8	0
9	0	122	↓	9	0
10	4	125	EFSDMD5	10	4
11	0	128	↓	11	0
12	V	131	EFSDMD6	12	M
13	C	134	↓	13	U
14	↓	137	EFSDMD7	14	↓
15	\$00	140	↓	15	\$00
16	↓	143	EFSDMD8	27	↓
29	Memory_type	144	EFSDMD9	28	Memory type
30	Memory#	144	EFSDMD10	29	Memory#
31	↓	144	EFSDMD11	30	\$F7
32	Refer to 20-5D of Table 1-10. EFMODE	128	EFSDMD12		
94	↓	131	EFSDMD13		
94	EFLFOPH	134	EFSDMD14		
		137	EFSDMD15		
		140	EFSDMD15		
		143	check_sum		
		144	\$F7		

* Memory type =
 not \$7F
 (Edit buffer)

< Table 4 >

Pan Bulk Dump

Note) Memory_type internal ; \$00
 preset ; \$02

When receiving Bulk dump, processed as Internal regardless of the Memory_type.

Note) Memory# \$00~\$1F ; INT1~32

Note) When receiving Bulk dump, bits 6, 5 of Memory# are ignored.

		dump request	
data		data	
0	\$F0	0	\$F0
1	\$43	1	\$43
2	\$0N	2	\$2N
3	\$7A	3	\$7A
4	byte count	4	L
5	byte count	5	M
6	L	6	-
7	M	7	-
8	-	8	8
9	-	9	1
10	8	10	0
11	1	11	1
12	0	12	P
13	1	13	N
14	P	14	↓
15	N	15	↓
16	↓	16	↓
29	\$00	27	\$00
30	Memory_type	28	Memory type
31	Memory#	29	Memory#
		30	\$F7
32	Refer to 00~1A of Table 1-12.		
33	PNSCSEL		
58	↓		
	PNNAM9		
59	check_sum		
60	\$F7		

< Table 5 >

Micro Tuning Bulk Dump

Note) Memory# \$00~\$01 ; INT1~2

Note) When receiving Bulk dump, bits 6~1 of Memory# are ignored.

		dump request	
data		data	
0	\$F0	0	\$F0
1	\$43	1	\$43
2	\$0N	2	\$2N
3	\$7A	3	\$7A
4	byte count	4	L
5	byte count	5	M
6	L	6	-
7	M	7	-
8	-	8	8
9	-	9	1
10	8	10	0
11	1	11	1
12	0	12	M
13	1	13	T
14	M	14	↓
15	T	15	↓
16	↓	16	↓
29	\$00	27	\$00
30	\$00	28	\$00
31	Memory#	29	Memory#
		30	\$F7
32	Refer to 0000~017E of Table 1-13.		
33	MCTC_2 (high 7bits)		
286	↓		
	MCTG_8 (high 7bits)		
287	↓		
	MCTG_8 (low 7bits)		
288	Refer to 0200~0209 of Table 1-13.		
289	MTNAM_1		
297	↓		
	MTNAM10		
298	check_sum		
299	\$F7		

< Table 6 >

Master Control Bulk Dump

		dump request	
data		data	
0	\$F0	58	CONENA1_1
1	\$43	71	↓
2	\$0N	72	MDRNUM1_1
3	\$7A	72	CONENA2_1
4	byte count	85	↓
5	byte count	85	MDRNUM2_1
6	L	86	CONENA3_1
7	M	99	↓
8	-	99	MDRNUM3_1
9	-	100	CONENA4_1
10	0	113	↓
11	0		MDRNUM4_1
12	4	114	CONENA1_2
13	0	505	↓
14	M		MDRNUM4_8
15	S	506	CNAM0_1
16	↓	515	↓
31	\$00		CNAM9_1
		516	CNAM0_2
32	Refer to Table 1-15.	585	↓
33	FILPGMH (MSB)		CNAM9_8
34	FILPGMH (low 7 bits)	586	PGM_1
35	FILPGML (MSB)	591	↓
52	↓		MNUM_1
53	FILMGVLH (MSB)	592	PGM_2
54	FILMGVLH (low 7 bits)	633	↓
55	FILMGVLL (MSB)		MNUM_8
	FILMGVLL (low 7 bits)	634	check_sum
56	reserved	635	\$F7
57	reserved		

< Table 7 >

System Setup Bulk Dump

		dump request	
data		data	
0	\$F0	0	\$F0
1	\$43	1	\$43
2	\$0N	2	\$2N
3	\$7A	3	\$7A
4	byte count	4	L
5	byte count	5	M
6	L	6	-
7	M	7	-
8	-	8	8
9	-	9	1
10	8	10	0
11	1	11	1
12	0	12	S
13	1	13	Y
14	S	14	↓
15	Y	15	↓
16	↓	16	↓
31	\$00	29	\$00
		30	\$F7
32	Refer to 00~3F of Table 1-16.		
95	GRTMSU_0		
	↓		
	reserve		
96	check_sum		
97	\$F7		

< Table 8-1 >

Squencer Setup Bulk Dump

		dump request	
	data		data
0	\$F0	0	\$F0
1	\$43	1	\$43
2	\$0N	2	\$2N
3	\$7E	3	\$7E
4	byte count	4	L
5]	5	M
6	L	6	-
7	M	7	-
8	-	8	8
9	-	9	1
10	8	10	0
11	1	11	1
12	0	12	S
13	1	13	S
14	S	14	\$F7
15	S		
16	QUANTIZE		
17	CLICK SWITCH		
18	CLICK BEAT		
19	reserve		
20	SYNC MODE		
21	REC MIDI CHANNEL		
22	VELOCITY SW.		
23	CONTROL CHANGE SW.		
24	PITCH BEND SW.		
25	PROGRAM CHANGE SW.		
26	AFTER TOUCH SW.		
27	SY.EXCLUSIVE SW.		
28	MIDI CONTROL SW.		
29	EDIT BEAT/CLOCK		
30	ACCENT1		
31	ACCENT2		
32	ACCENT3		
33	ACCENT4		
34	GATE TYPE		
35	check_sum		
36	\$F7		

< Table 9-1 >

Waveform Data Bulk Dump

Note) FROM, TO When Memory_type of \$01 (card), \$02 or \$03 (pre1), or \$04 (pre2) is specified, FROM and TO are both set to a value of \$00.

	data
0	\$F0
1	\$43
2	\$0N
3	\$7A
4	byte count
5]
6	L
7	M
8	-
9	-
10	0
11	0
12	4
13	0
14	W
15	V
16]
↓	\$00
29	↓
30	Memory_type
31	Memory#
32	Refer to 0-9 of Table 1-9.
↓	WVNAME
↓	↓
39	WVNAME
40	WVNAME
41	WVTO
42	check_sum
43	\$F7

< Table 8-2 >

Squencer Song & Pattern (KSEQ, NSEQ) Bulk Dump

KSEQ and NSEQ data is converted from 1 byte to 2 byte ascii data and transmitted. The data for one song consists of more than one track of data, where each track begins with F0 0n, (n=track number) and ends with F2. Empty tracks will not be included.

hex	description
F0	top of record track #1
00	time/event/control data
F2	end of record track #1
00	track #2-#15 data
F0	top of record track #16
00	time/event/control data
F2	end of record track #16

< Table 9-2 >

Sample data Bulk Dump

	data
0	\$F0
1	\$43
2	\$0N
3	\$7A
4	byte count
5]
6	L
7	M
8	-
9	-
10	0
11	0
12	4
13	0
14	S
15	A
16]
↓	\$00
30	↓
31	Memory#
32	Refer to 6-13 of Table 1-17.
↓	SANAME
↓	↓
39	SANAME
40	HIKEYCODEH (MSB)
41	HIKEYCODEH (low 7bit)
42	HIKEYCODEL (MSB)
43	HIKEYCODEL (low 7bit)
44	ORKEY
45	PITCHCODEH (MSB)
46	PITCHCODEH (low 7bit)
47	PITCHCODEL (MSB)
48	PITCHCODEL (low 7bit)
49	LOOPMODE
50	VOLCODE (MSB)
51	VOLCODE (low 7bit)
52	LOWKEYCODEH (MSB)
53	LOWKEYCODEH (low 7bit)
54	LOWKEYCODEL (MSB)
55	LOWKEYCODEL (low 7bit)
56	check_sum
57	\$F7

Function ...	Transmitted	Recognized	Remarks
Basic Default	1 - 16	1 - 16	memorized
Channel Changed	1 - 16	1 - 16	
Mode Default	x	x	
Messages	x	x	
Altered	*****	x	
Note Number : True voice	0 - 127 *****	0 - 127	
Velocity Note ON	o 9nH,v=1-127	o v=1-127	
Note OFF	x 9nH,v=0	x	
After Key's	x	x	
Touch Ch's	o	o	
Pitch Bender	o	o	
Control Change	0 - 120 o	o	
Prog Change : True #	o 0 - 127 *****	o 0 - 127	
System Exclusive	o	o	Song data etc.
Common : Song Pos	o	o	
: Song Sel	o	o	
: Tune	x	x	
System :Clock	o	o	
Real Time :Commands	o	o	
Aux :Local ON/OFF	x	x	
:All Notes OFF	x	x	
Mes- :Active Sense	x	x	
sages:Reset	x	x	
Notes			

Mode 1 : OMNI ON, POLY Mode 2 : OMNI ON, MONO o : Yes
 Mode 3 : OMNI OFF, POLY Mode 4 : OMNI OFF, MONO x : No

Function ...	Transmitted	Recognized	Remarks
Basic Default	1 - 16	1 - 16	memorized
Channel Changed	1 - 16	1 - 16	
Mode Default	3	1,2,3,4	memorized
Messages	x	x	
Altered	*****	x	
Note Number : True voice	28 - 103 *****	0 - 127 1 - 127	
Velocity Note ON	o 9nH,v=1-127	o v=1-127	
Note OFF	x 9nH,v=0	x	
After Key's	x	x	
Touch Ch's	o	o	
Pitch Bender	o	o 0-12 semi	7 bit resolution
Control	0 o 1 o M.Wheel 2 o Breath cont. 4 o Foot cont. 6 o Data entry	o o o o	Bank select
Change	7 o Foot volume 32 o 64 o Sustain sw. 65 o Portamento sw 96 o inc. 97 o dec. 1 - 6 o Assignable 8 - 120 o Assignable	o o o o o o o	Volume Bank select Sustain Portamento
Prog Change : True #	o 0-79 *1 *****	o 0-79 *2	
System Exclusive	o	o	voice etc.
: Song Pos Common : Song Sel : Tune	See the sequencer part.		
System :Clock Real Time :Commands			
Aux :Local ON/OFF	x	x	
:All Notes OFF	x	x	
Mes- :Active Sense	o	o	
sages:Reset	x	x	
Note *1 ; Send PC,Master control:	0 - 127		
*2 ; voice :	0 - 63 , multi : 64 - 79		

Mode 1 : OMNI ON, POLY Mode 2 : OMNI ON, MONO o : Yes
Mode 3 : OMNI OFF, POLY Mode 4 : OMNI OFF, MONO x : No